



TA75062P/F

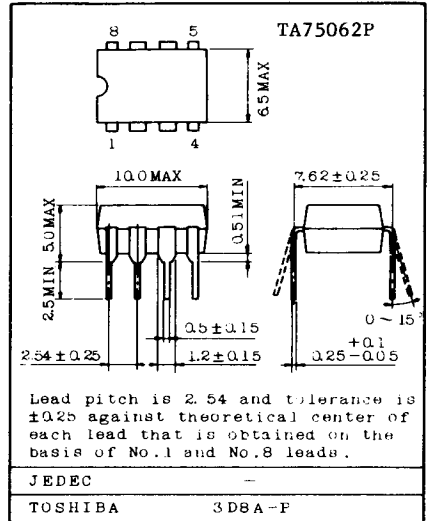
BIPOLAR LINEAR INTEGRATED CIRCUIT
SILICON MONOLITHIC

J-FET INPUT LOW-POWER
DUAL OPERATIONAL AMPLIFIER

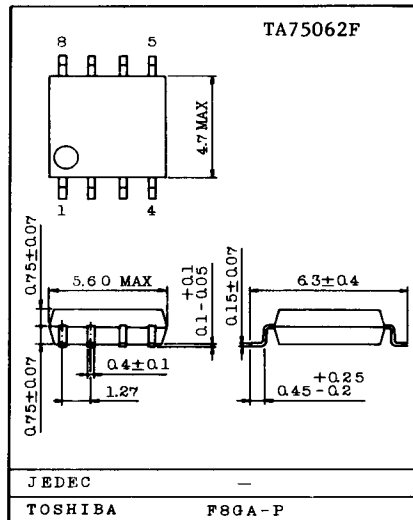
The TA75062P and TA75062F are J-FET input low-power operational amplifiers with low input bias, offset current and a fast slew rate. The TA75062P is pin compatible with the TA75458P and 1458. The TA75062F is mini-flat package. The TA75062P series are excellent choice for active filters, integrators, buffers and sample-and-hold circuits.

- . Low Supply Current : 500 μ A MAX.
- . High Input Impedance
- . Low Input Bias Current : 400pA MAX.
- . Low Input Offset Current : 200pA MAX.
- . High Slew Rate : 3.5V/ μ sec
- . Internal Frequency Compensation
- . Output Short Circuit Protection

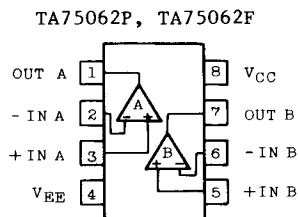
Unit in mm



Unit in mm



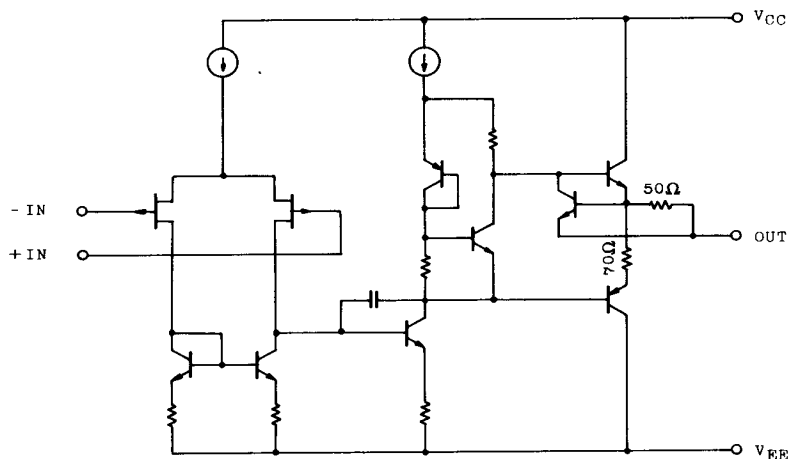
PIN CONNECTION (TOP VIEW)



MAXIMUM RATINGS (Ta=25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Supply Voltage		V _{CC}	+18	V
		V _{EE}	-18	
Differential Input Voltage		DV _{IN}	±30	V
Input Voltage		V _{IN}	±15	V
Power Dissipation	TA75062P	P _D	500	mW
	TA75062F		240	
Operating Temperature		T _{opr}	-40~85	°C
Storage Temperature		T _{stg}	-55~125	°C

EQUIVALENT CIRCUIT



TA75062P/S/F

ELECTRICAL CHARACTERISTICS ($V_{CC}=15V$, $V_{EE}=-15V$, $T_a=25^{\circ}C$)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V_{IO}	-	$R_g \leq 10k\Omega$	-	3	15	mV
TC of Input Offset Voltage	TCV_{IO}	-	-	-	10	-	$\mu V/^{\circ}C$
Input Bias Current	I_I	-	$T_j=25^{\circ}C$	-	30	400	pA
Input Offset Current	I_{IO}	-	$T_j=25^{\circ}C$	-	5	200	pA
Common Mode Input Voltage	CMV_{IN}	-		± 11.5	± 12	-	V
Maximum Output Voltage	V_{OM}		$R_L=10k\Omega$	± 10	± 13.5	-	V
Input Resistance	R_{IN}	-	-	-	10 ¹²	-	Ω
Voltage Gain (Open Loop)	G_v	-	$V_{OUT}=\pm 10V$, $R_L=10k\Omega$	3	6	-	V/mV
Common Mode Input Signal Rejection Ratio	$CMRR$	-	$R_g \leq 10k\Omega$	70	76	-	dB
Supply Voltage Rejection Ratio	$SVRR$	-	$R_g \leq 10k\Omega$	70	76	-	dB
Slew Rate	SR	-	$G_v=1$, $R_L=10k\Omega$	-	3.5	-	V/ μs
Unity Gain Cross Frequency	f_T	-	Open Loop	-	1	-	MHz
Supply Current	I_{CC}, I_{EE}	-	-	-	200	500	μA
Equivalent Input Noise Voltage	V_{NI}	-	$R_S=100\Omega$, $f=1kHz$	-	42	-	$nV/\sqrt{Hz}$