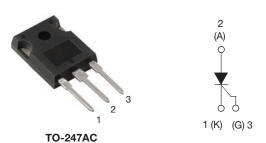


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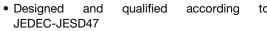
Vishay Semiconductors

Thyristor High Voltage, Phase Control SCR, 30 A



PRODUCT SUMMARY								
Package	TO-247AC							
Diode variation	Single SCR							
I _{T(AV)}	20 A							
V_{DRM}/V_{RRM}	800 V, 1200 V							
V_{TM}	1.3 V							
I _{GT}	45 mA							
T_J	- 40 °C to 125 °C							

FEATURES





Material categorization:
 For definitions of compliance please see www.vishay.com/doc?99912





APPLICATIONS

 Typical usage is in input rectification crowbar (soft start) and AC switch in motor control, UPS, welding and battery charge

DESCRIPTION

The VS-30TPS... high voltage series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

MAJOR RATINGS AND CHARACTERISTICS									
PARAMETER	TEST CONDITIONS	VALUES	UNITS						
I _{T(AV)}	Sinusoidal waveform	20	۸						
I _{RMS}		30	A						
V _{RRM} /V _{DRM}		800/1200	V						
I _{TSM}		300	А						
V _T	20 A, T _J = 25 °C	1.3	V						
dV/dt		500	V/µs						
dl/dt		150	A/µs						
T _J		- 40 to 125	°C						

VOLTAGE RATINGS									
PART NUMBER	V _{RRM} /V _{DRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA						
VS-30TPS08PbF, VS-30TPS08-M3	800	900	10						
VS-30TPS12PbF, VS-30TPS12-M3	1200	1300	10						



ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS			
Maximum average on-state current	I _{T(AV)}	T _C = 95 °C, 180° conduction	half sine wave	20				
Maximum RMS on-state current	I _{RMS}			30	۸			
Maximum peak, one-cycle		10 ms sine pulse, rated V _{RRN}	_M applied	250	Α			
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no voltage reapplied		300				
Marriage up 12t for friging	l ² t	10 ms sine pulse, rated V _{RRN}	_M applied	310	A ² s			
Maximum I ² t for fusing	1-1	10 ms sine pulse, no voltage reapplied		442	A-S			
Maximum I ² √t for fusing	I ² √t	t = 0.1 to 10 ms, no voltage r	4420	A²√s				
Maximum on-state voltage drop	V _{TM}	20 A, T _J = 25 °C			V			
On-state slope resistance	r _t			12	mΩ			
Threshold voltage	V _{T(TO)}	T _J = 125 °C		1.0	V			
Marian and discretization of the second	1 //	T _J = 25 °C	V Datad V A	0.5				
Maximum reverse and direct leakage current	I _{RM} /I _{DM}	T _J = 125 °C	$V_R = Rated V_{RRM}/V_{DRM}$	10	A			
Maximum holding current	I _H	Anode supply = 6 V, resistive load, initial I_T = 1 A, T_J = 25 °C		150	mA			
Maximum latching current	ΙL	Anode supply = 6 V, resistive load, T _J = 25 °C		200				
Maximum rate of rise of off-state voltage	dV/dt	$T_J = T_J$ maximum, linear to 8	80 % V _{DRM} , R _g -k = Open	500	V/µs			
Maximum rate of rise of turned-on current	dI/dt			150	A/μs			

TRIGGERING							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum peak gate power	P _{GM}		8.0	W			
Maximum average gate power	P _{G(AV)}		2.0	VV			
Maximum peak positive gate current	+ I _{GM}		1.5	Α			
Maximum peak negative gate voltage	- V _{GM}		10	V			
	I _{GT}	Anode supply = 6 V, resistive load, T _J = - 10 °C	60	mA			
Maximum required DC gate current to trigger		Anode supply = 6 V, resistive load, T _J = 25 °C	45				
		Anode supply = 6 V, resistive load, T _J = 125 °C	20				
		Anode supply = 6 V, resistive load, T _J = - 10 °C	2.5				
Maximum required DC gate voltage to trigger	V_{GT}	Anode supply = 6 V, resistive load, $T_J = 25 ^{\circ}\text{C}$	2.0	V			
voltage to trigger		Anode supply = 6 V, resistive load, T _J = 125 °C	1.0	V			
Maximum DC gate voltage not to trigger	V_{GD}	T 105 °C V Poted value	0.25				
Maximum DC gate current not to trigger	I _{GD}	T _J = 125 °C, V _{DRM} = Rated value	2.0	mA			

SWITCHING								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Typical turn-on time	t _{gt}	T _J = 25 °C	0.9					
Typical reverse recovery time	t _{rr}	T _{.1} = 125 °C	4	μs				
Typical turn-off time	t _q	1j = 125 C	110					

THERMAL AND MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 125	°C			
Maximum thermal resistance, junction to case		R_{thJC}	DC operation	0.8				
Maximum thermal resistance, junction to ambient		R _{thJA}	DC operation	40	°C/W			
Maximum thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.2				
Approximate weight				6	g			
Approximate weight				0.21	OZ.			
Mounting torque	minimum			6 (5)	kgf ⋅ cm			
Mounting torque -	maximum			12 (10)				
Marking daving			Coop at the TO 247AC (IEDEC)	30TF	PS08			
Marking device			Case style TO-247AC (JEDEC)	30TF	PS12			

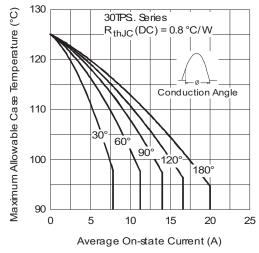


Fig. 1 - Current Rating Characteristics

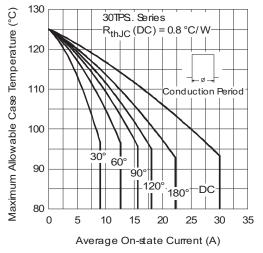


Fig. 2 - Current Rating Characteristics

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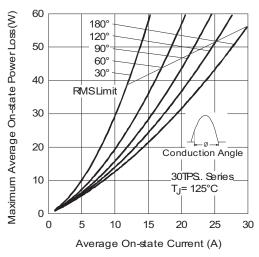


Fig. 3 - On-State Power Loss Characteristics

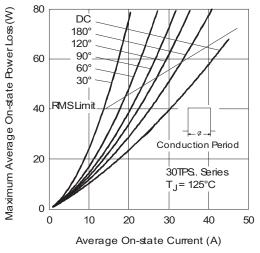
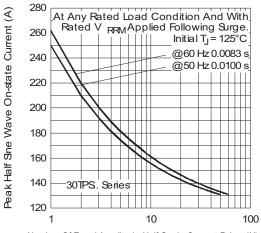


Fig. 4 - On-State Power Loss Characteristics



Number Of Equal Amplitude Half Cycle Current Pulses (N)

Fig. 5 - Maximum Non-Repetitive Surge Current

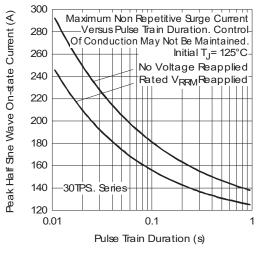


Fig. 6 - Maximum Non-Repetitive Surge Current

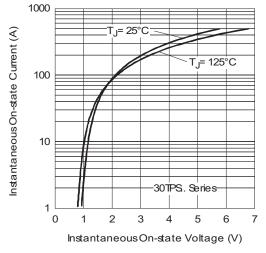


Fig. 7 - On-State Voltage Drop Characteristics

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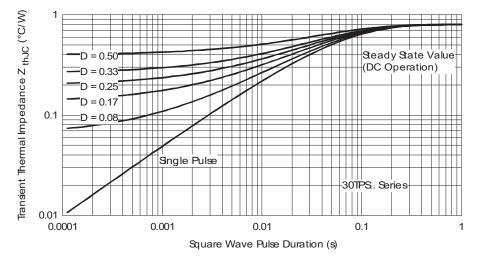


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

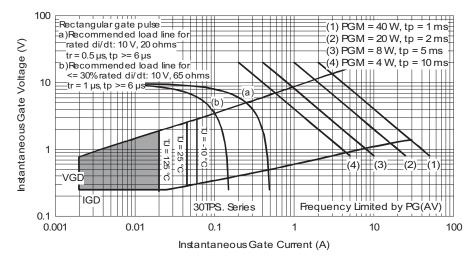
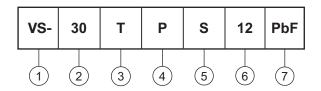


Fig. 9 - Gate Characteristics

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ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

Current rating (30 = 30 A)

Circuit configuration:

T = Thyristor

Package:

P = TO-247

5 Type of silicon:

S = Standard recovery rectifier

08 = 800 VVoltage code x $100 = V_{RRM}$

12 = 1200 V

Environmental digit:

PbF = Lead (Pb)-free and RoHS compliant

-M3 = Halogen-free, RoHS compliant, and terminations lead (Pb)-free

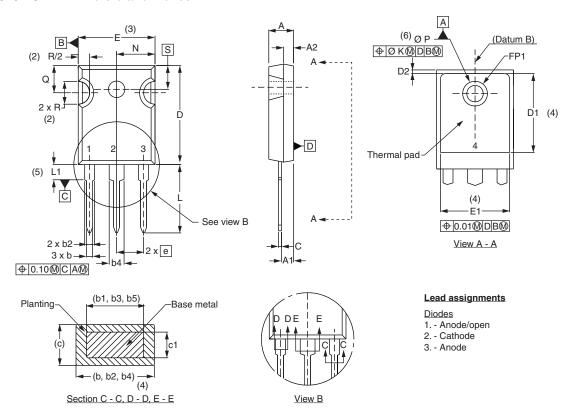
ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-30TPS08PbF	25	500	Antistatic plastic tubes						
VS-30TPS08-M3	25	500	Antistatic plastic tubes						
VS-30TPS12PbF	25	500	Antistatic plastic tubes						
VS-30TPS12-M3	25	500	Antistatic plastic tubes						

LINKS TO RELATED DOCUMENTS							
Dimensions		www.vishay.com/doc?95223					
Part marking information	TO-247AC PbF	www.vishay.com/doc?95226					
	TO-247AC -M3	www.vishay.com/doc?95007					



Vishay Semiconductors

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215	BSC	
FK	2.54		0.0	0.010	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62	BSC	0.3		
ΦР	3.56	3.66	0.14	0.144	
ФР1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51	BSC	0.217	BSC	

Notes

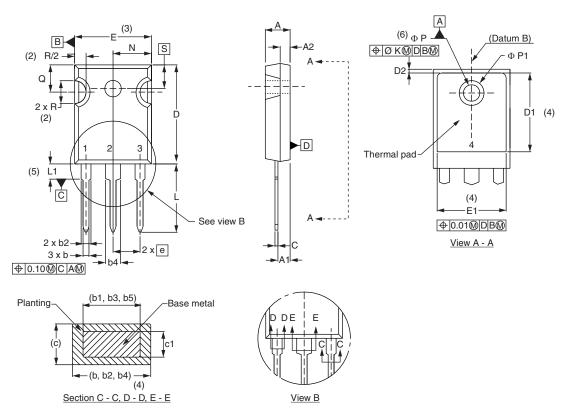
- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c



Vishay Semiconductors

TO-247

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ØK	0.2	254	0.0)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.33	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			N	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	7.39	-	0.291	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
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- $^{(7)}$ Outline conforms to JEDEC® outline TO-247 with exception of dimension c and Q



Legal Disclaimer Notice

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