

STW2040

High voltage fast-switching NPN power transistor

Features

- High voltage capability
- High DC current gain
- Minimum lot to lot spread for reliable operation

Application

■ Switching mode power supplies

Description

The STW2040 is manufactured using diffused collector in planar technology adopting base island layout.

The production of the producti

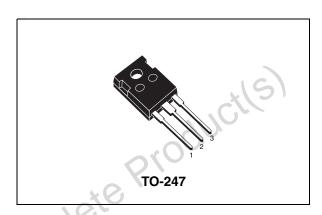


Figure 1. Internal schematic diagram

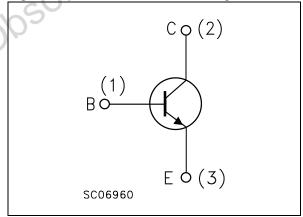


Table 1. Device summary

Order code	Marking	Package	Packaging
STW2040	W2040	TO-247	Tube

June 2009 Doc ID 15149 Rev 2 1/10

1 Absolute maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage (V _{CE} = 0)	700	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	500	V
V _{EBO}	Emitter-base voltage (I _C = 0)	9	٧
I _C	Collector current	20	Α
I _{CM}	Collector peak current	30	Α
I _B	Base current	7	Α
I _{BM}	Base peak current	10	Α
P _{TOT}	Total dissipation at T _c = 25 °C	125	W
T _{stg}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3. Thermal data

	Symbol	Parameter		Value	Unit	
	R_{thJC}	Thermal resistance junction-case	max	1	°C/W	
Obsole	ie Pr	oducités				

2 Electrical characteristics

 $(T_{case} = 25 \, ^{\circ}C; \text{ unless otherwise specified})$

Table 4. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current (V _{BE} = 0)	V _{CE} = 700 V			250	μА
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = 9 V			10	mA
V _{(BR)CEO}	Collector-emitter breakdown voltage (I _B = 0)	I _C = 10 mA	500			V
	Collector-emitter saturation voltage	$I_C = 6 \text{ A}$ $I_B = 1.2 \text{ A}$		0.2	0.5	V
V _{CE(sat)} ⁽¹⁾		$I_C = 12 \text{ A}$ $I_B = 2.4 \text{ A}$		0.3		V
		$I_C = 20 \text{ A}$ $I_B = 4 \text{ A}$		0.6		V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	$I_C = 6 \text{ A}$ $I_B = 1.2 \text{ A}$			1.2	V
VBE(sat)`		$I_C = 12 \text{ A}$ $I_B = 2.4 \text{ A}$	L		1.5	V
	DC current gain	$I_C = 10 \text{ mA}$ $V_{CE} = 5 \text{ V}$	/ 8			
h _{FE} ⁽¹⁾		$I_C = 6 A$ $V_{CE} = 5 V$	/ 15	21	27	
		$I_C = 12 A$ $V_{CE} = 5 V_{CE}$	/ 10			
	Resistive load	V _{CC} = 200 V				
t _{on}	Turn-on time	$V_{BE(off)} = -5 \text{ V } I_{C} = 7.5 \text{ A}$	4	140		ns
t _f	Fall time	$I_{B(on)} = 1.5 A$		100		ns
t _s	Storage time	$I_{B(off)} = -3 A$		1.6		μs
	Inductive load	V _{CL} = 250 V				
101	Storage time	$V_{BE(off)} = -5 \text{ V } I_{C} = 7.5 \text{ A}$	4	1.8		μs
t _c	Fall time	$I_{B(on)} = 1.5 A$		30		ns
	T all tillo	$I_{B(off)} = -3 A$		00		1.0

^{1.} Pulsed duration = 300 µs, duty cycle ≤ 1.5 %

STW2040 **Electrical characteristics**

 $V_{CE}(V)$

2.1 **Electrical characteristic (curves)**

Figure 2. Safe operating area

Ic(A) I_C MAX PULSED PULSE OPERATION * 10 Ic MAX CONT 0.1 For single non repetitive pulse 0.01

Figure 3. **Derating curve**

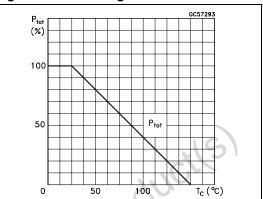
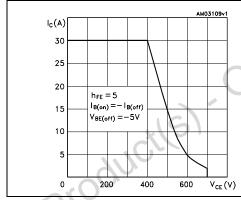


Figure 4. Reverse biased safe operating area

Figure 5. **Output characteristics**



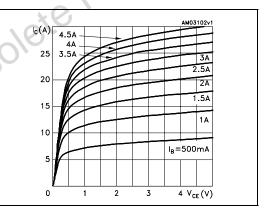
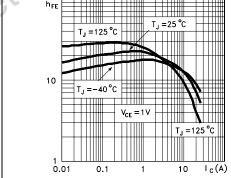
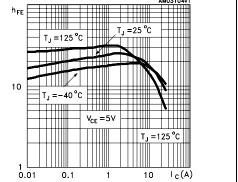


Figure 6. DC current gain (V_{CE} = 1 V)

Figure 7. DC current gain $(V_{CE} = 5 V)$ $T_J = 25$ °C $T_J = 25 \,^{\circ}C$





57 4/10 Doc ID 15149 Rev 2

Figure 8. Collector-emitter saturation Figure 9. Base-emitter saturation voltage voltage

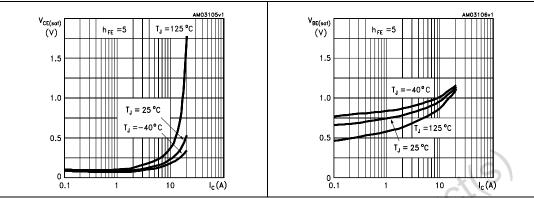
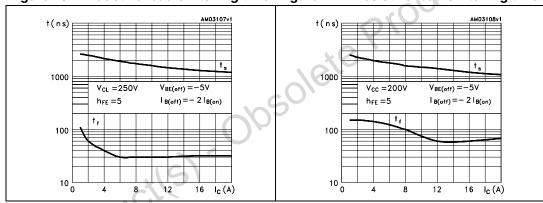
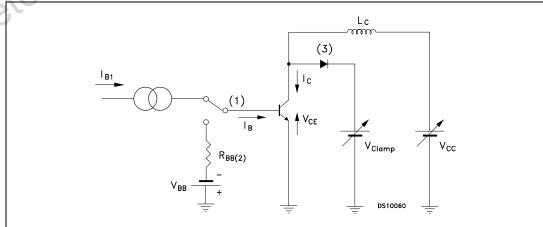


Figure 10. Inductive load switching time Figure 11. Resistive load switching time



2.2 Test circuits

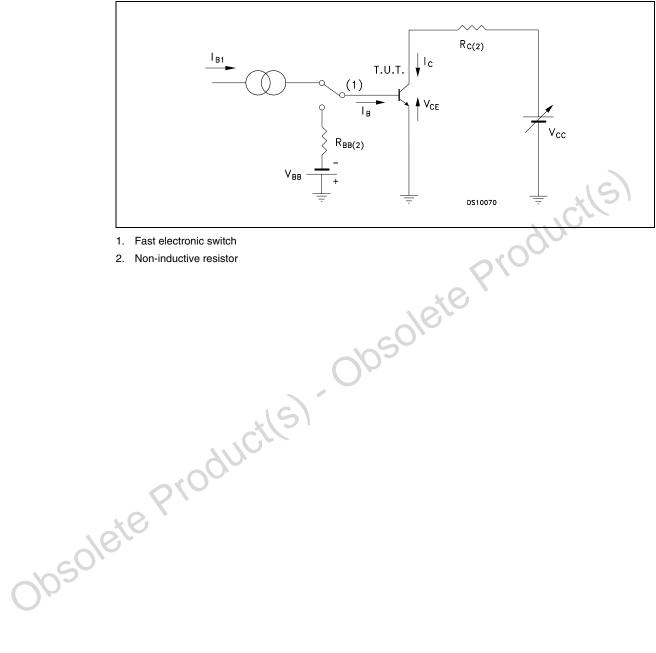
Figure 12. Inductive load switching test circuit



- 1. Fast electronic switch
- 2. Non-inductive resistor
- 3. Fast recovery rectifier

Electrical characteristics STW2040

Figure 13. Resistive load switching test circuit



57

3 Package mechanical data

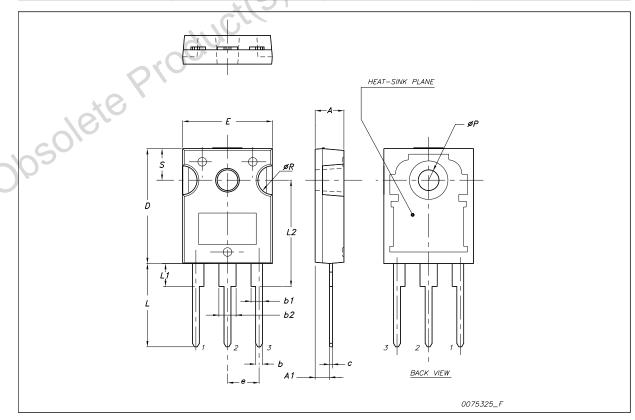
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577

Obsolete Product(s). Obsolete Product(s)

TO-247 mechanical data

Dim.	mm.			
	Min.	Тур.	Max.	
A	4.85		5.15	
A1	2.20		2.60	
b	1.0		1.40	
b1	2.0		2.40	
b2	3.0		3.40	
С	0.40		0.80	
D	19.85		20.15	
E	15.45		15.75	
е		5.45		
L	14.20	201	14.80	
L1	3.70	16/	4.30	
L2		18.50		
øΡ	3.55	103	3.65	
øR	4.50	7	5.50	
S	16	5.50		



STW2040 Revision history

4 Revision history

Table 5. Document revision history

	Date	Revision	Changes
	07-Nov-2008	1	Initial release.
	10-Jun-2009	2	Document status promoted from preliminary data to datasheet.
Obsole	Riod	ucil	Document status promoted from preliminary data to datasheet.

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10/10 Doc ID 15149 Rev 2