

Small switching (60V, 10A)

2SK2095N

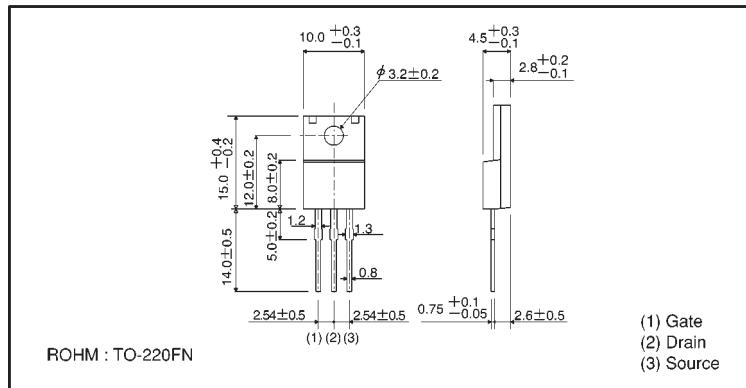
● Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Wide SOA (safe operating area).
- 4) Easily designed drive circuits.
- 5) Low $V_{GS(th)}$.
- 6) Easy to parallel.

● Structure

Silicon N-channel
MOSFET

● External dimensions (Units: mm)



● Absolute maximum ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Limits	Unit
Drain-source voltage	V_{DSS}	60	V
Gate-source voltage	V_{GSS}	± 20	V
Drain current	Continuous	I_D	A
	Pulsed	I_{DP}^*	A
Reverse drain current	Continuous	I_{DR}	A
	Pulsed	I_{DRP}^*	A
Total power dissipation ($T_c=25^\circ\text{C}$)	P_D	30	W
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	$-55 \sim +150$	$^\circ\text{C}$

* $P_w \leq 10 \mu\text{s}$, Duty cycle $\leq 1\%$

● Packaging specifications

Type	Package	Bulk
Code	—	
Basic ordering unit (pieces)	500	
2SK2095N	○	

● Electrical characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Gate-source leakage	I_{GSS}	—	—	± 100	nA	$V_{GS} = \pm 20\text{V}$, $V_{DS} = 0\text{V}$
Drain-source breakdown voltage	$V_{(BR)DSS}$	60	—	—	V	$I_D = 1\text{mA}$, $V_{GS} = 0\text{V}$
Zero gate voltage drain current	I_{DSS}	—	—	100	μA	$V_{DS} = 60\text{V}$, $V_{GS} = 0\text{V}$
Gate threshold voltage	$V_{GS(\text{th})}$	1.0	—	2.5	V	$V_{DS} = 10\text{V}$, $I_D = 1\text{mA}$
Static drain-source on-state resistance	$R_{DS(\text{on})}$	—	0.080	0.095	Ω	$I_D = 5\text{A}$, $V_{GS} = 10\text{V}$
	—	—	0.11	0.14		$I_D = 5\text{A}$, $V_{GS} = 4\text{V}$
Forward transfer admittance	$ Y_{fs} ^*$	5.0	—	—	S	$I_D = 5\text{A}$, $V_{DS} = 10\text{V}$
Input capacitance	C_{iss}	—	1600	—	pF	$V_{DS} = 10\text{V}$
Output capacitance	C_{oss}	—	600	—	pF	$V_{GS} = 0\text{V}$
Reverse transfer capacitance	C_{rss}	—	150	—	pF	$f = 1\text{MHz}$
Turn-on delay time	$t_{d(on)}$	—	30	—	ns	$I_D = 5\text{A}$, $V_{DD} = 30\text{V}$
Rise time	t_r	—	80	—	ns	$V_{GS} = 10\text{V}$
Turn-off delay time	$t_{d(off)}$	—	300	—	ns	$R_L = 6\Omega$
Fall time	t_f	—	100	—	ns	$R_G = 10\Omega$

* $P_w \leq 300\text{ }\mu\text{s}$, Duty cycle $\leq 1\%$

● Electrical characteristic curves

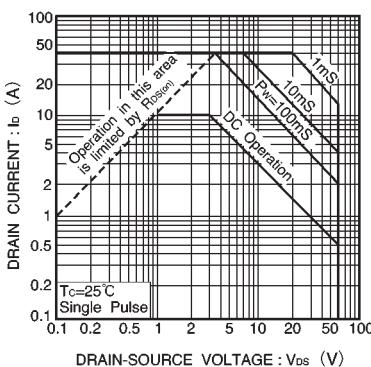


Fig.1 Maximum safe operating area

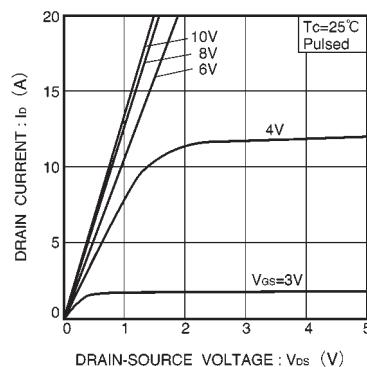


Fig.2 Typical output characteristics

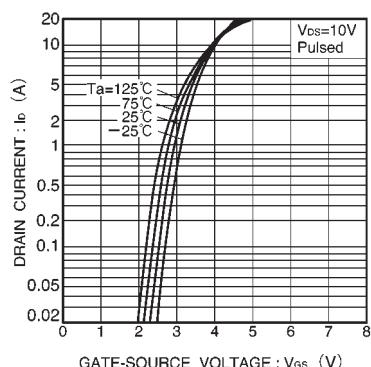


Fig.3 Typical transfer characteristics

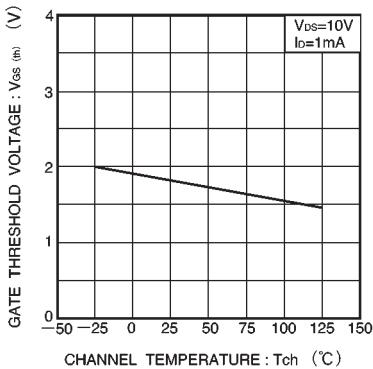


Fig.4 Gate threshold voltage vs. channel temperature

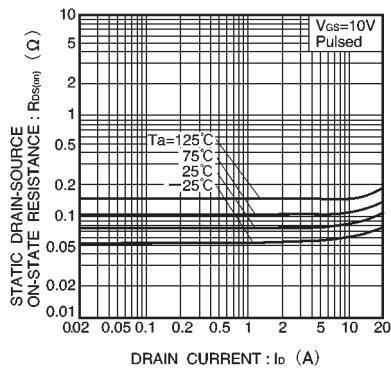


Fig.5 Static drain-source on-state resistance vs. drain current (I)

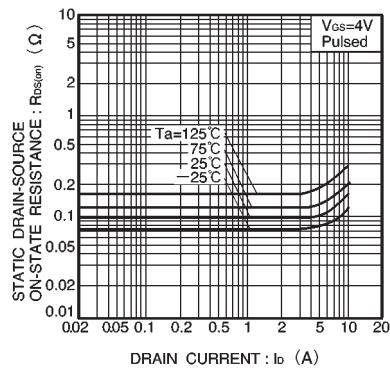


Fig.6 Static drain-source on-state resistance vs. drain current (II)

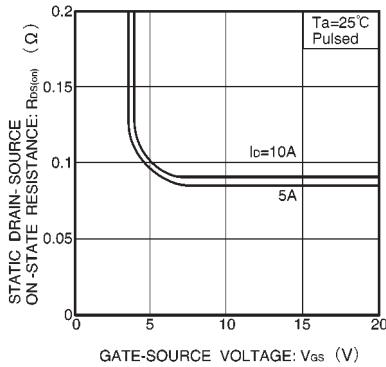


Fig.7 Static drain-source on-state resistance vs. gate-source voltage

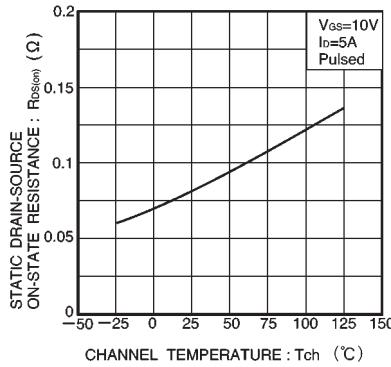


Fig.8 Static drain-source on-state resistance vs. channel temperature

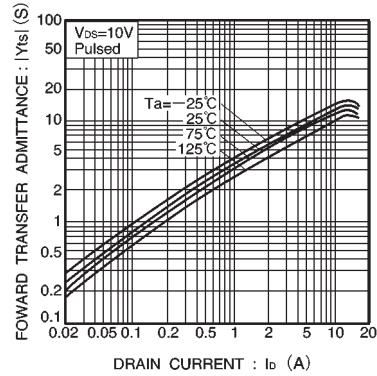


Fig.9 Forward transfer admittance vs. drain current

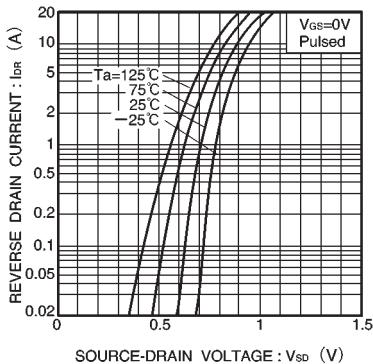


Fig.10 Reverse drain current vs. source-drain voltage (I)

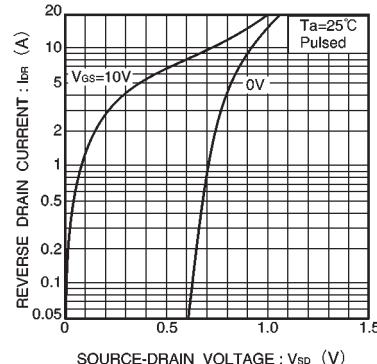


Fig.11 Reverse drain current vs. source-drain voltage (II)

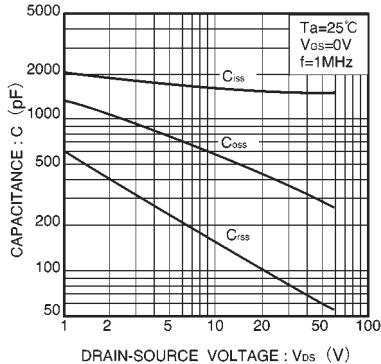


Fig.12 Typical capacitance vs. drain-source voltage

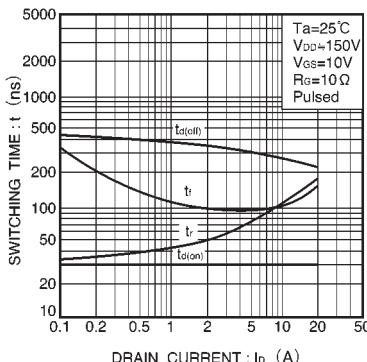


Fig.13 Switching characteristics
(See Figures 15 and 16 for the
measurement circuit and
resultant waveforms.)

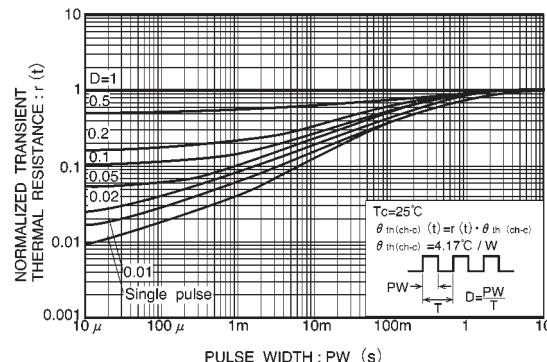


Fig.14 Normalized transient thermal
resistance vs. pulse width

● Switching characteristics measurement circuit

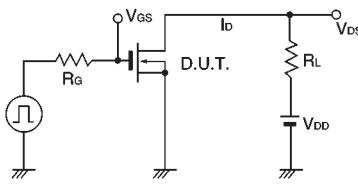


Fig.15 Switching time measurement circuit

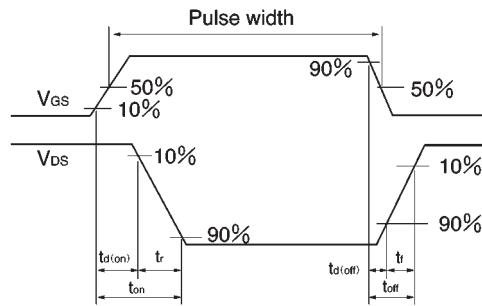


Fig.16 Switching time waveforms

Appendix

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