

TOSHIBA Field Effect Transistor with Built-in Schottky Barrier Diode
Silicon N-Channel MOS Type (U-MOS V-H)

TPC8A03-H

High Efficiency DC-DC Converter Applications

Notebook PC Applications

Portable Equipment Applications

- Built-in schottky barrier diode
Low forward voltage: $V_{DSF} = -0.6 \text{ V (max)}$
- High-speed switching
- Small gate charge: $Q_{SW} = 8.4 \text{ nC (typ.)}$
- Low drain-source ON-resistance: $R_{DS(ON)} = 4.1 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: $|Y_{fs}| = 54 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 100 \text{ }\mu\text{A (max)}$ ($V_{DS} = 30 \text{ V}$)
- Enhancement mode: $V_{th} = 1.3 \text{ to } 2.3 \text{ V}$ ($V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$)

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

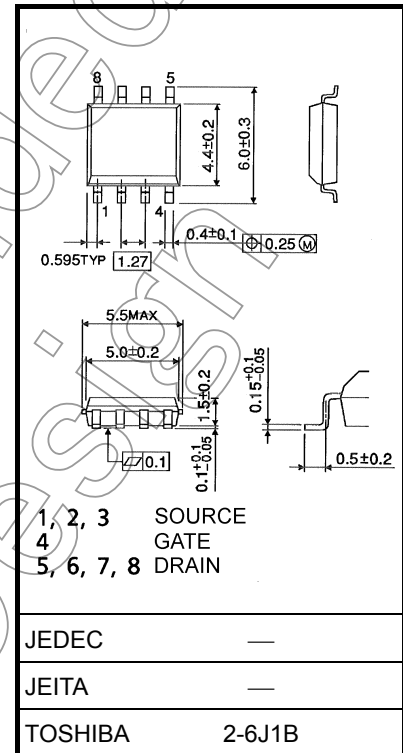
Characteristic		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	30	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	30	V
Gate-source voltage		V_{GSS}	± 20	V
Drain current	DC (Note 1)	I_D	17	A
	Pulsed (Note 1)	I_{DP}	68	
Drain power dissipation ($t = 10 \text{ s}$) (Note 2a)		P_D	1.9	W
Drain power dissipation ($t = 10 \text{ s}$) (Note 2b)		P_D	1.0	W
Single-pulse avalanche energy (Note 3)		E_{AS}	188	mJ
Avalanche current		I_{AR}	17	A
Repetitive avalanche energy ($T_c = 25^\circ\text{C}$) (Note 4)		E_{AR}	0.108	mJ
Channel temperature		T_{ch}	150	$^\circ\text{C}$
Storage temperature range		T_{stg}	-55 to 150	$^\circ\text{C}$

Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

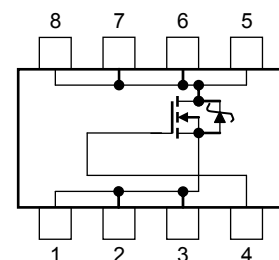
This transistor is an electrostatic-sensitive device. Handle with care.

Unit: mm



Weight: 0.085g (typ.)

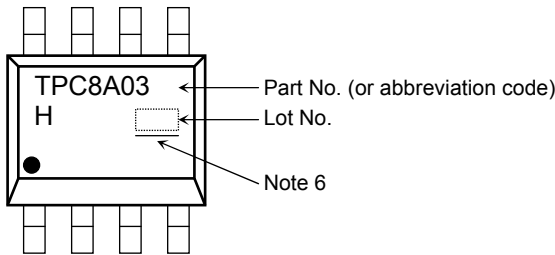
Circuit Configuration



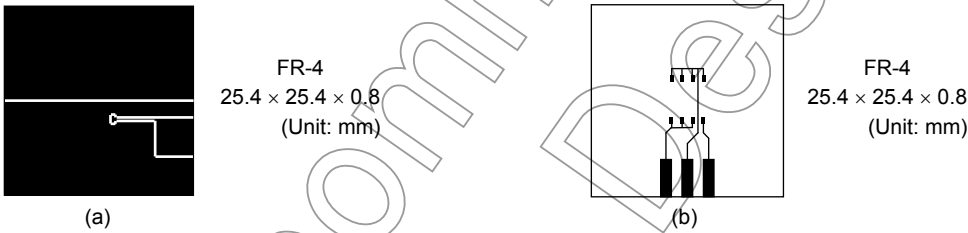
Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	$R_{th} (ch-a)$	65.8	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	$R_{th} (ch-a)$	125	°C/W

Marking (Note 5)



- Note 1: Ensure that the channel temperature does not exceed 150°C.
- Note 2: (a) Device mounted on a glass-epoxy board (a) (b) Device mounted on a glass-epoxy board (b)



- Note 3: $V_{DD} = 24\text{ V}$, $T_{ch} = 25^\circ\text{C}$ (initial), $L = 0.5\text{ mH}$, $R_G = 25\ \Omega$, $I_{AR} = 17\text{ A}$
- Note 4: Repetitive rating: pulse width limited by maximum channel temperature
- Note 5:

* Weekly code: (Three digits)

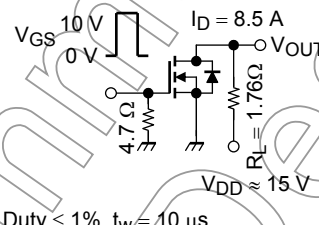
□ □ □ Week of manufacture
(01 for first week of year, continuing up to 52 or 53)

□ Year of manufacture
(The last digit of the calendar year)

- Note 6: A line under a Lot No. identifies the indication of product Labels.
- Not underlined: $[[Pb]]/INCLUDES > MCV$
- Underlined: $[[G]]/RoHS\ COMPATIBLE$ or $[[G]]/RoHS\ [[Pb]]$

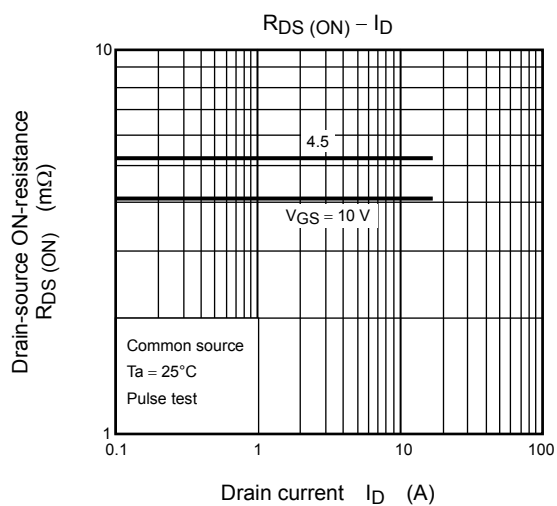
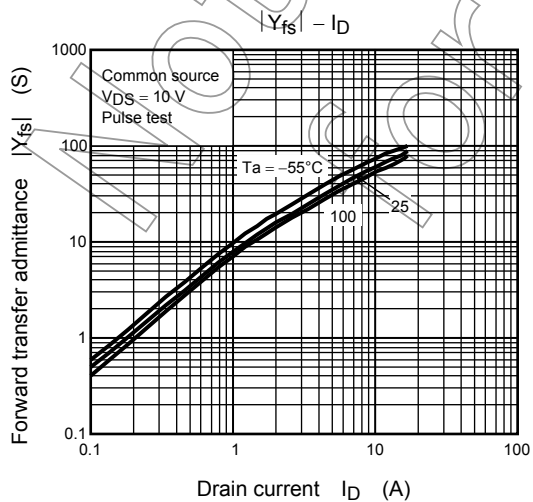
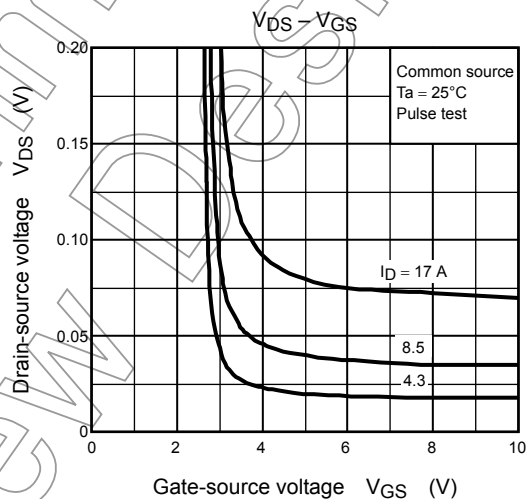
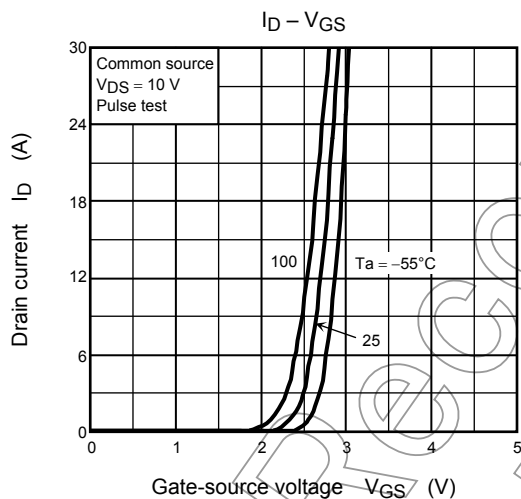
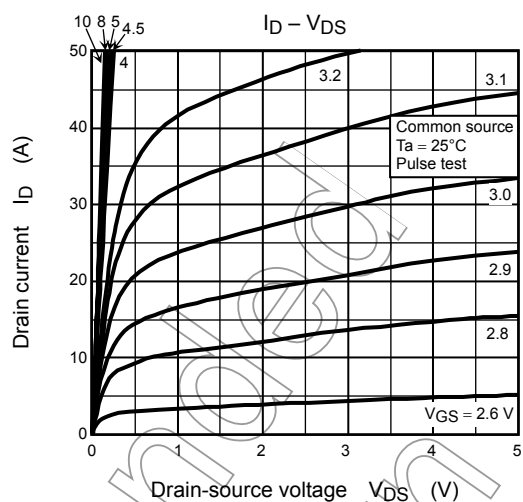
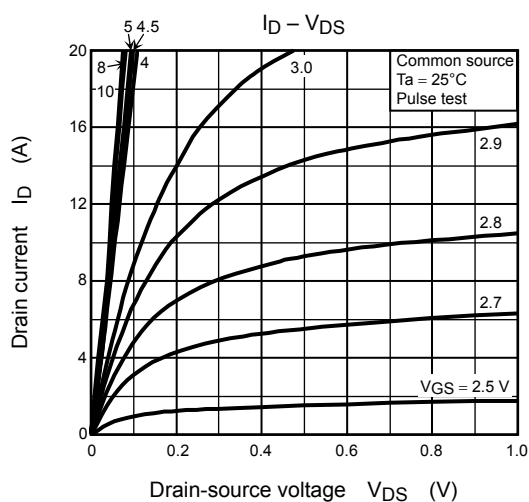
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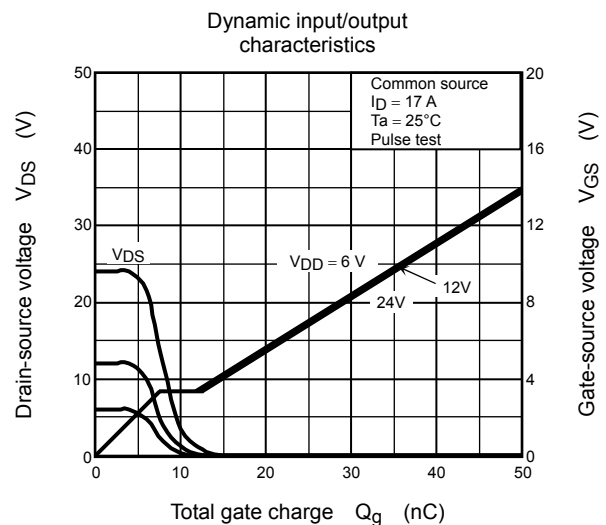
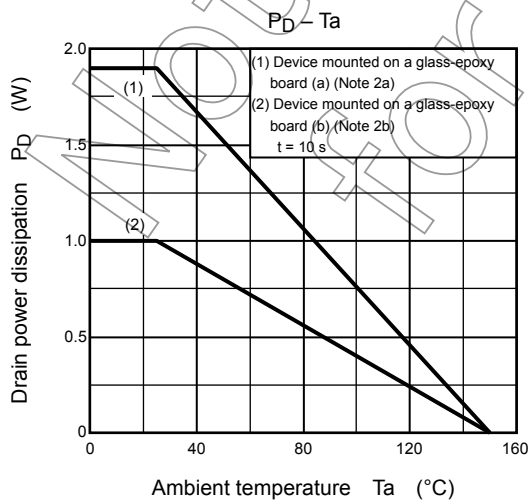
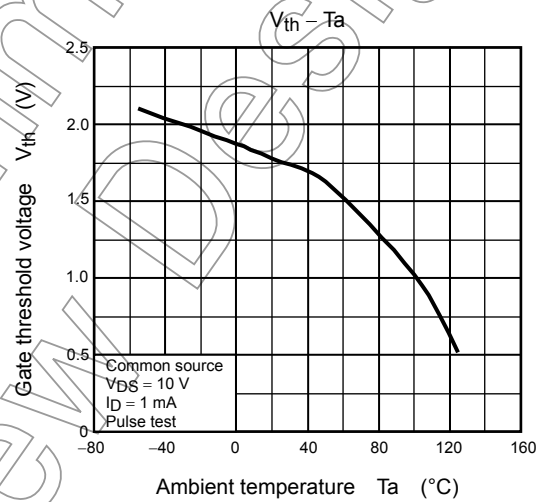
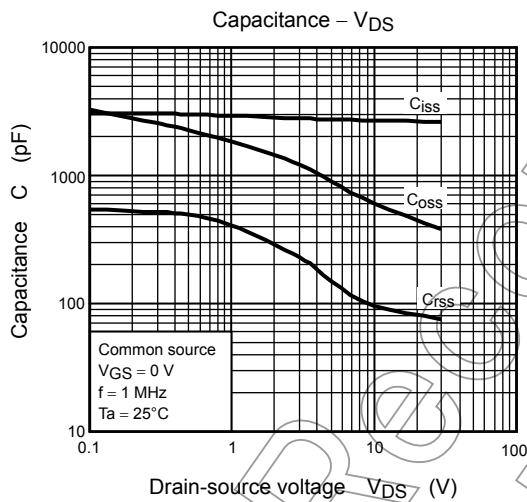
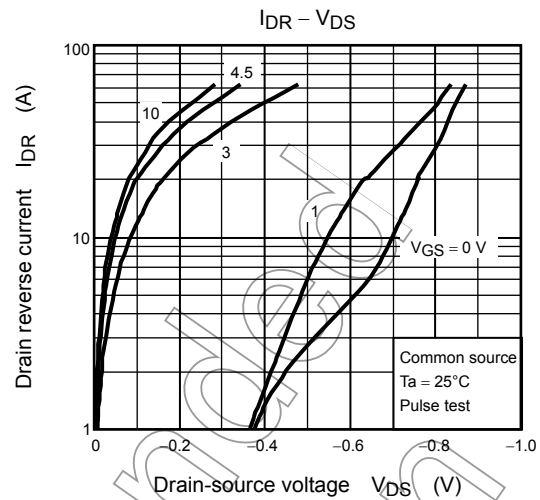
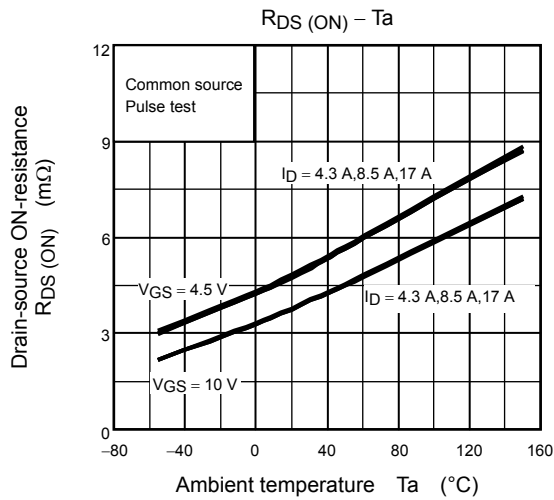
Electrical Characteristics (Ta = 25°C)

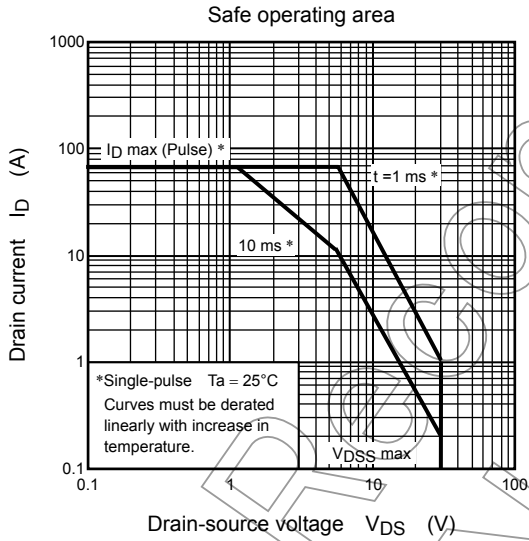
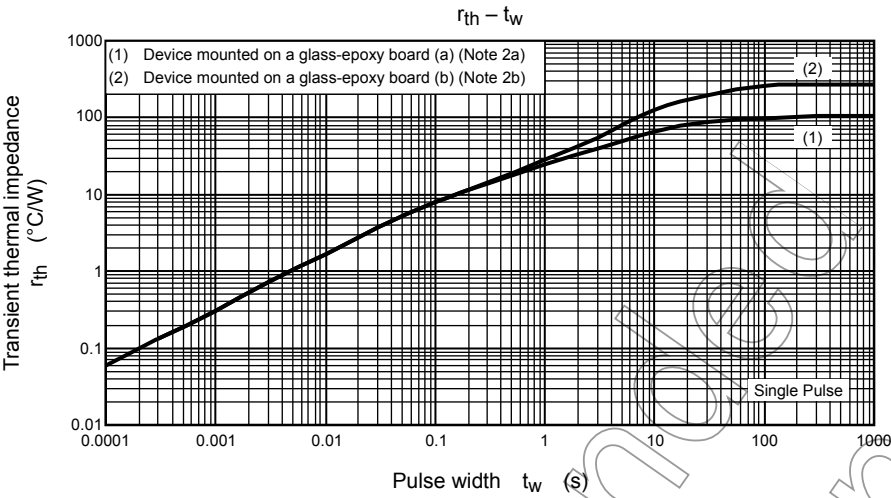
Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I _{GSS}	V _{GS} = ±20 V, V _{DS} = 0 V	—	—	±100	nA
Drain cut-off current		I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	—	—	100	μA
Drain-source breakdown voltage		V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	30	—	—	V
		V (BR) DSX	I _D = 10 mA, V _{GS} = -20 V	15	—	—	
Gate threshold voltage		V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.3	—	2.3	V
Drain-source ON-resistance		R _{DS (ON)}	V _{GS} = 4.5 V, I _D = 8.5 A	—	5.1	7.0	mΩ
			V _{GS} = 10 V, I _D = 8.5 A	—	4.1	5.6	
Forward transfer admittance		Y _{fs}	V _{DS} = 10 V, I _D = 8.5 A	27	54	—	S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	—	2640	3430	pF
Reverse transfer capacitance		C _{rss}		—	100	150	
Output capacitance		C _{oss}		—	610	—	
Gate resistance		r _g	V _{DS} = 10 V, V _{GS} = 0 V, f = 5 MHz	—	1.0	1.5	Ω
Switching time	Rise time	t _r		—	3.6	—	ns
	Turn-on time	t _{on}		—	11.0	—	
	Fall time	t _f		—	7.2	—	
	Turn-off time	t _{off}		Duty ≤ 1%, t _w = 10 μs	—	42	
Total gate charge (gate-source plus gate-drain)		Q _g	V _{DD} ≈ 24 V, V _{GS} = 10 V, I _D = 17 A	—	36	—	nC
			V _{DD} ≈ 24 V, V _{GS} = 5 V, I _D = 17 A	—	19	—	
Gate-source charge 1		Q _{gs1}	V _{DD} ≈ 24 V, V _{GS} = 10 V, I _D = 17 A	—	7.6	—	
Gate-drain ("Miller") charge		Q _{gd}		—	5.0	—	
Gate switch charge		Q _{SW}		—	8.4	—	

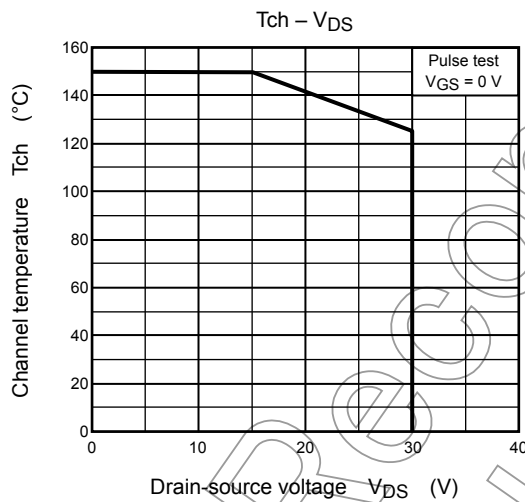
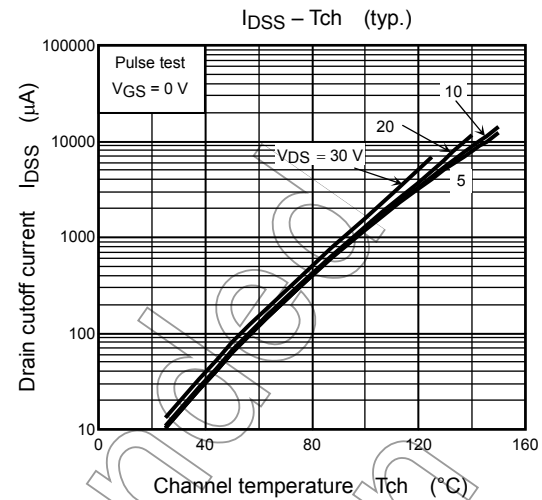
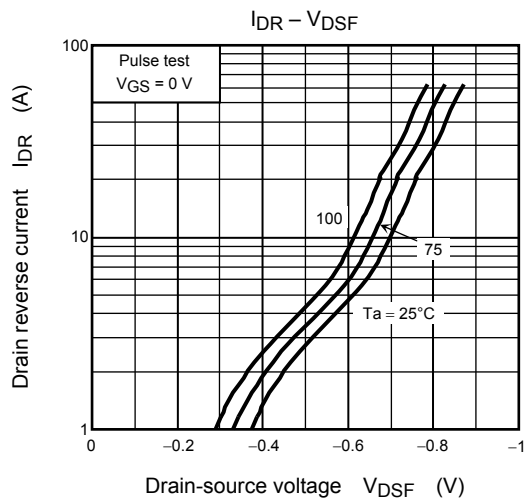
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
Drain reverse current	Pulse (Note 1)	I_{DRP}	—	—	—	68	A
Forward voltage (diode)		V_{DSF}	$I_{DR} = 1 \text{ A}, V_{GS} = 0 \text{ V}$	—	-0.4	-0.6	V
			$I_{DR} = 17 \text{ A}, V_{GS} = 0 \text{ V}$	—	—	-1.2	V









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