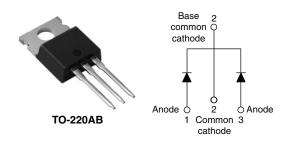


Vishay High Power Products

Schottky Rectifier, 2 x 10 A



PRODUCT SUMMARY				
I _{F(AV)} 2 x 10 A				
V_{R}	150 V			

FEATURES

- 175 °C T_J operation
- Center tap configuration
- · Low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	20	А		
V _{RRM}		150	V		
I _{FSM}	t _p = 5 μs sine	1030	А		
V _F	10 Apk, T _J = 125 °C (per leg)	0.66	V		
T _J	Range	- 55 to 175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	20CTQ150	UNITS	
Maximum DC reverse voltage	V _R	150	V	
Maximum working peak reverse voltage	V_{RWM}	150	V	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	. TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per leg		F(AV) 50 % duty cycle at T _C = 154 °C, rectangular waveform		10	Α
See fig. 5	per device	'F(AV)			20	^
Maximum peak one cycle			5 μs sine or 3 μs rect. pulse	Following any rated load	1030	
non-repetitive surge current p See fig. 7	er leg	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	180	Α
Non-repetitive avalanche energy per leg		E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 0.7 \text{A}, L = 10 \text{mH}$		2.45	mJ
Repetitive avalanche current per leg I _{AR}		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.7	А

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
	V _{FM} ⁽¹⁾	10 A	- T _J = 25 °C	0.80	0.88	V
Maximum forward voltage drop per leg		20 A		0.90	1.0	
See fig. 1		10 A	- T _J = 125 °C	0.63	0.66	
		20 A		0.73	0.77	
Maximum reverse leakage current per leg			V _B = Rated V _B	3.0	25	μΑ
See fig. 2	I _{RM}	T _J = 125 °C	VR = nateu VR	2.7	5.0	mA
Typical junction capacitance per leg	C_T $V_R = 5 V_{DC}$ (test signal range		ge 100 kHz to 1 MHz) 25 °C	1	280	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		1	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	je	T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance junction to case per leg	,	В	DO constitut		
Maximum thermal resistance junction to case per package	,	R _{thJC}	DC operation	1.0	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased (Only for TO-220)	0.50	
Approximate weight				2	g
Approximate weight				0.07	OZ.
Mounting torque	minimum			6 (5)	kgf · cm
Mounting torque —	maximum			12 (10)	(lbf ⋅ in)
Marking device	•		Case style TO-220AB	20CT	Q150

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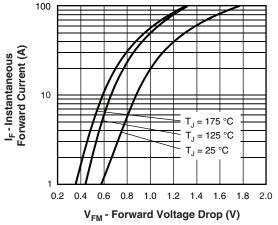


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

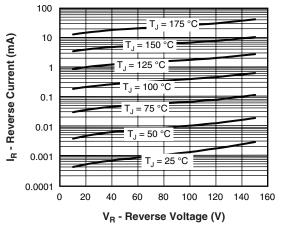


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

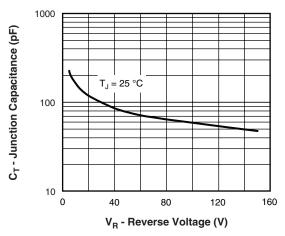


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

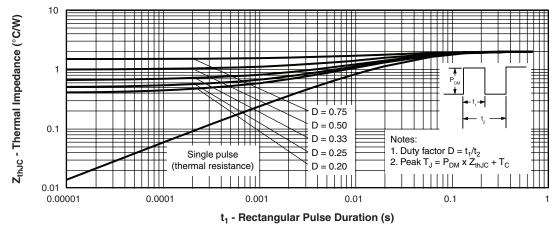


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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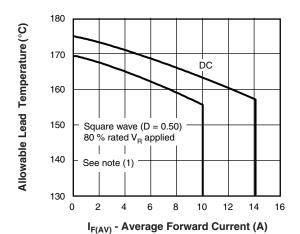


Fig. 5 - Maximum Average Forward Current vs.
Allowable Lead Temperature

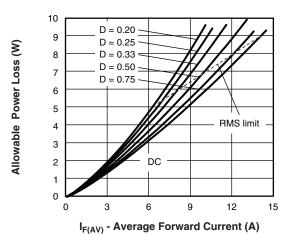


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

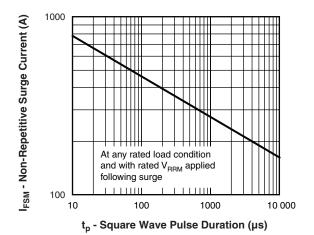


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

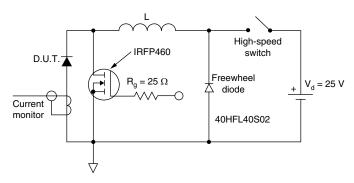


Fig. 8 - Unclamped Inductive Test Circuit

Note

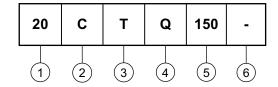
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



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ORDERING INFORMATION TABLE

Device code



- 1 Current rating (20 = 20 A)
- 2 Circuit configuration:

C = Common cathode

Package:

T = TO-220

- 4 Schottky "Q" series
- 5 Voltage rating (150 = 150 V)
- 6 • None = Standard production
 - PbF = Lead (Pb)-free

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95222					
Part marking information	http://www.vishay.com/doc?95225				

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Document Number: 91000 Revision: 18-Jul-08