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# ADC1210S125F1 ADC1210S125F1 Demo Board; CMOS

#### version

IDTs ADC1210S125F1 demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVCMOS output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance

### **Features**

- SMA connector for clock and analog input signals
- CMOS (buffered outputs)
- · Single power supply (on-board regulators)

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### **Documents**

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A	Demo Board Software ADC_1_r04	Manual - Software	ZIP	2.33 MB	07/19/2012

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PRODUCTID	TITLE
ADC1210S125HN	Single 12-bit ADC 125 Msps CMOS or LVDS DDR digital outputs

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