

Product Summary

Device	BV _{DSS}	R _{DSON} Max	I _D Max TA = +25°C
Q1	20V	0.99Ω @ V _{GS} = 4.5V	450mA
		1.2Ω @ V _{GS} = 2.5V	400mA
		1.8Ω @ V _{GS} = 1.8V	330mA
		2.4Ω @ V _{GS} = 1.5V	300mA
Q2	-20V	1.9Ω @ V _{GS} = -4.5V	-310mA
		2.4Ω @ V _{GS} = -2.5V	-280mA
		3.4Ω @ V _{GS} = -1.8V	-240mA
		5Ω @ V _{GS} = -1.5V	-180mA

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

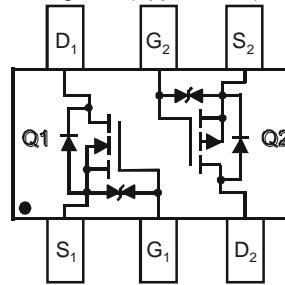
- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch



ESD PROTECTED



Top View



Top View
Schematic and
Transistor Diagram

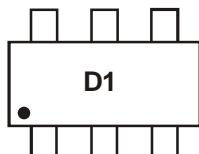
Ordering Information (Note 5)

Part Number	Case	Packaging
DMC2990UDJQ-7	SOT963	10K/Tape & Reel
DMC2990UDJQ-7B	SOT963	10K/Tape & Reel

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html.
5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



D1 = Product Type Marking Code

Maximum Ratings Q1 N-CHANNEL (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V_{GSS}	± 8	V
Continuous Drain Current (Note 6) $V_{GS} = 4.5\text{V}$	Steady State	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	450 350	mA
	$t < 5\text{s}$	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	520 410	mA
Continuous Drain Current (Note 6) $V_{GS} = 1.8\text{V}$	Steady State	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	330 260	mA
	$t < 5\text{s}$	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	390 310	mA
Maximum Continuous Body Diode Forward Current (Note 6)			I_S	440	mA
Pulsed Drain Current (Note 7)			I_{DM}	800	mA

Maximum Ratings Q2 P-CHANNEL (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-20	V
Gate-Source Voltage			V_{GSS}	± 8	V
Continuous Drain Current (Note 6) $V_{GS} = -4.5\text{V}$	Steady State	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	-310 -240	mA
	$t < 5\text{s}$	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	-360 -280	mA
Continuous Drain Current (Note 6) $V_{GS} = -1.8\text{V}$	Steady State	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	-240 -190	mA
	$t < 5\text{s}$	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	-280 -220	mA
Maximum Continuous Body Diode Forward Current (Note 6)			I_S	-440	mA
Pulsed Drain Current (Note 7)			I_{DM}	-800	mA

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Total Power Dissipation (Note 6)			P_D	350	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$		360	°C/W
	$t < 5\text{s}$			270	°C/W
Operating and Storage Temperature Range			T_J, T_{STG}	-55 to +150	°C

Notes: 6. Device mounted on FR-4 PCB, with minimum recommended pad layout.
 7. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.

Electrical Characteristics Q1 N-CHANNEL (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	20	-	-	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current @ $T_C = +25^\circ\text{C}$	I_{DSS}	-	-	100	nA	$\text{V}_{\text{DS}} = 16\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
		-	-	50		$\text{V}_{\text{DS}} = 5\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
		-	-	± 100	nA	$\text{V}_{\text{GS}} = \pm 5\text{V}, \text{V}_{\text{DS}} = 0\text{V}$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{TH})}$	0.4	-	1.0	V	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{I}_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$\text{R}_{\text{DS}(\text{ON})}$	-	0.60	0.99	Ω	$\text{V}_{\text{GS}} = 4.5\text{V}, \text{I}_D = 100\text{mA}$
		-	0.75	1.2		$\text{V}_{\text{GS}} = 2.5\text{V}, \text{I}_D = 50\text{mA}$
		-	0.90	1.8		$\text{V}_{\text{GS}} = 1.8\text{V}, \text{I}_D = 20\text{mA}$
		-	1.2	2.4		$\text{V}_{\text{GS}} = 1.5\text{V}, \text{I}_D = 10\text{mA}$
		-	2.0	-		$\text{V}_{\text{GS}} = 1.2\text{V}, \text{I}_D = 1\text{mA}$
Forward Transfer Admittance	$ \text{Y}_{\text{fs}} $	180	850	-	ms	$\text{V}_{\text{DS}} = 5\text{V}, \text{I}_D = 125\text{mA}$
Diode Forward Voltage	V_{SD}	-	0.6	1.0	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_S = 10\text{mA}$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C_{iss}	-	27.6	-	pF	$\text{V}_{\text{DS}} = 15\text{V}, \text{V}_{\text{GS}} = 0\text{V}, \text{f} = 1.0\text{MHz}$
Output Capacitance	C_{oss}	-	4.0	-	pF	
Reverse Transfer Capacitance	C_{rss}	-	2.8	-	pF	
Gate Resistance	R_g	-	113	-	Ω	$\text{V}_{\text{DS}} = 0\text{V}, \text{V}_{\text{GS}} = 0\text{V}, \text{f} = 1.0\text{MHz}$
Total Gate Charge	Q_{g}	-	0.5	-	nC	$\text{V}_{\text{GS}} = 4.5\text{V}, \text{V}_{\text{DS}} = 10\text{V}, \text{I}_D = 250\text{mA}$
Gate-Source Charge	Q_{gs}	-	0.07	-	nC	
Gate-Drain Charge	Q_{gd}	-	0.07	-	nC	
Turn-On Delay Time	$\text{t}_{\text{D}(\text{ON})}$	-	4.0	-	ns	$\text{V}_{\text{DD}} = 15\text{V}, \text{V}_{\text{GS}} = 4.5\text{V}, \text{R}_{\text{L}} = 47\Omega, \text{R}_g = 2\Omega, \text{I}_D = 200\text{mA}$
Turn-On Rise Time	t_{R}	-	3.3	-	ns	
Turn-Off Delay Time	$\text{t}_{\text{D}(\text{OFF})}$	-	19.0	-	ns	
Turn-Off Fall Time	t_{F}	-	6.4	-	ns	

 Electrical Characteristics Q2 P-CHANNEL (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	-20	-	-	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current @ $T_C = +25^\circ\text{C}$	I_{DSS}	-	-	100	nA	$\text{V}_{\text{DS}} = -16\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
		-	-	50		$\text{V}_{\text{DS}} = -5\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
		-	-	± 100	nA	$\text{V}_{\text{GS}} = \pm 5\text{V}, \text{V}_{\text{DS}} = 0\text{V}$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{TH})}$	-0.4	-	-1.0	V	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{I}_D = -250\mu\text{A}$
Static Drain-Source On-Resistance	$\text{R}_{\text{DS}(\text{ON})}$	-	1.2	1.9	Ω	$\text{V}_{\text{GS}} = -4.5\text{V}, \text{I}_D = -100\text{mA}$
		-	1.5	2.4		$\text{V}_{\text{GS}} = -2.5\text{V}, \text{I}_D = -50\text{mA}$
		-	2.1	3.4		$\text{V}_{\text{GS}} = -1.8\text{V}, \text{I}_D = -20\text{mA}$
		-	2.5	5		$\text{V}_{\text{GS}} = -1.5\text{V}, \text{I}_D = -10\text{mA}$
		-	4.0	-		$\text{V}_{\text{GS}} = -1.2\text{V}, \text{I}_D = -1\text{mA}$
Forward Transfer Admittance	$ \text{Y}_{\text{fs}} $	100	450	-	ms	$\text{V}_{\text{DS}} = -5\text{V}, \text{I}_D = -125\text{mA}$
Diode Forward Voltage	V_{SD}	-	-0.6	-1.0	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_S = -10\text{mA}$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C_{iss}	-	28.7	-	pF	$\text{V}_{\text{DS}} = -15\text{V}, \text{V}_{\text{GS}} = 0\text{V}, \text{f} = 1.0\text{MHz}$
Output Capacitance	C_{oss}	-	4.2	-	pF	
Reverse Transfer Capacitance	C_{rss}	-	2.9	-	pF	
Gate Resistance	R_g	-	399	-	Ω	$\text{V}_{\text{DS}} = 0\text{V}, \text{V}_{\text{GS}} = 0\text{V}, \text{f} = 1.0\text{MHz}$
Total Gate Charge	Q_{g}	-	0.4	-	nC	$\text{V}_{\text{GS}} = -4.5\text{V}, \text{V}_{\text{DS}} = -10\text{V}, \text{I}_D = -250\text{mA}$
Gate-Source Charge	Q_{gs}	-	0.08	-	nC	
Gate-Drain Charge	Q_{gd}	-	0.06	-	nC	
Turn-On Delay Time	$\text{t}_{\text{D}(\text{ON})}$	-	5.8	-	ns	$\text{V}_{\text{DD}} = -15\text{V}, \text{V}_{\text{GS}} = -4.5\text{V}, \text{R}_g = 2\Omega, \text{I}_D = -200\text{mA}$
Turn-On Rise Time	t_{R}	-	5.7	-	ns	
Turn-Off Delay Time	$\text{t}_{\text{D}(\text{OFF})}$	-	31.1	-	ns	
Turn-Off Fall Time	t_{F}	-	16.4	-	ns	

Notes: 8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

Typical Characteristics - N-CHANNEL

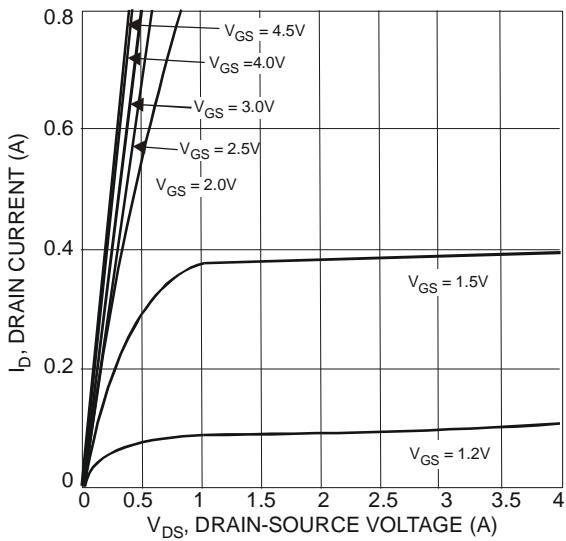


Fig. 1 Typical Output Characteristics

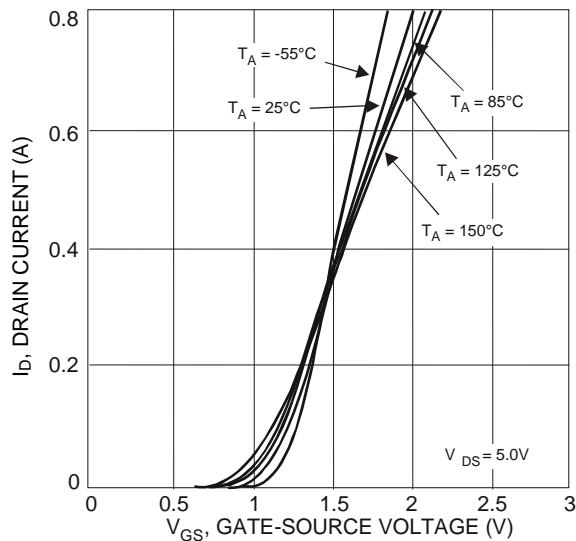


Fig. 2 Typical Transfer Characteristics

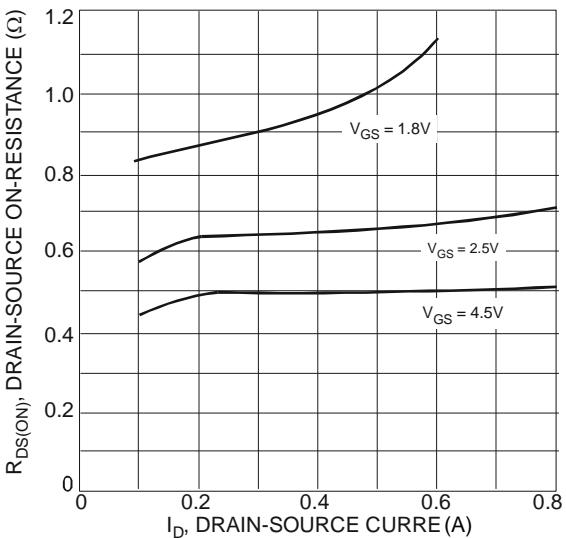


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

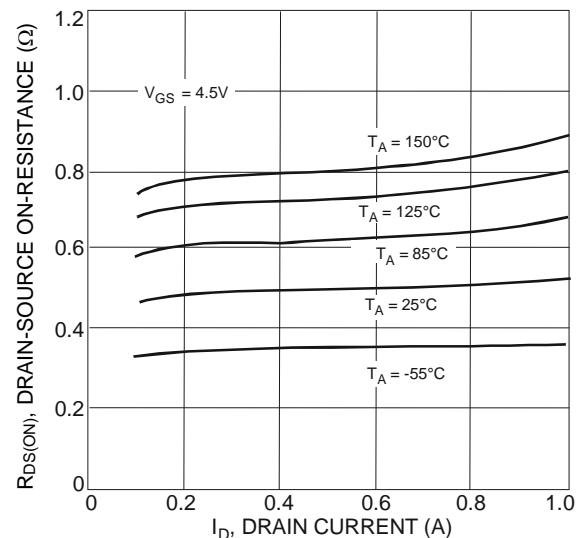


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

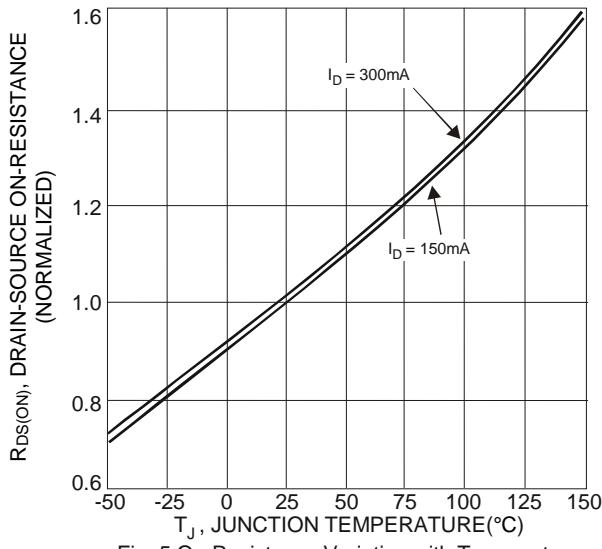


Fig. 5 On-Resistance Variation with Temperature

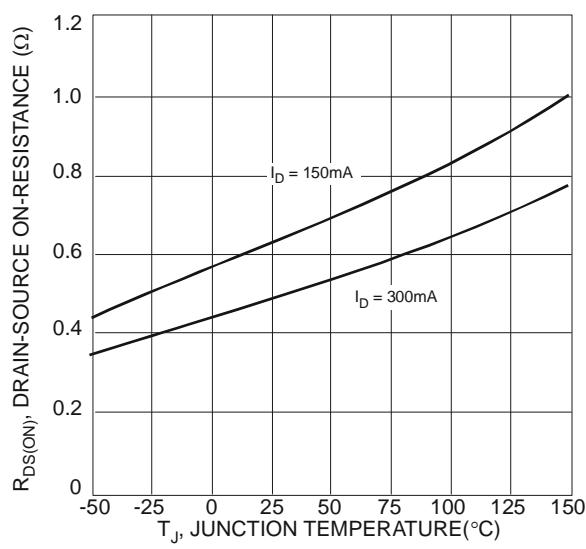


Fig. 6 On-Resistance Variation with Temperature

Typical Characteristics - N-CHANNEL (Cont.)

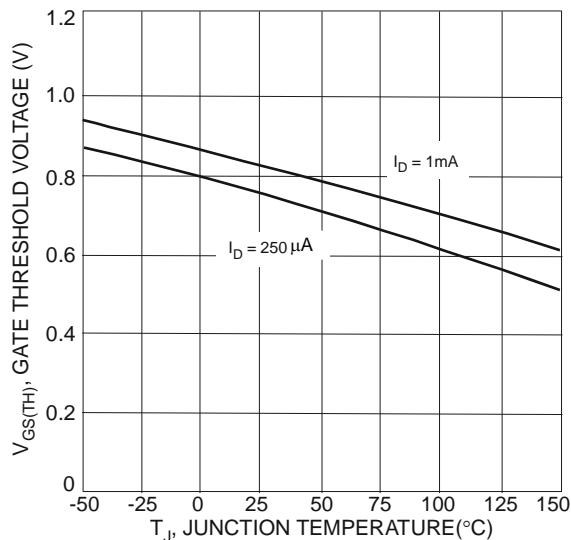


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

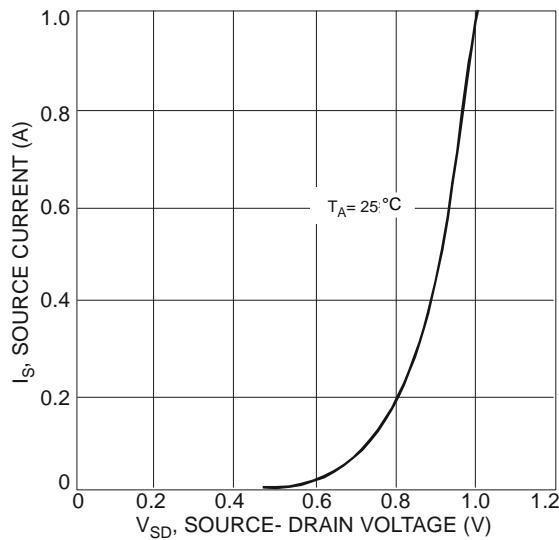


Fig. 8 Diodes Forward Voltage vs. Current

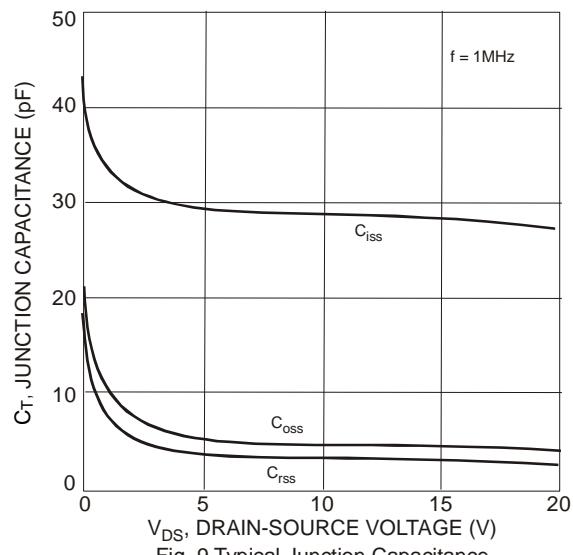


Fig. 9 Typical Junction Capacitance

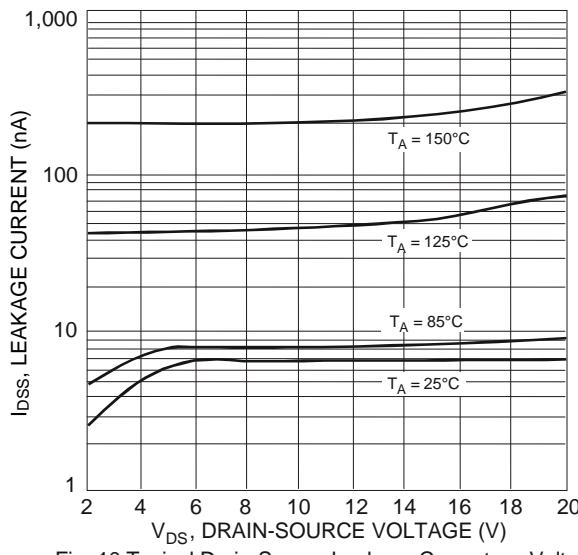


Fig. 10 Typical Drain-Source Leakage Current vs. Voltage

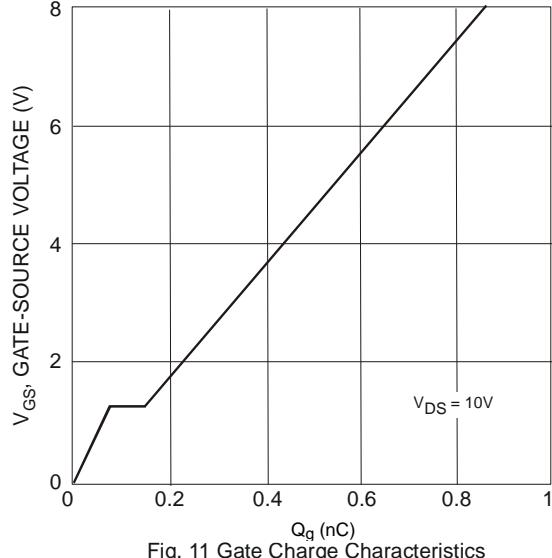


Fig. 11 Gate Charge Characteristics

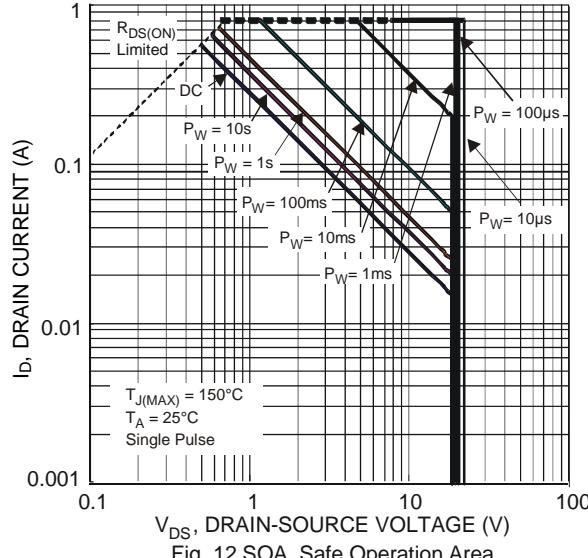


Fig. 12 SOA, Safe Operation Area

Typical Characteristics - P-CHANNEL

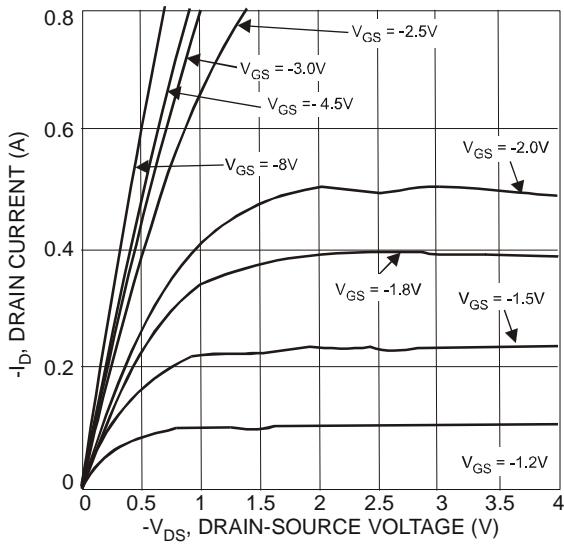


Fig. 13 Typical Output Characteristics

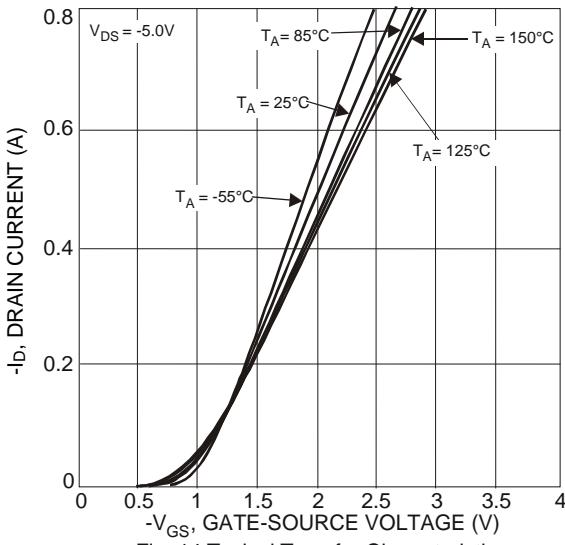


Fig. 14 Typical Transfer Characteristics

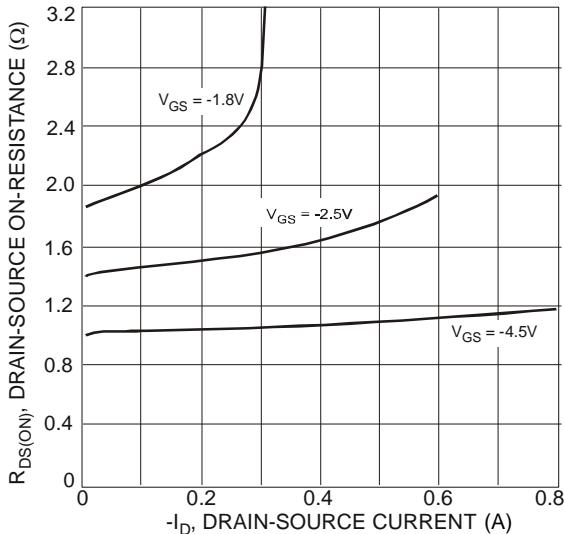


Fig. 15 Typical On-Resistance vs.
Drain Current and Gate Voltage

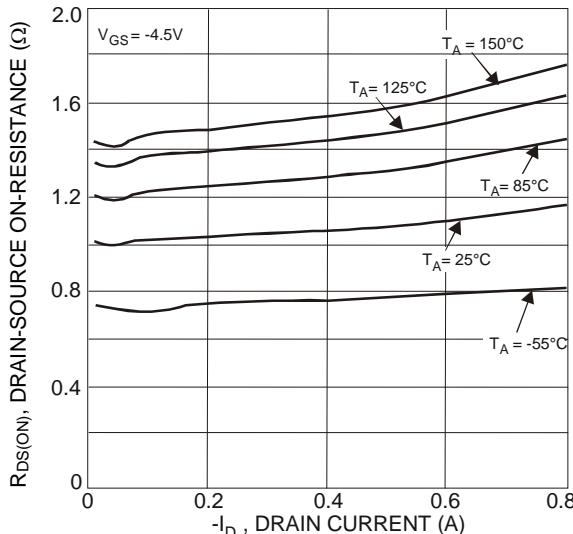


Fig. 16 Typical On-Resistance vs.
Drain Current and Temperature

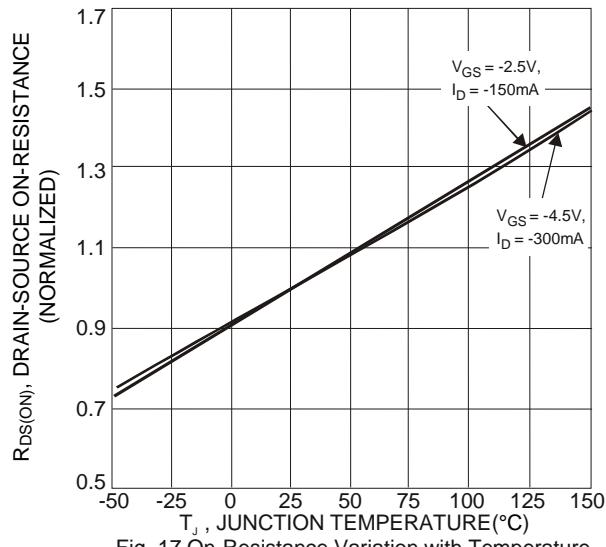


Fig. 17 On-Resistance Variation with Temperature

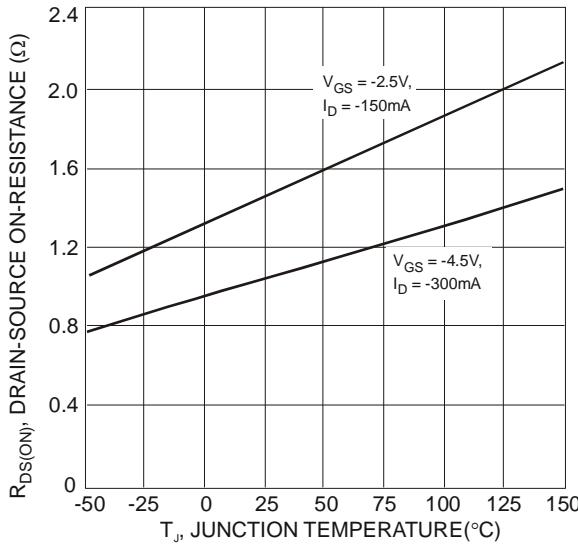


Fig. 18 On-Resistance Variation with Temperature

Typical Characteristics - P-CHANNEL (Cont.)

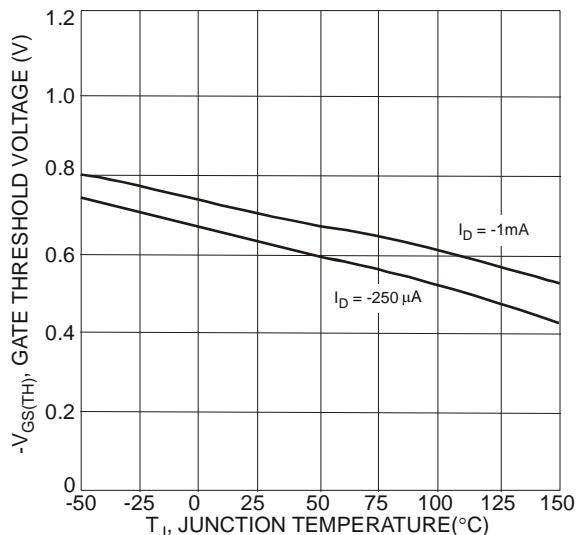


Fig. 19 Gate Threshold Variation vs. Ambient Temperature

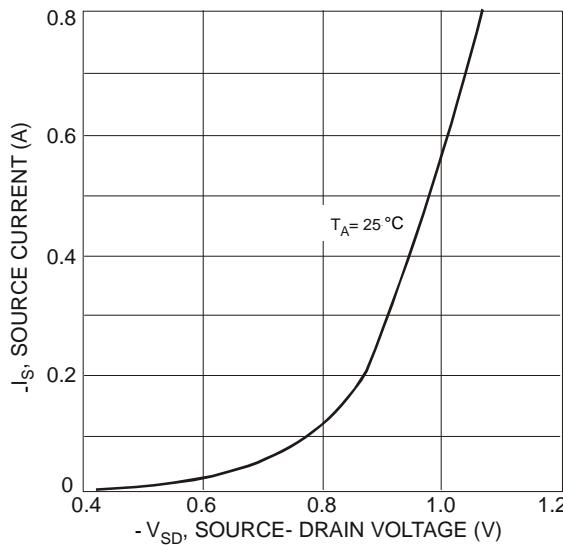


Fig. 20 Diodes Forward Voltage vs. Current

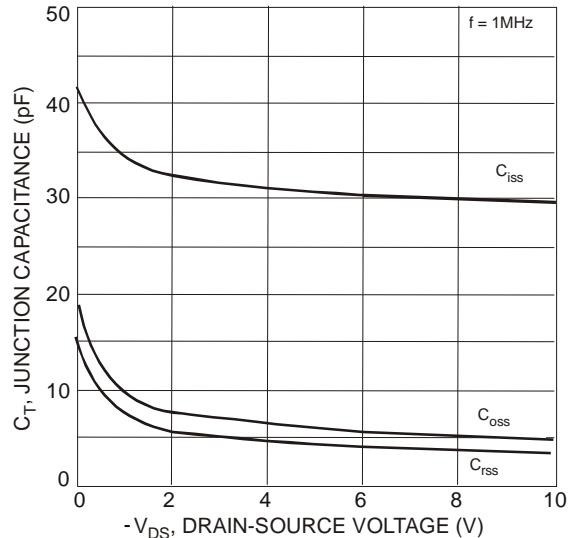


Fig. 21 Typical Junction Capacitance

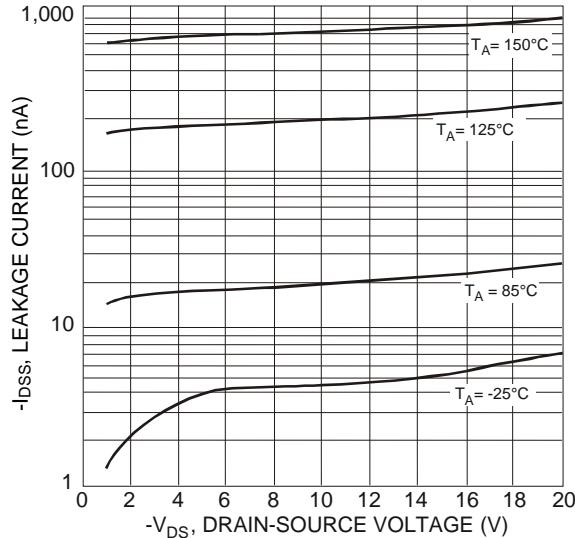


Fig. 22 Typical Leakage Current vs. Drain-Source Voltage

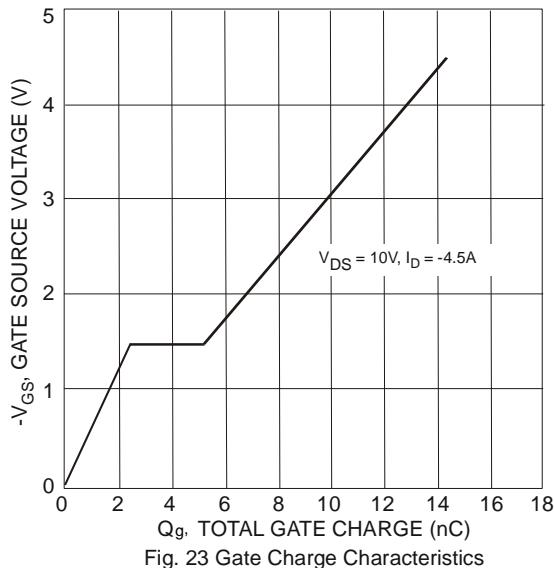


Fig. 23 Gate Charge Characteristics

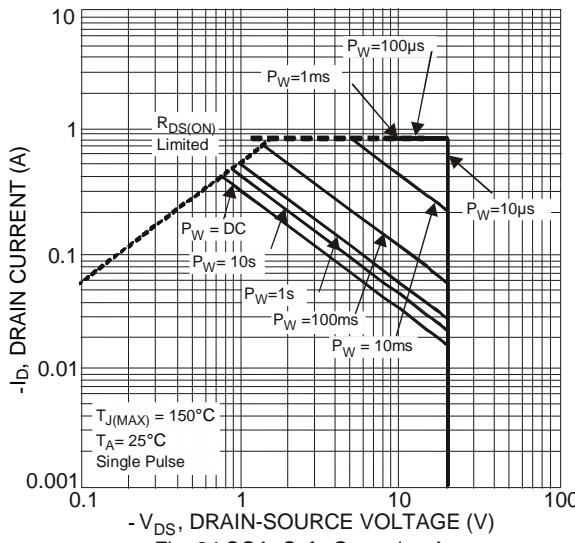
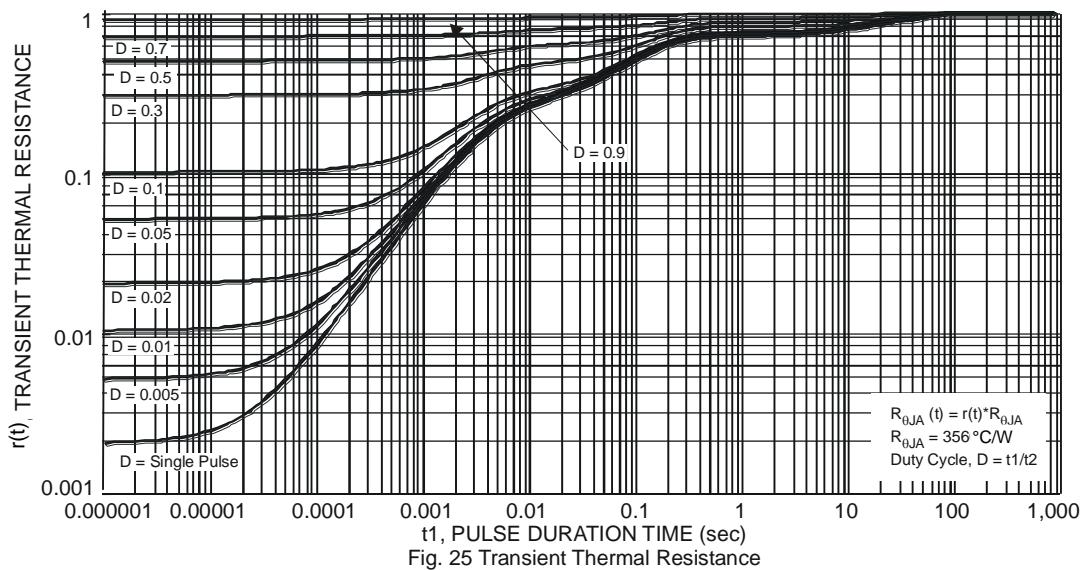


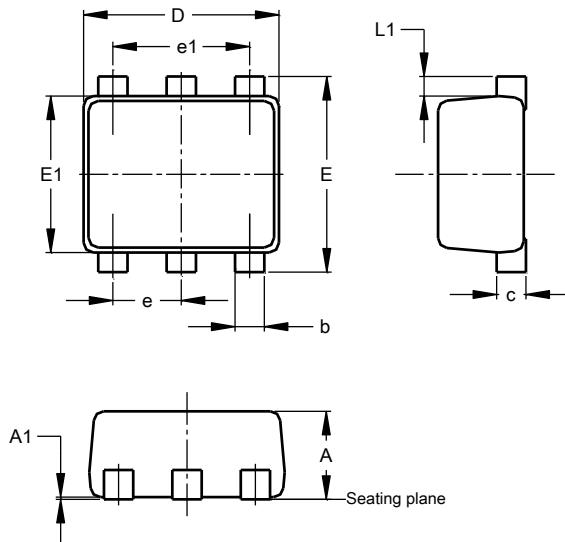
Fig. 24 SOA, Safe Operation Area



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT963



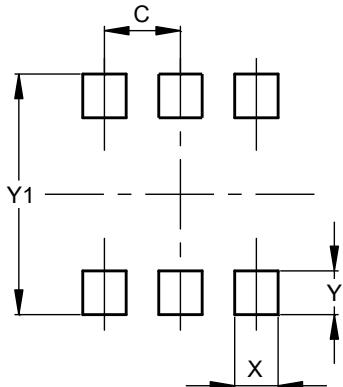
SOT963			
Dim	Min	Max	Typ
A	0.40	0.50	0.45
A1	0.00	0.05	--
b	0.10	0.20	0.15
c	0.120	0.180	0.150
D	0.95	1.05	1.00
E	0.95	1.05	1.00
E1	0.75	0.85	0.80
e	--	--	0.35
e1	--	--	0.70
L1	0.05	0.15	0.10

All Dimensions in mm

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT963



Dimensions	Value (in mm)
C	0.350
X	0.200
Y	0.200
Y1	1.100

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