

Product Summary

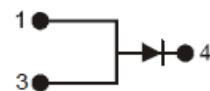
V _{RRM} (V)	I _o (A)	V _F max (V)	I _R max (mA)
60	10	0.52	0.4

Description

Packaged in the robust industry-standard TO252 (DPAK) package, the SBRT10U60D1Q provides very low V_F and excellent reverse leakage stability at high temperatures.



Top View



Package Pin-Out Configuration

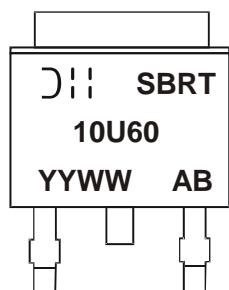
Ordering Information (Note 5)

Part Number	Compliance	Case	Packaging
SBRT10U60D1Q-13	Automotive	TO252 (DPAK)	2500 pieces/reel

Notes:

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html.
5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



SBRT10U60 = Product Type Marking Code

AB = Foundry and Assembly Code

YYWW = Date Code Marking

YY = Last Two Digits of Year (ex: 17 = 2017)

WW = Week (01 to 53)

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
 For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}		
Working Peak Reverse Voltage	V_{RWM}	60	V
DC Blocking Voltage	V_{RM}		
Average Rectified Output Current	I_O	10	A
Non-Repetitive Peak Forward Surge Current 8.3ms	I_{FSM}	140	A
Single Half Sine-Wave Superimposed on Rated Load			

Thermal Characteristics (Per Leg)

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 6)	$R_{\theta JA}$	18	°C/W
Typical Thermal Resistance Junction to Case (Note 6)	$R_{\theta JC}$	2	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (Per Leg) (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop	V_F	—	0.44	0.52	V	$I_F = 10\text{A}, T_J = +25^\circ\text{C}$ $I_F = 10\text{A}, T_J = +125^\circ\text{C}$
Leakage Current (Note 7)	I_R	—	0.08	0.4	mA	$V_R = 60\text{V}, T_J = +25^\circ\text{C}$ $V_R = 60\text{V}, T_J = +125^\circ\text{C}$

Notes: 6. 2inch*2inch Al board, minimum recommended pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
 7. Short duration pulse test used to minimize self-heating effect.

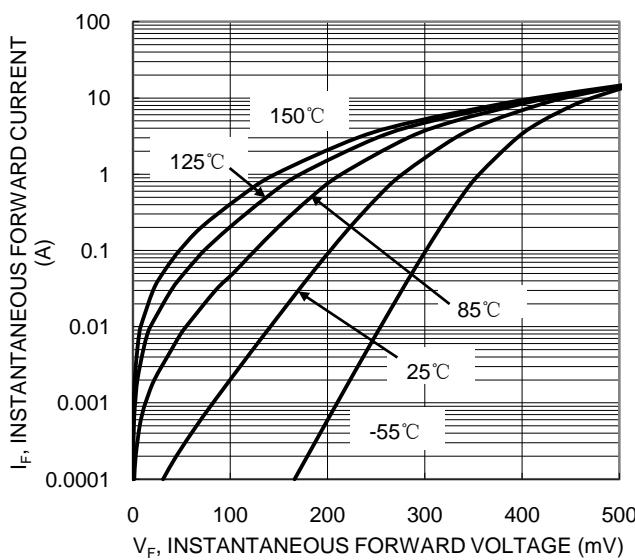


Figure 1. Typical Forward Characteristics

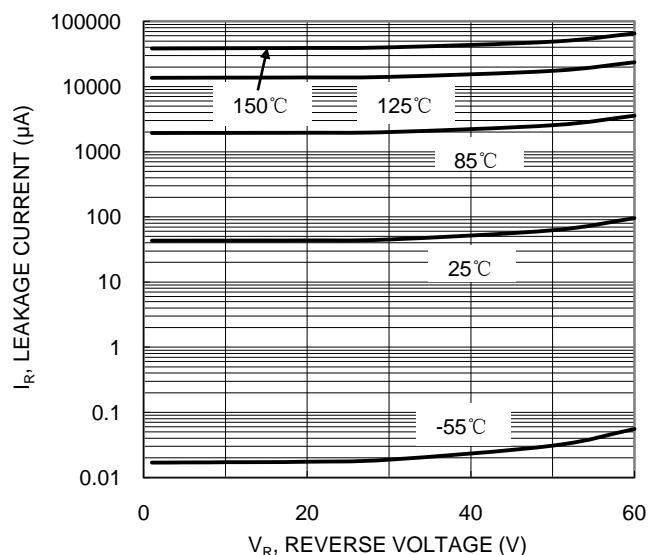


Figure 2. Typical Reverse Characteristics

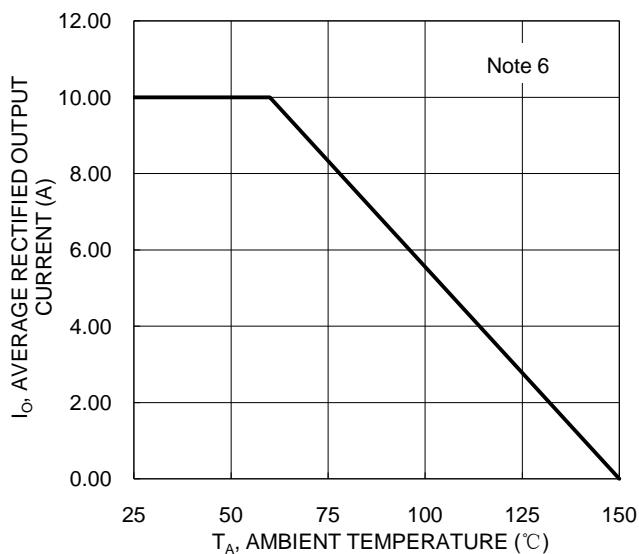


Figure 3. DC Forward Current Derating

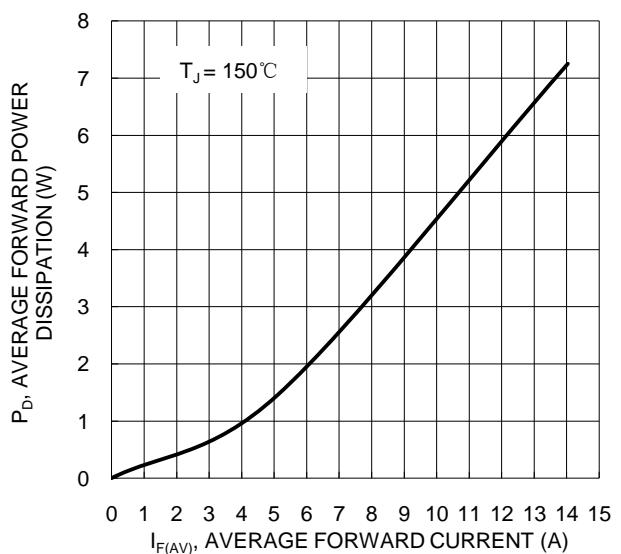


Figure 4. Forward Power Dissipation

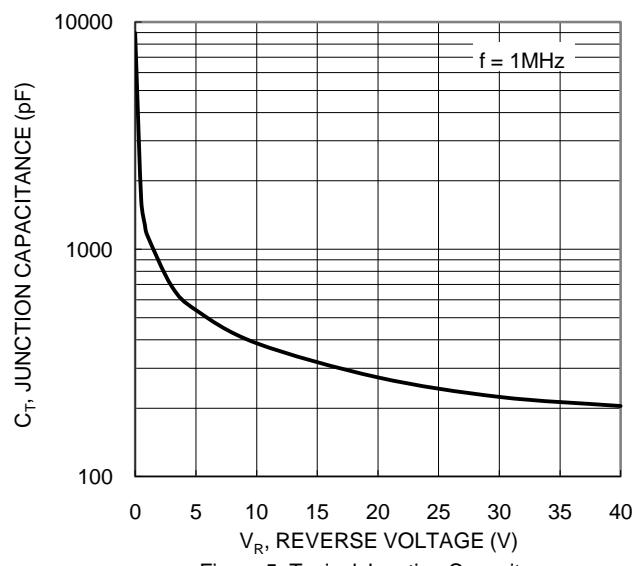
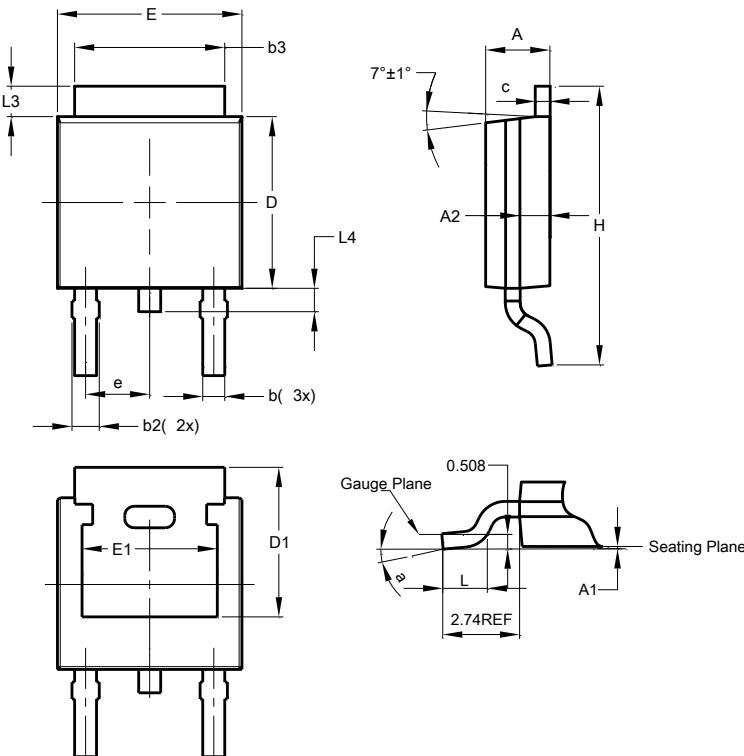


Figure 5. Typical Junction Capacitance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

TO252 (DPAK)



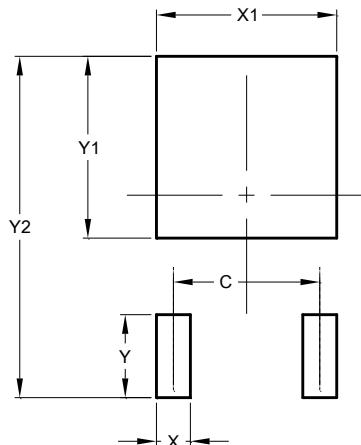
TO252 (DPAK)			
Dim	Min	Max	Typ
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b2	0.76	1.14	0.95
b3	5.21	5.46	5.33
c	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	-	-
e	-	-	2.286
E	6.45	6.70	6.58
E1	4.32	-	-
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	-

All Dimensions in mm

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

TO252 (DPAK)



Dimensions	Value (in mm)
C	4.572
X	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700

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