

Status: **Active**

- Specifications
- Product Options
- Documentation & Tools
- Technical Support
- Contact Sales

Features

- Product Description/
- Max frequency supported = 266MHz (DDR 533)
- Low skew, low jitter PLL clock driver
- I2C address selection for dual buffer application
- I2C for functional and output control
- Feedback pins for input to output synchronization
- Spread Spectrum tolerant inputs
- 3.3V tolerant CLK_INT input

OUTPUTS (#)	OUTPUT TYPE	OUTPUT FREQ RANGE (MHZ)	INPUTS (#)	INPUT SIGNAL	OUTPUT VOLTAGE (V)	OUTPUT SKEW (PS)
6		22.000000 - 340.000000			2.5	

ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
93777BFLF	Active	PYG28	SSOP	28	C	Y	Check Inventory
93777BFLFT	Active	PYG28	SSOP	28	C	Y	Check Inventory

	TITLE	TYPE ▲	FORMAT	FILE SIZE	DATE
	PC Clocks Contact Info	Misc	PDF	62 KB	05/29/2007

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