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Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

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HD74LS123 ● Dual Retriggerable Monostable Multivibrators (with Clear)

This d-c triggered multivibrator features output pulse width control by three method. The basic pulse time is programmed by selection of external resistance and capacitance values. Once triggered, the basic pulse width may be extended by retriggering the gated low-level-active (A) or high-level-active (B) inputs, or be reduced by use of the overriding clear. Fig. 1 illustrates pulse control by retriggering and early clear. This device is provided enough Schmitt hysteresis to ensure jitter-free triggering from the B input with transition rates as slow as 0.1 mV/ns.

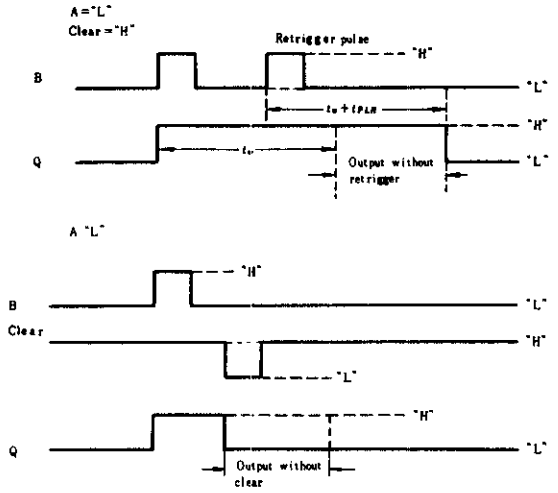
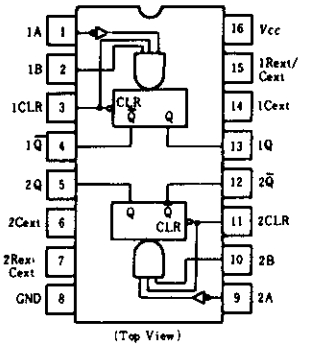
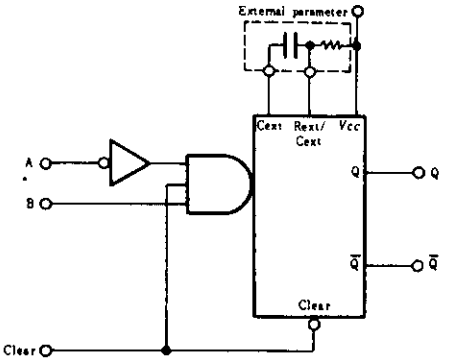


Fig.1 Typical Input/Output Pulses

■PIN ARRANGEMENT



■BLOCK DIAGRAM(½)



■RECOMMENDED OPERATING CONDITIONS

Item			Symbol	min	typ	max	Unit
Input pulse width	A, B	"H"	$t_{w(in)}$	40	—	—	ns
		"L"		40	—	—	ns
	CLR	"L"		40	—	—	ns
External timing resistance			R_{ext}	5	—	260	kΩ
External capacitance			C_{ext}	Non restriction			
Wiring capacitance at Rext/Cext terminal				—	—	50	pF

■FUNCTION TABLE

Inputs			Outputs	
CLEAR	A	B	Q	$\overline{Q}$
L	×	×	L	H
×	H	×	L	H
×	×	L	L	H
H	L	↑	⎍	⎎
H	↓	H	⎍	⎎
↑	L	H	⎍	⎎

Notes) H; high level, L; low level, X; irrelevant
↓; transition from high to low level
↑; transition from low to high level
⎍; one high-level pulse
⎎; one low-level pulse

■ELECTRICAL CHARACTERISTICS ($Ta = -20 \sim +75°C$)

Item	Symbol	Test Conditions		min	typ*	max	Unit
Input voltage	V_{IH}			2.0	–	–	V
	V_{IL}			–	–	0.8	V
Output voltage	V_{OH}	$V_{CC}=4.75V$, $V_{IH}=2V$, $V_{IL}=0.8V$, $I_{OH}=-400\mu A$		2.7	–	–	V
	V_{OL}	$V_{CC}=4.75V$, $V_{IH}=2V$, $V_{IL}=0.8V$	$I_{OL}=4mA$	–	–	0.4	V
			$I_{OL}=8mA$	–	–	0.5	
Input current	I_{IH}	$V_{CC}=5.25V$, $V_I=2.7V$		–	–	20	μA
	I_{IL}	$V_{CC}=5.25V$, $V_I=0.4V$		–	–	−0.4	mA
	I_I	$V_{CC}=5.25V$, $V_I=7V$		–	–	0.1	mA
Short-circuit output current	I_{OS}	$V_{CC}=5.25V$		−20	–	−100	mA
Supply current**	I_{CC}	$V_{CC}=5.25V$		–	12	20	mA
Input clamp voltage	V_{IK}	$V_{CC}=4.75V$, $I_{IN}=-18mA$		–	–	−1.5	V

* V_{CC}=5V, Ta=25°C

** With all outputs open and 4.5V applied to all data and clear inputs, I_{CC} is measured after a momentary ground, then 4.5V, is applied clock.

Note) To measure V_{OH} at Q, V_{OL} at $\overline{Q}$, or I_{OS} at Q, ground R_{ext}/C_{ext}, apply 2V to B and clear, and pulse A from 2V to 0V.

■SWITCHING CHARACTERISTICS ($V_{CC}=5V$, $Ta=25°C$)

Item	Symbol	Inputs	Outputs	Test Conditions	min	typ	max	Unit
Propagation delay time	t_{PLH}	A	Q	$C_{ext}=0pF$ $R_{ext}=5k\Omega$ $C_L=15pF$ $R_L=2k\Omega$	–	23	33	ns
	t_{PHL}		$\overline{Q}$		–	32	45	
	t_{PLH}	B	Q		–	23	44	
	t_{PHL}		$\overline{Q}$		–	34	56	
	t_{PHL}	CLR	Q		–	20	27	
	t_{PLH}		$\overline{Q}$		–	28	45	
Output pulse width	$t_{w(out)min}$	A, B	Q	$C_{ext}=1,000pF$, $R_{ext}=10k\Omega$ $C_L=15pF$, $R_L=2k\Omega$	–	116	200	ns
	$t_{w(out)}$				4	4.5	5	μs

■TYPICAL APPLICATION DATA FOR HD74LS123

For pulse widths when $Cext<1000pF$, See Fig. 3.
The output pulse is primarily a function of the external capacitor and resistor. For $C_{ext}>1000pF$, the output pulse width (tw) is defined as : $tw(out) = K \cdot Rext \cdot Cext$; See Fig. 4

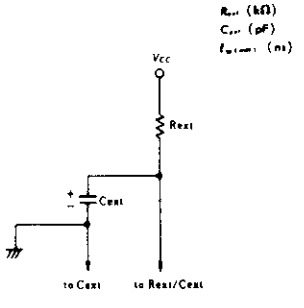


Fig.2 Timing Component Connections

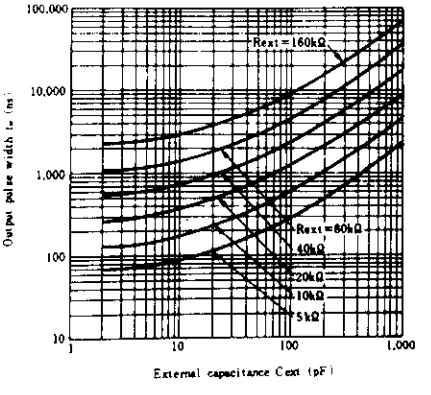


Fig.3 Typical Output Pulse Width (Cext≦1000pF)

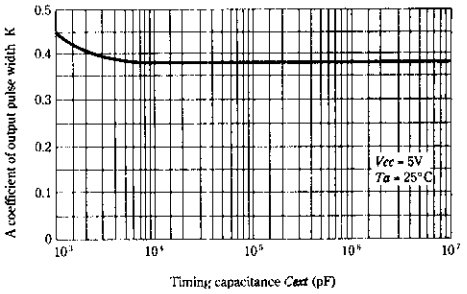
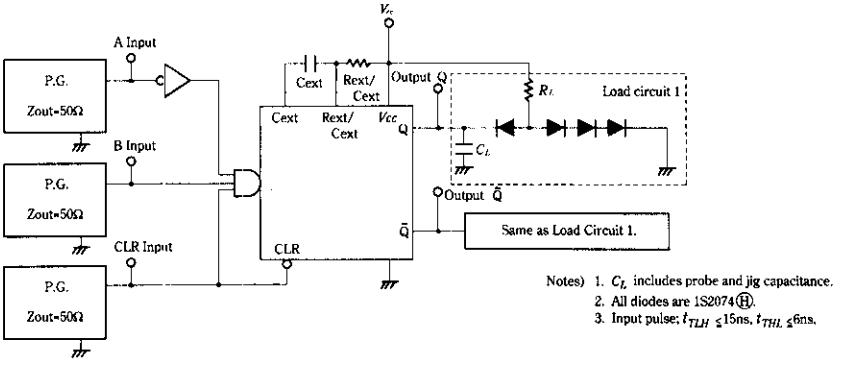


Fig.4 Cext vs K (Cext > 1000pF)

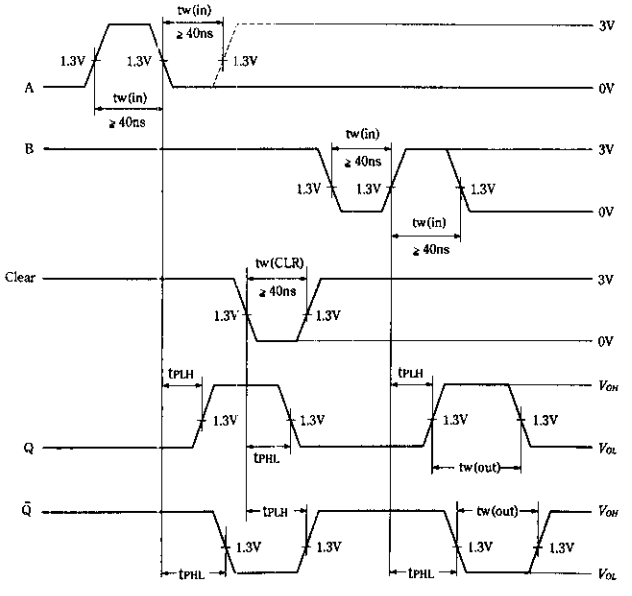
■TESTING METHOD

1) Test Circuit

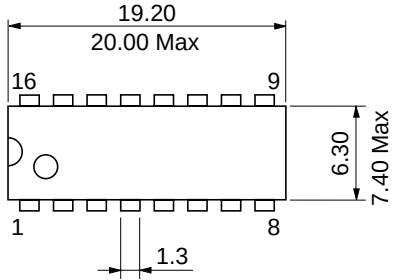


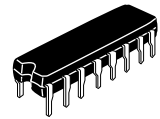
Notes) 1. C_L includes probe and jig capacitance.
2. All diodes are 1S2074Ⓗ.
3. Input pulse; t_{TLH} ≤15ns, t_{THL} ≤6ns.

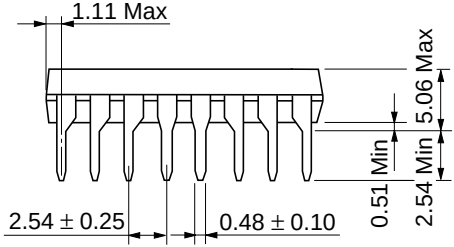
Waveform

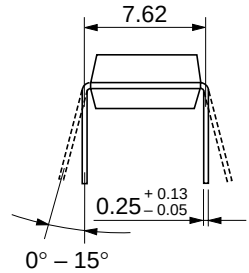


Unit: mm



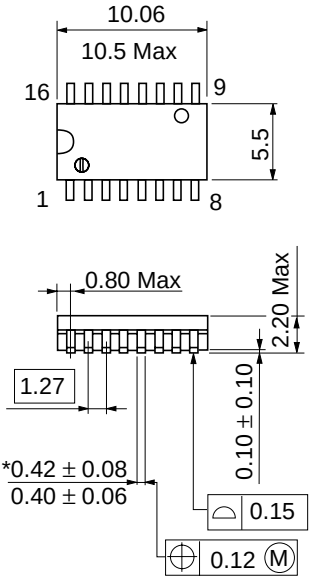


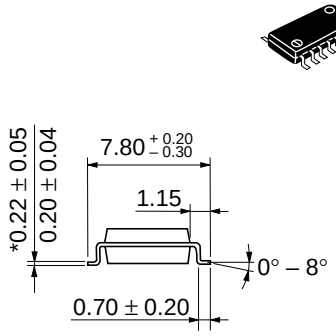




Hitachi Code	DP-16
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	1.07 g

Unit: mm

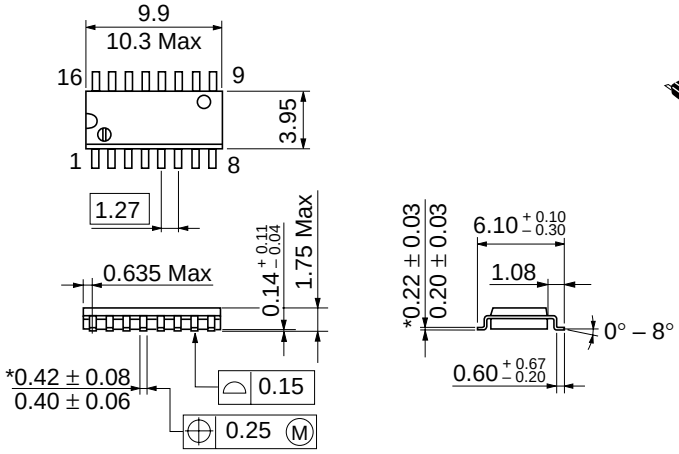




*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-16DA
JEDEC	—
EIAJ	Conforms
Weight (reference value)	0.24 g

Unit: mm



Hitachi Code	FP-16DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.15 g

*Dimension including the plating thickness
Base material dimension

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HITACHI

Hitachi, Ltd.
Semiconductor & Integrated Circuits.
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

URL
NorthAmerica : http:semiconductor.hitachi.com/
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For further information write to:

Hitachi Semiconductor
(America) Inc.
179 East Tasman Drive,
San Jose,CA 95134
Tel: <1> (408) 433-1990
Fax: <1>(408) 433-0223

Hitachi Europe GmbH
Electronic components Group
Dornacher Straße 3
D-85622 Feldkirchen, Munich
Germany
Tel: <49> (89) 9 9180-0
Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd.
Electronic Components Group.
Whitebrook Park
Lower Cookham Road
Maidenhead
Berkshire SL6 8YA, United Kingdom
Tel: <44> (1628) 585000
Fax: <44> (1628) 778322

Hitachi Asia Pte. Ltd.
16 Collyer Quay #20-00
Hitachi Tower
Singapore 049318
Tel: 535-2100
Fax: 535-1533

Hitachi Asia Ltd.
Taipei Branch Office
3F, Hung Kuo Building. No.167,
Tun-Hwa North Road, Taipei (105)
Tel: <886> (2) 2718-3666
Fax: <886> (2) 2718-8180

Hitachi Asia (Hong Kong) Ltd.
Group III (Electronic Components)
7/F., North Tower, World Finance Centre,
Harbour City, Canton Road, Tsim Sha Tsui,
Kowloon, Hong Kong
Tel: <852> (2) 735 9218
Fax: <852> (2) 730 0281
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