



16V Auto-Zero, Rail-to-Rail Output, Precision Amplifiers

Preliminary Technical Data

AD8638

FEATURES

Low Offset Voltage: 10 μV max.
Offset Drift: 0.08 $\mu\text{V}/^\circ\text{C}$
Rail-to-Rail Output
16V Single or $\pm 8\text{V}$ Dual Supply Operation
High Gain and CMRR: 140dB
High PSRR: 140 dB
Very Low Input Bias Current: 100 pA
Low Supply Current: 1.4 mA/amp

APPLICATIONS

Pressure and Position Sensors
Strain Gage Amplifiers
Medical Instrumentation
Thermocouple Amplifiers
Automotive Sensors
Precision References
Precision Current Sources

GENERAL DESCRIPTION

The AD8638 is a wide bandwidth auto-zero amplifiers featuring rail-to-rail output swing while operating from 5 V to 16 V single supply or $\pm 2.5\text{ V}$ to $\pm 8\text{V}$ dual supplies.

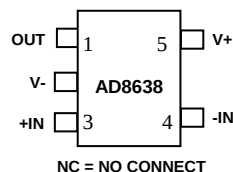
Using Analog Devices' new topology these zero-drift amplifiers combine low cost, with high accuracy and low noise. No external capacitors are required. In addition, the AD8638 family greatly reduces the digital switching noise often found in chopper-stabilized amplifiers.

With an offset voltage of 10 μV , offset drift less than 0.08 $\mu\text{V}/^\circ\text{C}$ and noise of only 1.5 $\mu\text{Vp-p}$ (0 Hz to 10 Hz), the AD8638 family is perfectly suited for applications where error sources must be minimized. Position and pressure sensors, thermocouple and thermopile detectors, and strain gage amplifiers benefit greatly from nearly zero drift over their operating temperature range.

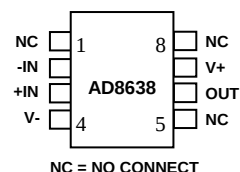
The AD8638 family is specified for the extended industrial (-40° to $+125^\circ\text{C}$) temperature range. The AD8638 is available in SOT-23 and SOIC.

PIN CONFIGURATIONS

5- Lead SOT-23
(RJ-5 Suffix)



8- Lead SOIC
(R-8 Suffix)



Rev. PrA

Information furnished by Analog Devices is believed to be accurate and reliable. However, no responsibility is assumed by Analog Devices for its use, nor for any infringements of patents or other rights of third parties that may result from its use. Specifications subject to change without notice. No license is granted by implication or otherwise under any patent or patent rights of Analog Devices. Trademarks and registered trademarks are the property of their respective owners.

One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106, U.S.A.
Tel: 781.329.4700
Fax: 781.461.3113
www.analog.com
©2007 Analog Devices, Inc. All rights reserved.

ELECTRICAL CHARACTERISTICS (@ $V_S = 16V$, $V_{CM} = V_S/2$, $T_A = +25^\circ C$ unless otherwise specified.)

Parameter	Symbol	Conditions	A Grade			Units
			Min	Typ	Max	
INPUT CHARACTERISTICS						
Offset Voltage	V _{OS}	25°C -40°< T _A < +125°C		3	10 20	μV μV
Input Bias Current	I _B	25°C -40°< T _A < +125°C		20	100 250	pA pA
Input Offset Current	I _{OS}	-40°< T _A < +125°C		40	80 150	pA pA
Input Voltage Range		-40°< T _A < +125°C	0		V _S - 2	V
Common-Mode Rejection Ratio	CMRR	V _{CM} = -5V to +5V -40°< T _A < +125°C	120 120	140 130		dB dB
Large Signal Voltage Gain (Note 1)	A _{VO}	R _L = 10 kΩ,V _O = -5 to +5V -40°< T _A < +125°C	130 115	130 130		dB dB
Offset Voltage Drift	ΔV _{OS} /ΔT	-40°< T _A < +125°C		0.004	0.08	μV/°C
OUTPUT CHARACTERISTICS						
Output Voltage High	V _{OH}	R _L = 2 kΩ -40°C to +125°C	15.8			V V
		R _L = 10KΩ -40°C to +125°C	15.9	15.7 15.95		V V
Output Voltage Low	V _{OL}	R _L = 2 kΩ -40°C to +125°C		100 170		mV mV
		R _L = 10KΩ -40°C to +125°C		30 70	50	mV mV
Short Circuit Limit	I _{SC}	-40°C to +125°C		±45		mA
Output Current	I _{OUT}	-40°C to +125°C		40 ±30		mA mA
		-40°C to +125°C				mA
POWER SUPPLY						
Power Supply Rejection Ratio	PSRR	V _S = 5V to 16V -40°C to +125°C	110	140		dB dB
Supply Current/Amplifier	I _{SY}	V _O = 0V -40°< T _A < +125°C		1.0	1.2 1.4	mA mA
DYNAMIC PERFORMANCE						
Slew Rate	SR	R _L =10 kΩ		2		V/μs
Overlay Recovery Time				50		μs
Gain Bandwidth Product	GBP			1.5		MHz
NOISE PERFORMANCE						
Voltage Noise	e _{n p-p}	f=0.1 to 10 Hz		1.5		μV _{p-p}
Voltage Noise Density	e _n	f=1kHz		59		nV/√Hz
Current Noise Density	i _n	f=10Hz		tbd		fA/√Hz

Note 1: Gain testing is highly dependent upon test bandwidth

ELECTRICAL CHARACTERISTICS (@ $V_S = 5V$, $V_{CM} = V_S/2$, $T_A = +25^\circ C$ unless otherwise specified.)

Parameter	Symbol	Conditions	A Grade		Units
			Min	Typ	Max
INPUT CHARACTERISTICS					
Offset Voltage	V _{OS}	-40°< T _A < +125°C		3	10 20
Input Bias Current	I _B	-40°< T _A < +125°C		15	40 120
Input Offset Current	I _{OS}	-40°< T _A < +125°C		30	40 60
Input Voltage Range		-40°< T _A < +125°C	0		V _S - 2
Common-Mode Rejection Ratio	CMRR	V _{CM} = -5V to +5V -40°< T _A < +125°C	120 120	135 130	dB dB
Large Signal Voltage Gain (Note 1)	A _{VO}	R _L = 10 kΩ,V _O = -5 to +5V -40°< T _A < +125°C	130 120	140 140	dB dB
Offset Voltage Drift	ΔV _{OS} /ΔT	-40°< T _A < +125°C		0.004	0.08
OUTPUT CHARACTERISTICS					
Output Voltage High	V _{OH}	R _L = 2 kΩ -40°C to +125°C	4.9	4.95 4.9	V V
		R _L = 10 kΩ -40°C to +125°C	4.95	4.99 4.99	V V
Output Voltage Low	V _{OL}	R _L = 2 kΩ -40°C to +125°C		25 40	mV mV
		R _L = 10 kΩ -40°C to +125°C		7 15	14 mV
Short Circuit Limit	I _{SC}	-40°C to +125°C		±20	mA
Output Current	I _{OUT}	-40°C to +125°C		15 ±tbd	mA mA
POWER SUPPLY					
Power Supply Rejection Ratio	PSRR	V _S = 5V to 16V -40°C to +125°C	110	140	dB dB
Supply Current/Amplifier	I _{SY}	V _O = 0V -40°< T _A < +125°C		0.9	1 1.2
DYNAMIC PERFORMANCE					
Slew Rate	SR	R _L =10 kΩ		2	V/μs
Overlay Recovery Time				50	μs
Gain Bandwidth Product	GBP			1.3	MHz
NOISE PERFORMANCE					
Voltage Noise	e _{n p-p}	f=0.1 to 10 Hz		1.5	μV _{p-p}
Voltage Noise Density	e _n	f=1kHz		59	nV/√Hz
Current Noise Density	i _n	f=10Hz		tbd	fA/√Hz

Note 1: Gain testing is highly dependent upon test bandwidth

ABSOLUTE MAXIMUM RATINGS¹

Supply voltage	+16V
Input Voltage	$\pm V_s$
Differential Input Voltage ¹	$\pm V_s$
Output Short-Circuit Duration to Gnd	Indefinite
Storage Temperature Range	
R, RM, RU Packages	-65°C to +150°C
Operating Temperature Range	
AD8638	-40°C to +125°C
Junction Temperature Range	
R, RT Packages	-65°C to +150°C
Lead Temperature Range (Soldering, 60 Sec)	+300°C

Package Type	θ_{JA} ²	θ_{JC}	Units
5-Lead SOT-23 (RT-5)	230	146	°C/W
8-Pin SOIC (R)	158	43	°C/W

NOTES

¹ Differential input voltage is limited to $\pm 5V$ or the supply voltage whichever is less.

² θ_{JA} is specified for the worst case conditions, i.e., θ_{JA} is specified for device soldered in circuit board for surface mount packages.

ORDERING GUIDE

Model	Temperature Range	Package Description	Package Option
AD8638ARJZ	-40°C to +125°C	5-Lead SOT-23	RJ-5
AD8638ARZ	-40°C to +125°C	8-Lead SOIC_N	R-8

