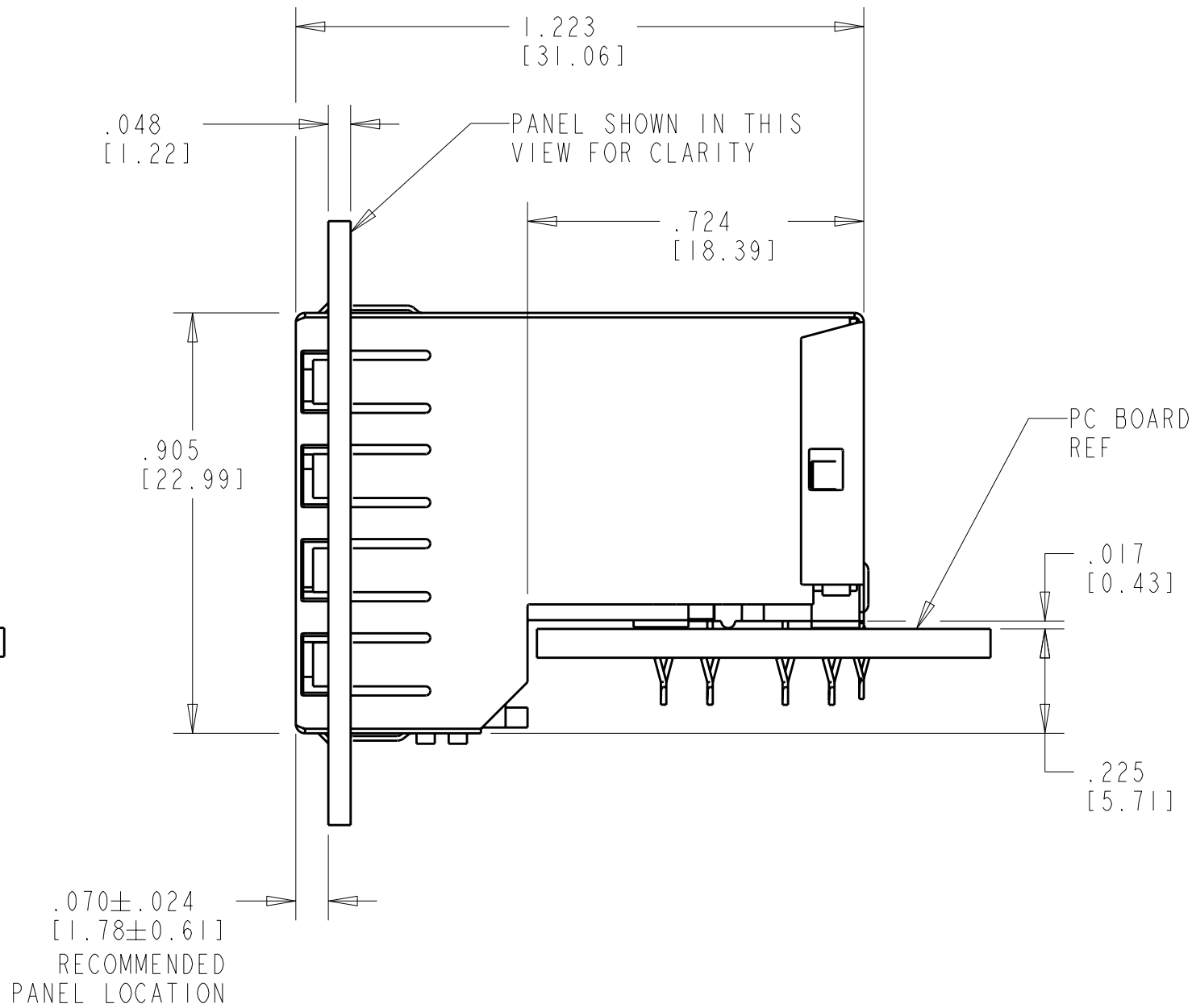
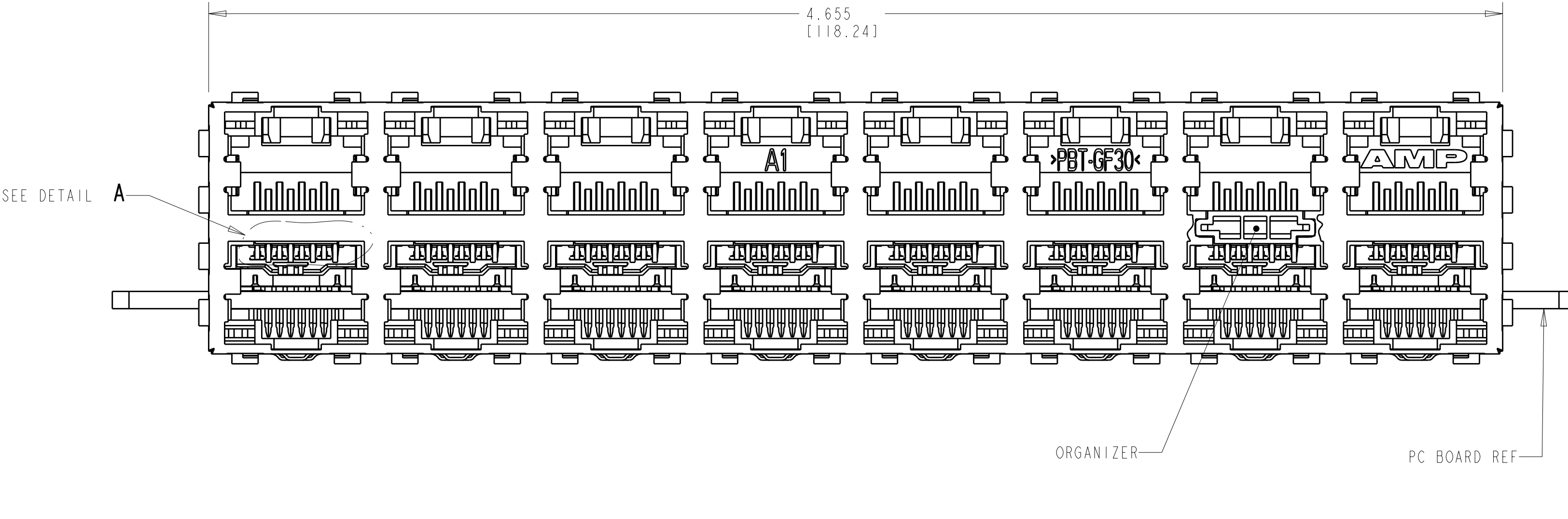
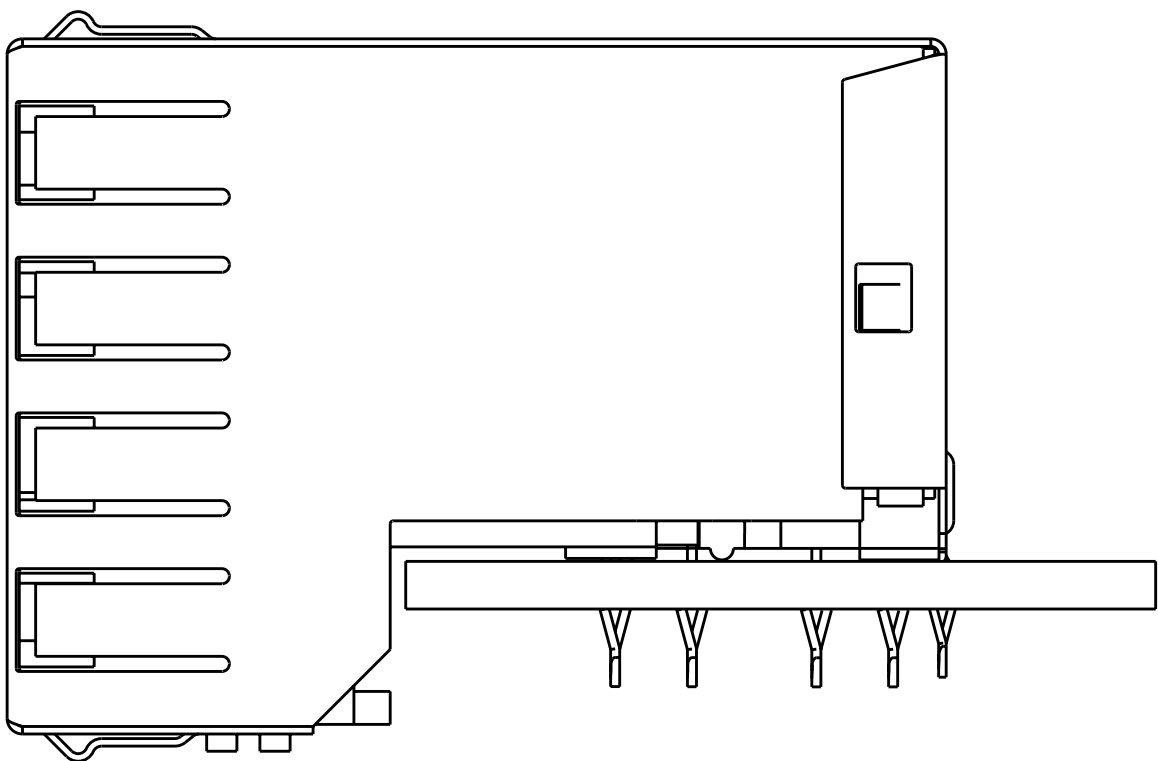


LOC		DIST		REVISIONS						
AA		22		P	LTR	DESCRIPTION	DATE	DWN	APVD	
					A	REVISED PER EC 0S11-0201-04	23JUN2005	LV	SF	
					A1	REVISED PER ECO-09-025699	12DEC09	KK	AEQ	

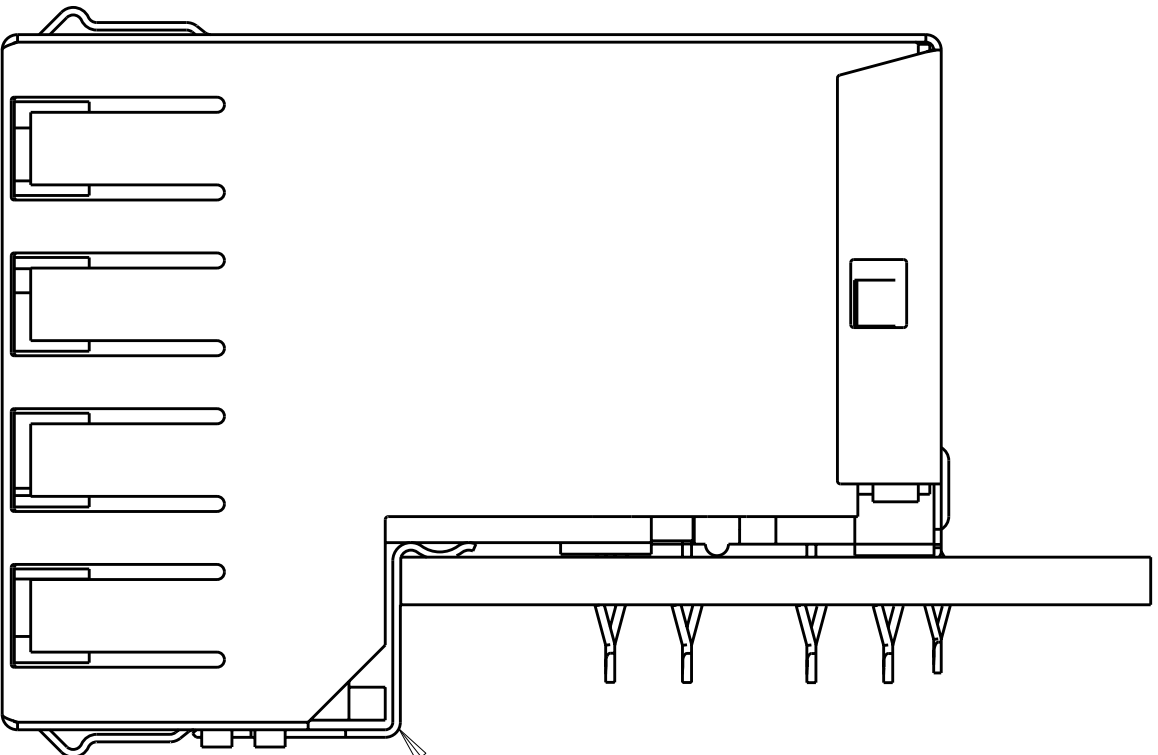


1. HOUSING: PBT POLYESTER, BLACK, UL 94V-0.
TERMINALS: 0.30 THICK PHOS BRONZE PLATED WITH 1.27µm MINIMUM THICK HARD GOLD IN LOCALIZED AREA AND 2µm MINIMUM THICK MATTE TIN IN ACTION PIN COMPLIANT PIN TERMINAL AREA OVER 1.27µm MINIMUM THICK NICKEL UNDERPLATE.
SHIELD: 0.25 THICK COPPER ALLOY PLATED WITH 2.03µm MINIMUM BRIGHT TIN OVER 1.27µm MINIMUM NICKEL.
GROUND CLIPS: 0.25 THICK COPPER ALLOY PLATED WITH 2.03µm MINIMUM MATTE TIN OVER 1.27µm MINIMUM NICKEL.
2. JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS, PART 68 SUBPART F.
3. ALL DIMENSIONS ARE NOMINAL UNLESS OTHERWISE SPECIFIED.
4. DRILLED HOLE DIA = .040±.001.
COPPER THICKNESS = .001-.003.
TIN THICKNESS = .0003 MIN.
5. DATUMS AND BASIC DIMENSIONS ESTABLISHED BY CUSTOMER.
6. THESE FOUR SIDE PCB ACTION PIN GROUNDS AND CORRESPONDING PCB HOLES ARE REQUIRED ONLY ON 6116689-3.
7. DATE CODE LOCATED ON REAR OF PART APPROXIMATELY AS SHOWN.
8. OBSOLETE PARTS: OBSOLETE CIS STREAMLINING PER D.RENAUD/D.SINISI

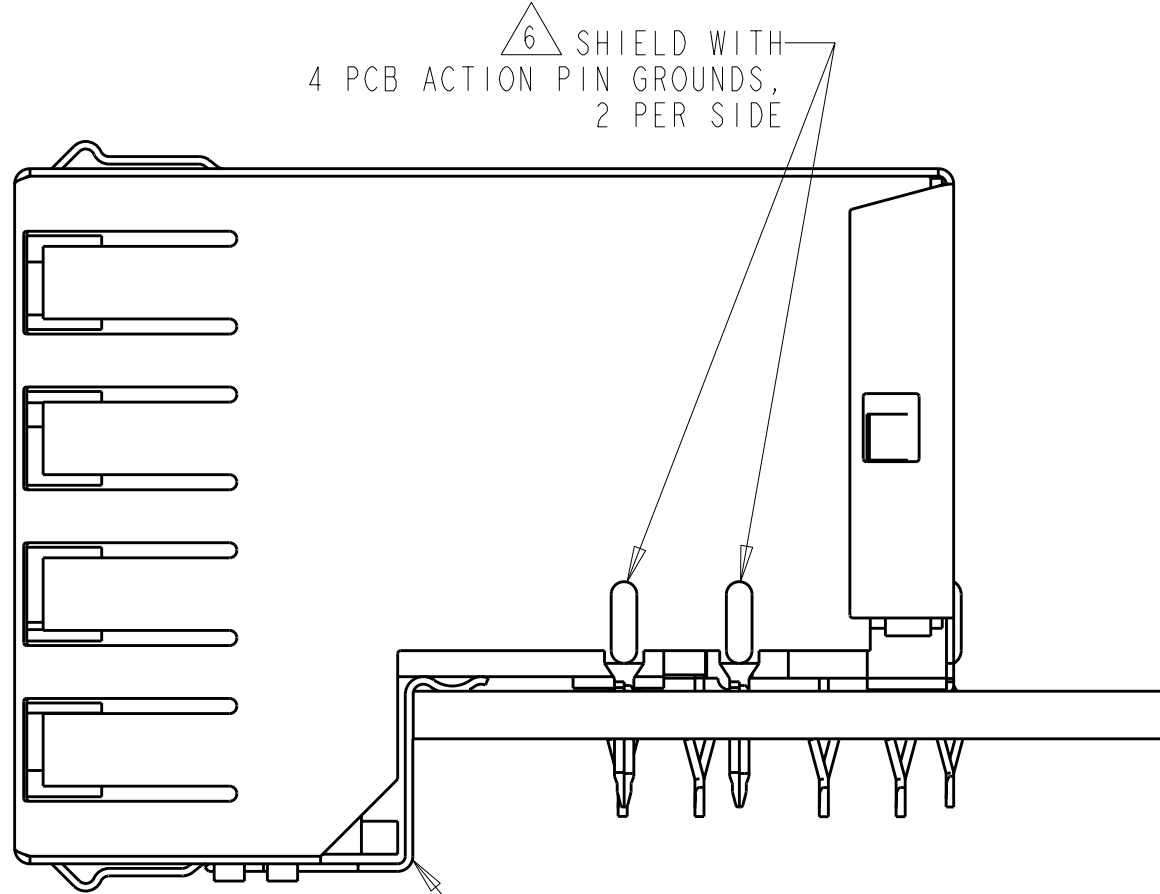
P/N 6116689-1 SHOWN



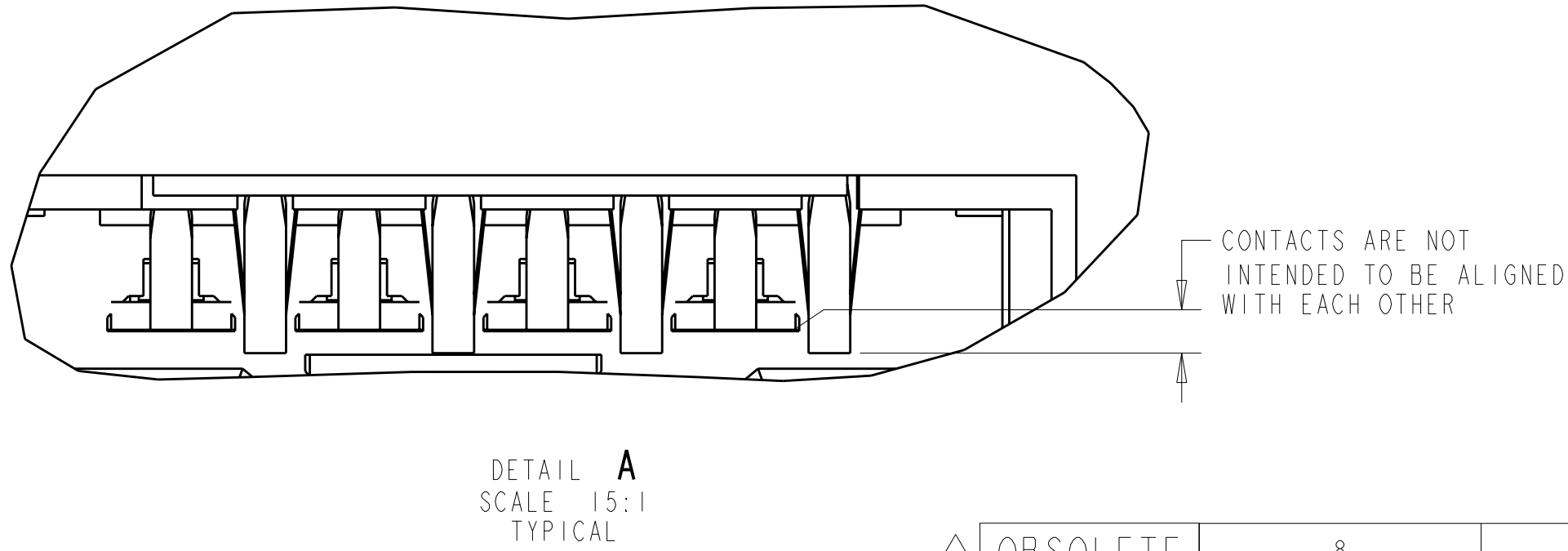
P/N 6116689-1
SCALE 4:1



P/N 6116689-4
SCALE 4:1



P/N 6116689-3
SCALE 4:1

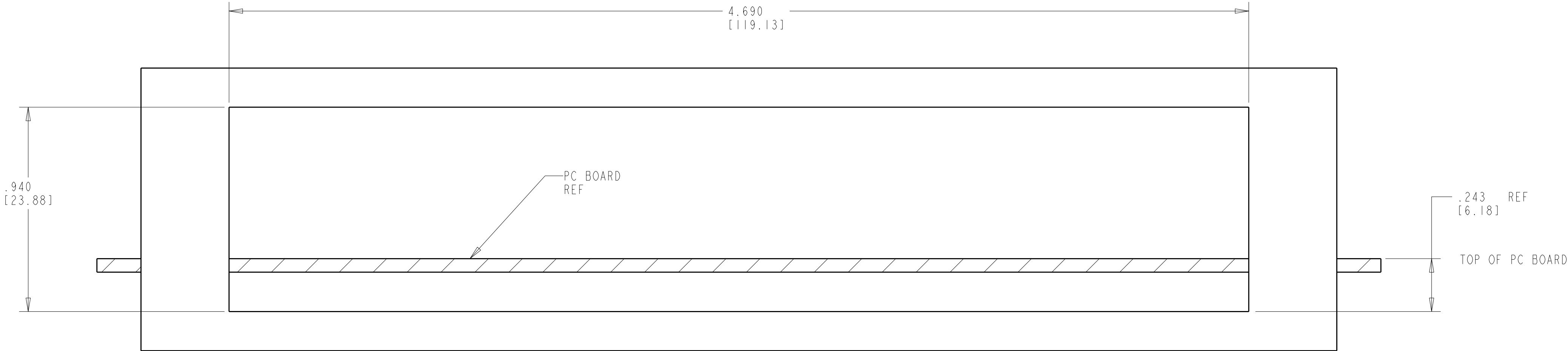


DETAIL A
SCALE 15:1
TYPICAL

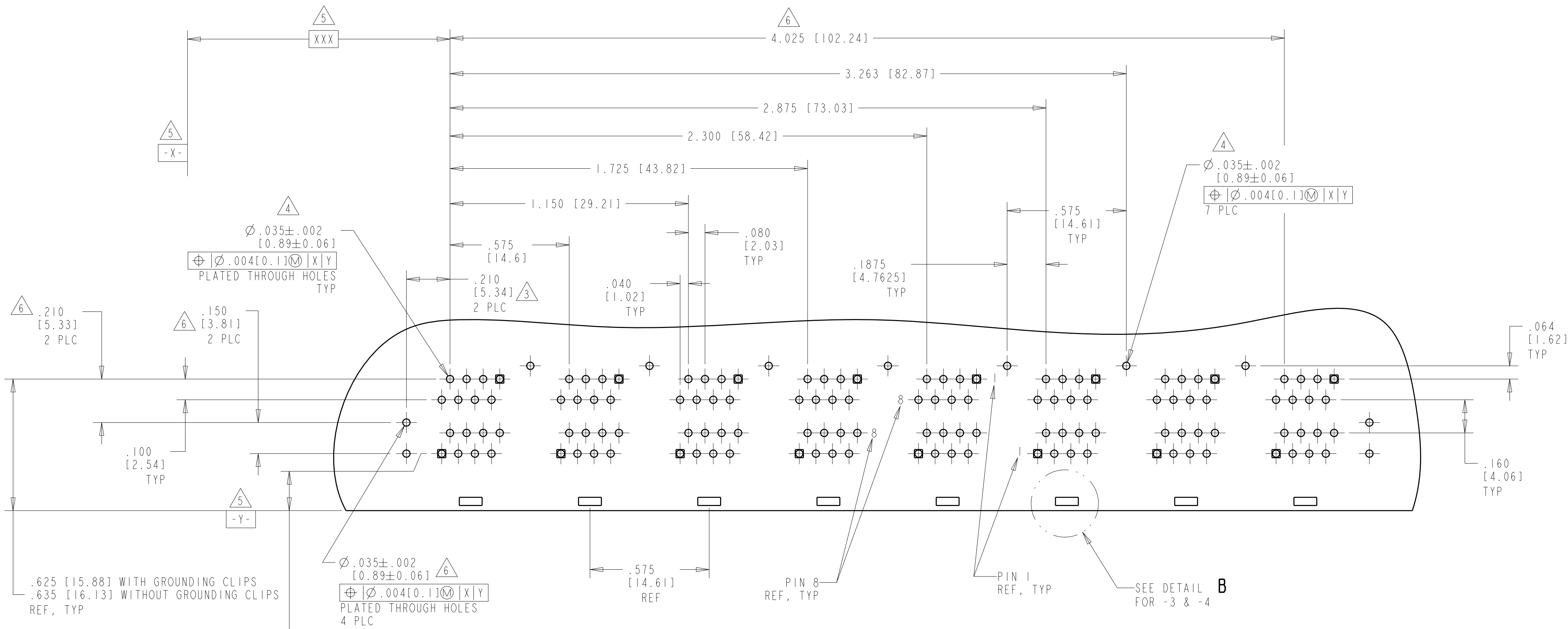
8	OBSOLETE		PART NUMBER
	8	NONE	
	8	4	
	NONE	NONE	
GROUND CLIPS		SIDE PCB ACTION PIN GNDS	

DIMENSIONS: INCHES [mm]		TOLERANCES UNLESS OTHERWISE SPECIFIED:		DWN 23JUN2005 CHK J. VARELA - DOCKS J. WESTMAN APVD S. FLICKINGER		Tyco Electronics Harrisburg, PA 17105-3608	
9 PLC ±		±		PRODUCT SPEC		NAME	
1 PLC ±		±		108-1854		STACKED MODULAR JACK ASSEMBLY, 2 X 8, 8 POSITION, SHIELDED, PANEL GROUND, OFFSET, CAT 5	
2 PLC ±		±		APPLICATION SPEC		SIZE	
3 PLC ±		±		114-2160		CAGE CODE	
4 PLC ±		±		WEIGHT		DRAWING NO	
ANGLES		FINISH		CUSTOMER DRAWING		A1 00779	
						C=6116689	
						RESTRICTED TO	
						SCALE 3:1	
						SHEET 1 OF 2	
						REV A1	

LOC		DIST		REVISONS			
AA	22	P	LYR	DESCRIPTION	DATE	DWN	APVD
		-	-	SEE SHEET 1	-	-	-

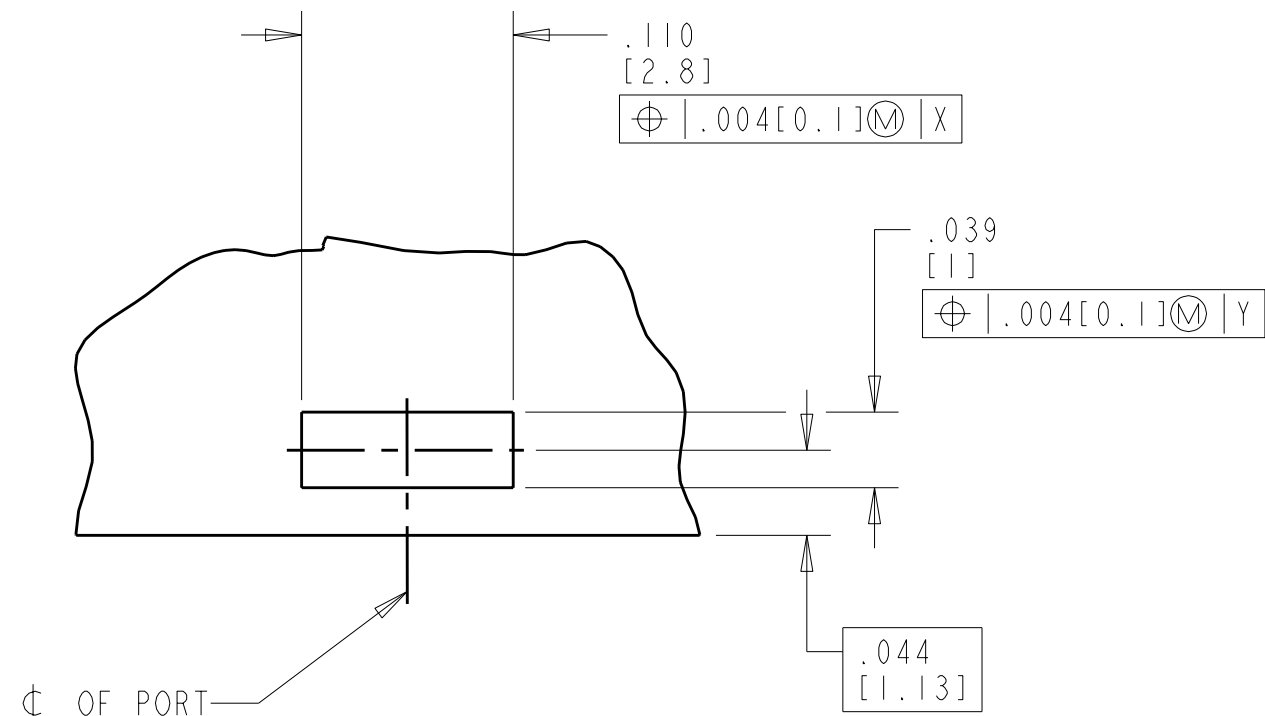


RECOMMENDED PANEL CUT-OUT



PC BOARD LAYOUT
(COMPONENT SIDE)

PARTS WITH GROUNDING CLIPS = .265[6.73]
PARTS WITHOUT GROUNDING CLIPS = .275[6.99]



DETAIL B
THESE PLATED PADS REQUIRED FOR 6116689-3 & -4
PRODUCTS WITH GROUNDING CLIPS
SCALE 10:1
8 PLACES

TYCO ELECTRONICS CORPORATION 17105-3608 HARRISBURG, PA 17105-3608		DWN L. VARELA - 23JUN2005 CHK J. WESTMAN - 23JUN2005 APVD S. FLICKINGER - 23JUN2005		NAME STACKED MODULAR JACK ASSEMBLY, 2 X 8, 8 POSITION, SHIELDED, PANEL GROUND, OFFSET, CAT 5	
DIMENSIONS: INCHES [mm]		TOLERANCES UNLESS OTHERWISE SPECIFIED:		SIZE A1	
9 PLC 8 PLC 7 PLC 6 PLC 5 PLC 4 PLC 3 PLC 2 PLC 1 PLC 0 PLC		±.010 ±.010 ±.010 ±.010 ±.010 ±.010 ±.010 ±.010 ±.010 ±.010		CAGE CODE 00779	
MATERIAL -		FINISH -		DRAWING NO. 6116689	
CUSTOMER DRAWING		SCALE 3:1		SHEET 2 OF 2	
				REV A1	