

TOSHIBA Schottky Barrier Diode

CRS12

Switching Mode Power Supply Applications
(Output voltage: ≤ 12 V)

DC/DC Converter Applications

- Forward voltage: $V_{FM} = 0.58$ V (max)
- Average forward current: $I_F (AV) = 1.0$ A
- Repetitive peak reverse voltage: $V_{RRM} = 60$ V
- Suitable for compact assembly due to small surface-mount package
“S-FLAT™” (Toshiba package name)

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Rating	Unit
Repetitive peak reverse voltage	V_{RRM}	60	V
Average forward current	$I_F (AV)$	1.0 (Note 1)	A
Non-repetitive peak surge current	I_{FSM}	20 (50 Hz)	A
Junction temperature	T_j	-40~150	$^\circ\text{C}$
Storage temperature range	T_{stg}	-40~150	$^\circ\text{C}$

Note 1: $T_a = 73^\circ\text{C}$

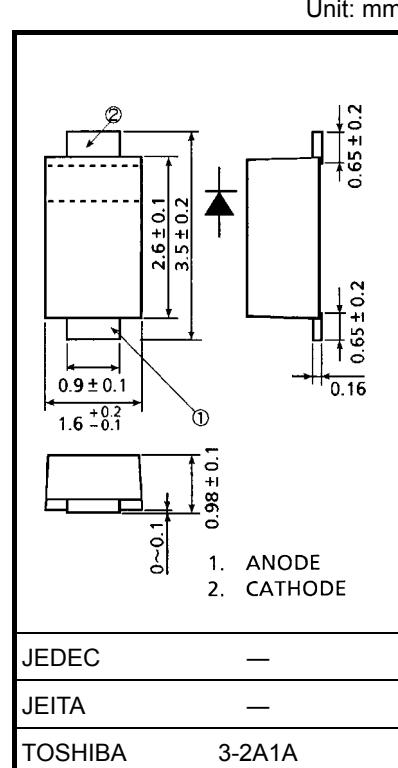
Device mounted on a ceramic board

Board size: 50 mm \times 50 mmSoldering size: 2 mm \times 2 mm

Board thickness: 0.64 t

Rectangular waveform ($\alpha = 180^\circ$), $V_R = 30$ V

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).



Weight: 0.013 g (typ.)

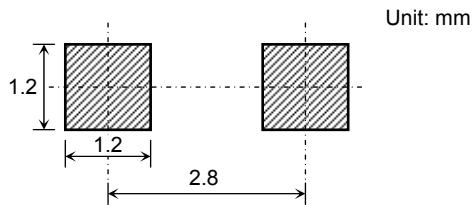
Electrical Characteristics ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Peak forward voltage	V_{FM} (1)	$I_{FM} = 0.7$ A (pulse test)	—	0.48	—	V
	V_{FM} (2)	$I_{FM} = 1.0$ A (pulse test)	—	0.52	0.58	
Peak repetitive reverse current	I_{RRM} (1)	$V_{RRM} = 5$ V (pulse test)	—	0.4	—	μA
	I_{RRM} (2)	$V_{RRM} = 60$ V (pulse test)	—	4.0	100	
Junction capacitance	C_j	$V_R = 10$ V, $f = 1.0$ MHz	—	40	—	pF
Thermal resistance (junction to ambient)	$R_{th(j-a)}$	Device mounted on a ceramic board (board size: 50 mm \times 50 mm) (soldering land: 2 mm \times 2 mm) (board thickness: 0.64 t)	—	—	70	$^\circ\text{C/W}$
		Device mounted on a glass-epoxy board (board size: 50 mm \times 50 mm) (soldering land: 6 mm \times 6 mm) (board thickness: 1.6 t)	—	—	140	
		Device mounted on a glass-epoxy board (board size: 50 mm \times 50 mm) (soldering land: 1.2 mm \times 1.2 mm) (board thickness: 1.6 t)	—	—	240	
Thermal resistance (junction to lead)	$R_{th(j-l)}$	—	—	—	20	$^\circ\text{C/W}$

Marking

Abbreviation Code	Part No.
SB	CRS12

Standard Soldering Pad



Handling Precaution

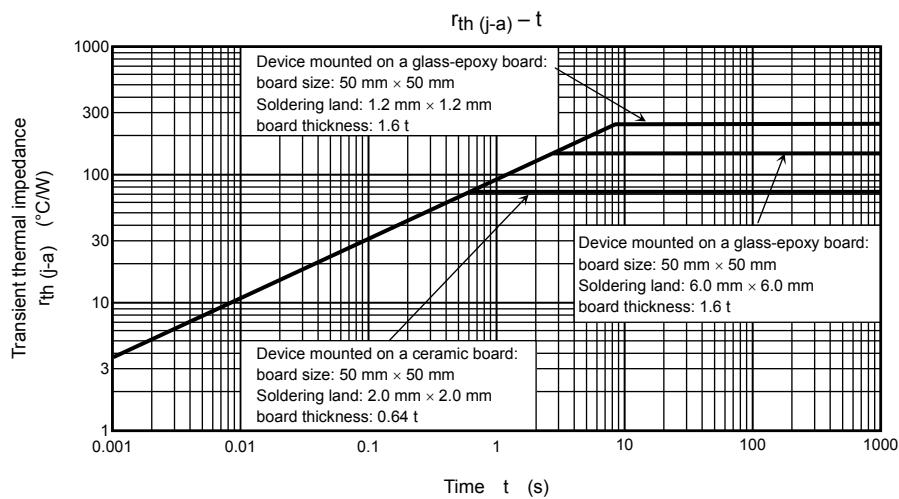
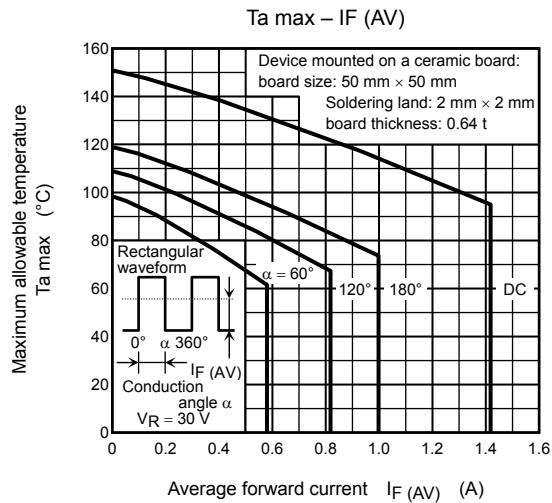
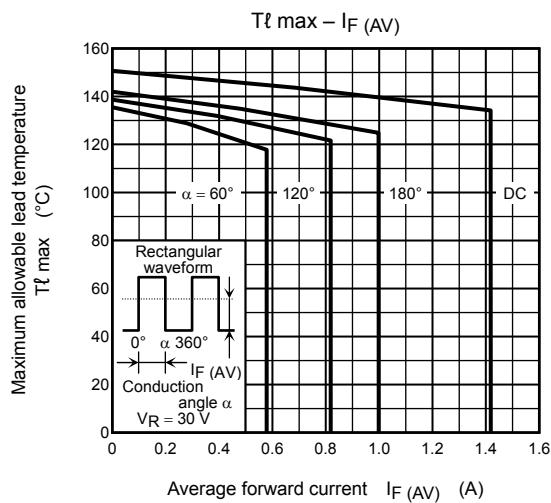
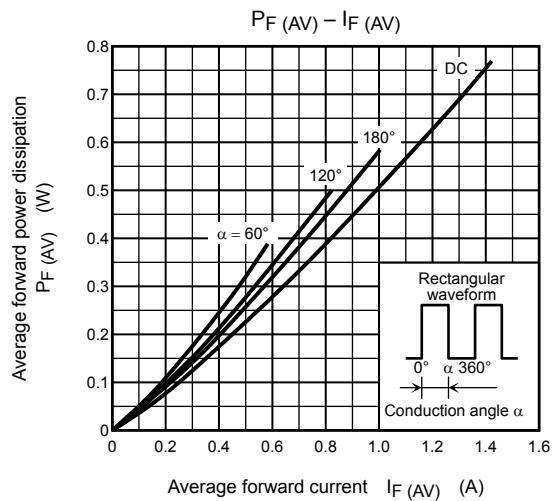
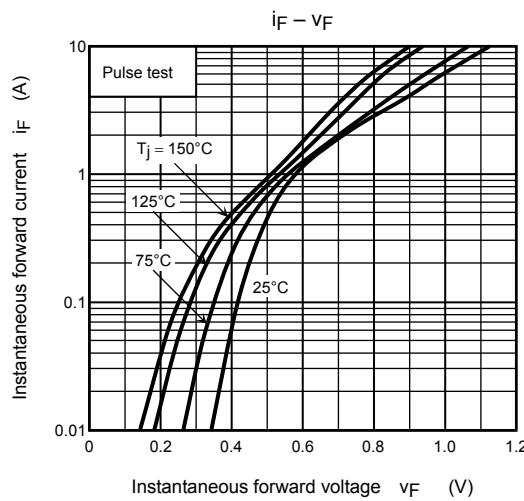
- 1) Schottky barrier diodes have reverse current characteristic compared to other diodes. There is a possibility SBD may cause thermal runaway when it is used under high temperature or high voltage. Please take forward and reverse loss into consideration during design.
- 2) The absolute maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The followings are the general derating method that we recommend when you design a circuit with a device.

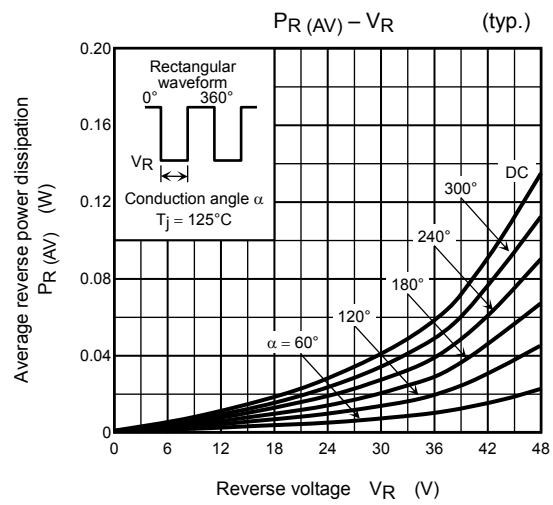
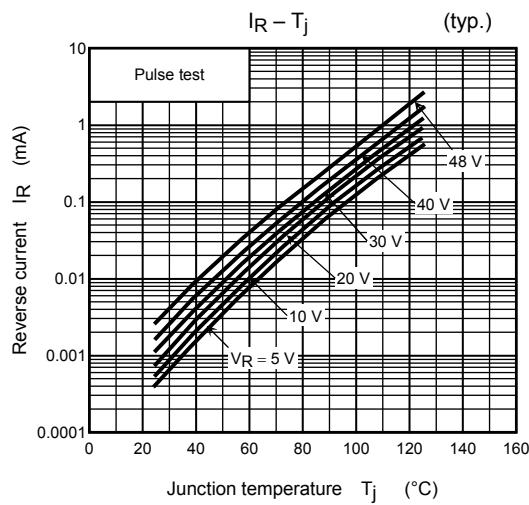
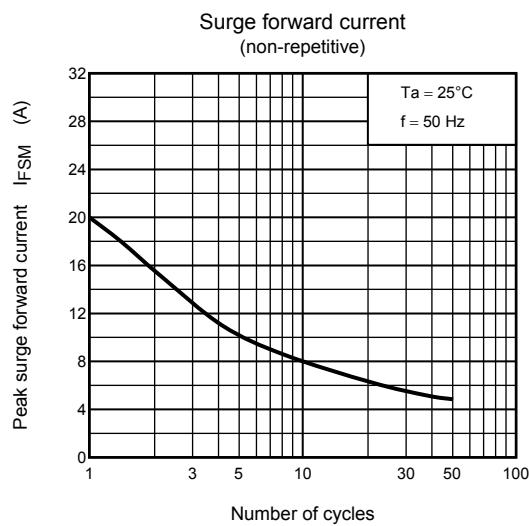
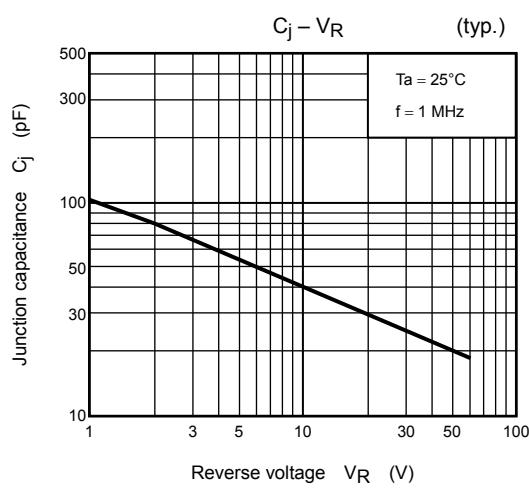
V_{RRM}: Use this rating with reference to (1) above. V_{RRM} has a temperature coefficient of (0.1%/°C). Take this temperature coefficient into account designing a device at low temperature.

I_F(AV): We recommend that the worst case current be no greater than 80% of the absolute maximum rating of I_F(AV) and T_j below 120°C. When using this device, take the margin into consideration by using an allowable T_{max}-I_F(AV) curve.

I_{FSM}: This rating specifies the non-repetitive peak current. This is only applied for an abnormal operation, which seldom occurs during the lifespan of the device.

T_j: Derate this rating when using a device in order to ensure high reliability. We recommend that a device be used at a T_j of below 120°C.
- 3) Thermal resistance between junction and ambient fluctuates depending on the device's mounting condition. When using a device, design a circuit board and a soldering land size to match the appropriate thermal resistance value.
- 4) Please refer to the Rectifiers databook for further information.





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