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SN74LVC861

Status: OBSOLETE

10-Bit Bus Transceiver With 3-State Outputs



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Technical Documents

Application Notes

- [Selecting the Right Level Translation Solution \(Rev. A\)](#) (scea035a.htm, 8 KB)
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- [Texas Instruments Little Logic Application Report](#) (scea029.htm, 8 KB)
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- [16-Bit Widebus Logic Families in 56-Ball, 0.65-mm Pitch Very Thin Fine-Pitch BGA \(Rev. B\)](#) (szza029b.htm, 8 KB)
22 May 2002 [Abstract](#)
- [Power-Up 3-State \(PU3S\) Circuits in TI Standard Logic Devices](#) (szza033.htm, 9 KB)
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- [Selecting the Right Texas Instruments Signal Switch](#) (szza030.htm, 8 KB)
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- [Migration From 3.3-V To 2.5-V Power Supplies For Logic Devices](#) (scea005.htm, 9 KB)
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- [Live Insertion](#) (sdya012.htm, 8 KB)
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- [Input and Output Characteristics of Digital Integrated Circuits](#) (sdya010.htm, 9 KB)
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- [Understanding Advanced Bus-Interface Products Design Guide](#) (scaa029.pdf, 253 KB)
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User Guides

- [LOGIC Pocket Data Book \(Rev. B\)](#) (scyd013b.pdf, 6001 KB)
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- [Signal Switch Data Book \(Rev. A\)](#) (scdd003a.pdf, 18396 KB)
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- [LVC and LV Low-Voltage CMOS Logic Data Book \(Rev. B\)](#) (scbd152b.pdf, 13291 KB)
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More Literature

- [Communications Infrastructure Solutions Guide 1Q2008 \(Rev. G\)](#) (sstc001g.pdf, 1726 KB)
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- [Logic Selection Guide 2007 \(Rev. Z\)](#) (sdyu001z.pdf, 2862 KB)
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