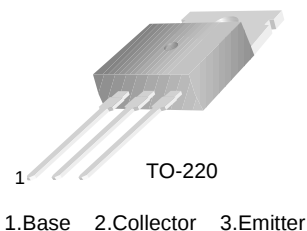


BDW24/A/B/C

Hammer Drivers, Audio Amplifiers Applications

- Power Darlington TR
- Complement to BDW23, BDW23A, BDW23B and BDW23C respectively



PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage		
	: BDW24	- 45	V
	: BDW24A	- 60	V
	: BDW24B	- 80	V
	: BDW24C	- 100	V
V_{CEO}	Collector-Emitter Voltage		
	: BDW24	- 45	V
	: BDW24A	- 60	V
	: BDW24B	- 80	V
	: BDW24C	- 100	V
V_{EBO}	Emitter-Base Voltage	- 5	V
I_C	Collector Current (DC)	- 6	A
I_{CP}	*Collector Current (Pulse)	- 8	A
I_B	Base Current	- 0.2	A
P_C	Collector Dissipation ($T_C=25^\circ\text{C}$)	50	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature	- 65 ~ 150	$^\circ\text{C}$

Electrical Characteristics $T_C=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
$V_{CEO(sus)}$	* Collector-Emitter Sustaining Voltage : BDW24 : BDW24A : BDW24B : BDW24C	$I_C = -100\text{mA}, I_B = 0$	- 45 - 60 - 80 - 100			V V V V
I_{CBO}	Collector Cut-off Current : BDW24 : BDW24A : BDW24B : BDW24C	$V_{CB} = -45\text{V}, I_E = 0$ $V_{CB} = -60\text{V}, I_E = 0$ $V_{CB} = -80\text{V}, I_E = 0$ $V_{CB} = -100\text{V}, I_E = 0$			- 200 - 200 - 200 - 200	μA μA μA μA
I_{CEO}	Collector Cut-off Current : BDW24 : BDW24A : BDW24B : BDW24C	$V_{CE} = -22\text{V}, I_B = 0$ $V_{CE} = -30\text{V}, I_B = 0$ $V_{CE} = -40\text{V}, I_B = 0$ $V_{CE} = -50\text{V}, I_B = 0$			- 500 - 500 - 500 - 500	μA μA μA μA
I_{EBO}	Emitter Cut-off Current	$V_{EB} = -5\text{V}, I_C = 0$			- 2	mA
h_{FE}	* DC Current Gain	$V_{CE} = -3\text{V}, I_C = -1\text{A}$ $V_{CE} = -3\text{V}, I_C = -2\text{A}$ $V_{CE} = -3\text{V}, I_C = -6\text{A}$	1000 750 100		20000	
$V_{CE(sat)}$	* Collector-Emitter Saturation Voltage	$I_C = -2\text{A}, I_B = -8\text{mA}$ $I_C = -6\text{A}, I_B = -60\text{mA}$			- 2 - 3	V V
$V_{BE(sat)}$	* Base-Emitter Saturation Voltage	$I_C = -2\text{A}, I_B = -8\text{mA}$			- 2.5	V
$V_{BE(on)}$	* Base-Emitter ON Voltage	$V_{CE} = -3\text{V}, I_C = -1\text{A}$ $V_{CE} = -3\text{V}, I_C = -6\text{A}$			- 2.5 - 3	V V
V_F	* Parallel Diode Forward Voltage	$I_F = -2\text{A}$			- 1.8	V

* Pulse Test: PW=300 μs , duty Cycle =1.5% Pulsed

Typical Characteristics

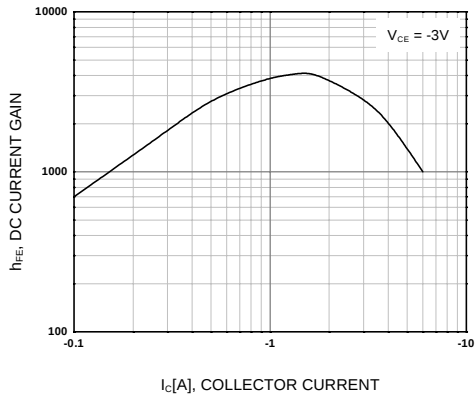


Figure 1. DC current Gain

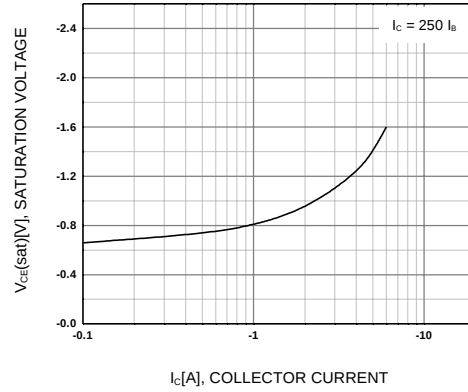


Figure 2. Collector-Emitter Saturation Voltage

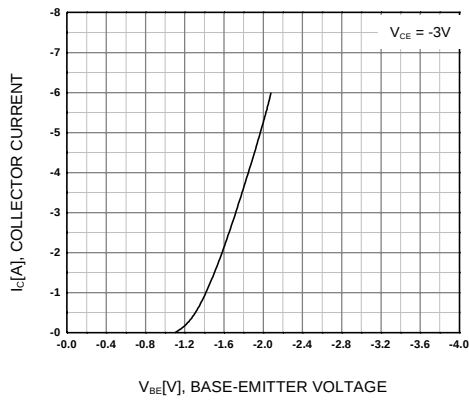


Figure 3. Base-Emitter On Voltage

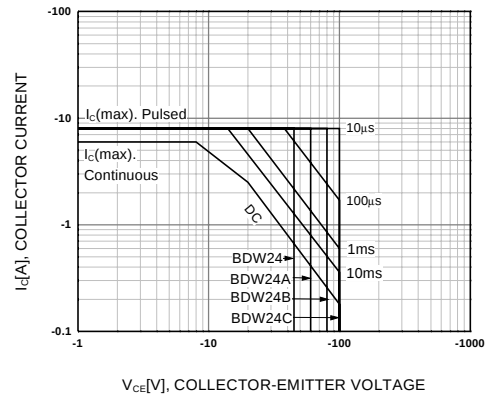


Figure 4. Safe Operating Area

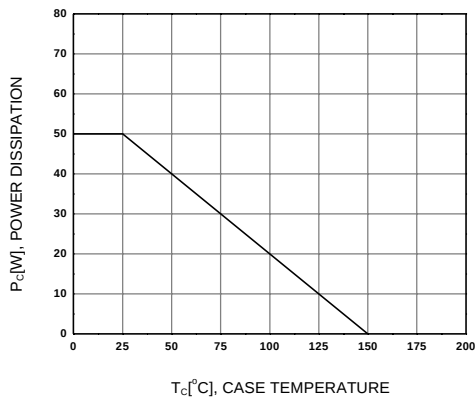


Figure 5. Power Derating

BDW24/A/B/C

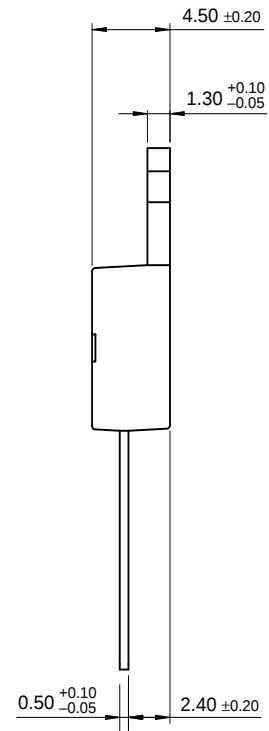
Technical drawing of a 3-pin connector showing front and top views with dimensions.

Front View Dimensions:

- Overall Width: 9.90 ± 0.20
- Overall Height: 18.95 MAX.
- Top Section Height: 2.80 ± 0.10
- Bottom Section Height: 15.90 ± 0.20
- Pin Section Height: 13.08 ± 0.20
- Pin Diameter: 2.54 TYP [2.54 ± 0.20]
- Pin Spacing: 1.27 ± 0.10 and 1.52 ± 0.10
- Pin Angle: 45°
- Pin Length: 10.08 ± 0.30
- Internal Hole Diameter: $\varnothing 3.60 \pm 0.10$
- Internal Hole Position: (8.70) from the top edge.
- Internal Hole Position: (3.70) from the bottom edge.
- Internal Hole Position: (3.00) from the pin section.
- Internal Hole Position: (1.46) from the pin section.
- Internal Hole Position: (1.70) from the top edge.
- Internal Hole Position: 1.30 ± 0.10 from the top edge.

Top View Dimensions:

- Overall Width: 10.00 ± 0.20
- Pin Diameter: 2.54 TYP [2.54 ± 0.20]
- Pin Spacing: 0.80 ± 0.10



©2000 Fairchild Semiconductor International

Rev. A, February 2000

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™
Bottomless™
CoolFET™
CROSSVOLT™
E²CMOS™
FACT™
FACT Quiet Series™
FAST®
FASTr™
GTO™

HiSeC™
ISOPLANAR™
MICROWIRE™
POP™
PowerTrench®
QFET™
QS™
Quiet Series™
SuperSOT™-3
SuperSOT™-6

SuperSOT™-8
SyncFET™
TinyLogic™
UHC™
VCX™

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR INTERNATIONAL.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.