TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

2SK2679

Chopper Regulator, DC-DC Converter and Motor Drive Applications

Low drain-source ON resistance : R_{DS} (ON) = 0.84 Ω (typ.)

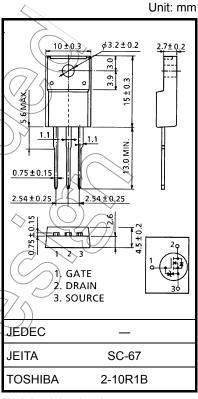
High forward transfer admittance : |Y_{fs}| = 4.4 S (typ.)

Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 400 V)

• Enhancementmode : V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	400	A
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	400	y
Gate-source voltage		V_{GSS}	±30	> V
Drain current	DC (Note 1)	I _D	5.5	Α
	Pulse (Note 1)	I _{DP}	22	A
Drain power dissipation	n (Tc = 25°C)	P _D <	35	W
Single pulse avalanche	e energy (Note 2)	EAS	223)
Avalanche current		IAR	5.5	Α
Repetitive avalanche e	nergy (Note 3)	(EAR \	3.5	mJ
Channel temperature		Tch	150	7,¢
Storage temperature ra	ange ((√T _{stg}	-55 to 150	%C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	3.57	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 12 mH, R_G = 25 Ω , I_{AR} = 5.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

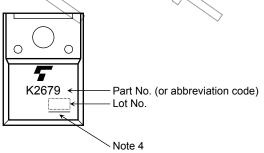
Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I_{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V _(BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 400 V, V _{GS} = 0 V	//	_	100	μΑ
Drain-source br	eakdown voltage	V _{(BR) DSS}	I _D = 10 mA, V _{GS} = 0 V	400		_	V
Gate threshold v	oltage/	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0) >_	4.0	V
Drain-source O	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 3 A	<u> </u>	0.84	1.2	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 3 A	2.0	4.4	_	S
Input capacitano	e	C _{iss}		_	720	_	
Reverse transfe	r capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	, —	80	_	pF
Output capacitance		C _{oss}			250	_	
Switching time	Rise time	t _r	V _{GS} ov I _D = 2A V _{out}	- (15	>	
	Turn-on time	t _{on}	$R_L = 100\Omega$		30) _	ns
	Fall time	t _f	V _{DD} =200V		25	_	115
	Turn-off time	t _{off}	Duty ≤1%, t _w =10μs) –	110	_	
Total gate charg plus gate-drain)		Qg			17	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 320 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 5.5 \text{ A}$		10		nC
Gate-drain ("mil	ler") Charge	Qgd		_	7	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	5.5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	22	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 5.5 Å, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 5.5 A, V _{GS} = 0 V	_	350	_	ns
Reverse recovery charge	Qrr	dI _{DR} / dt = 100 Å / μs	_	2.1	_	μC



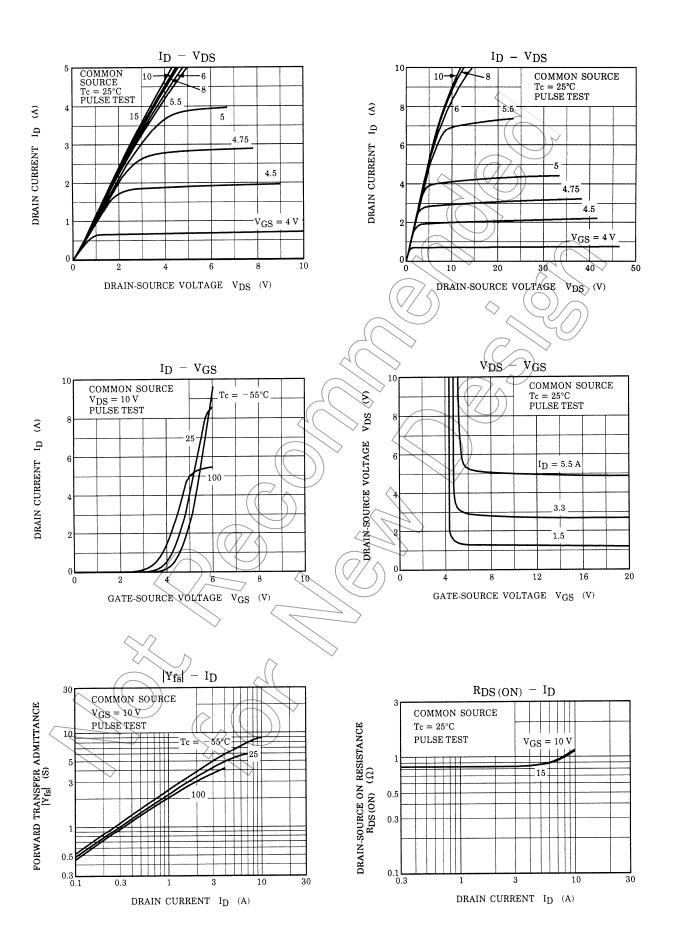


Note 4: A line under a Lot No. identifies the indication of product Labels.

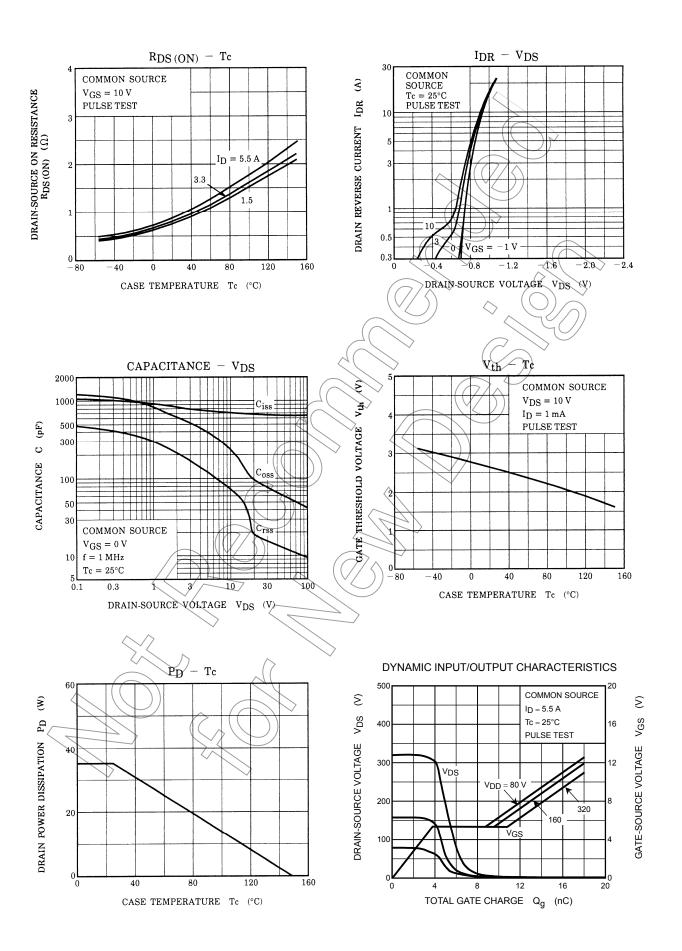
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

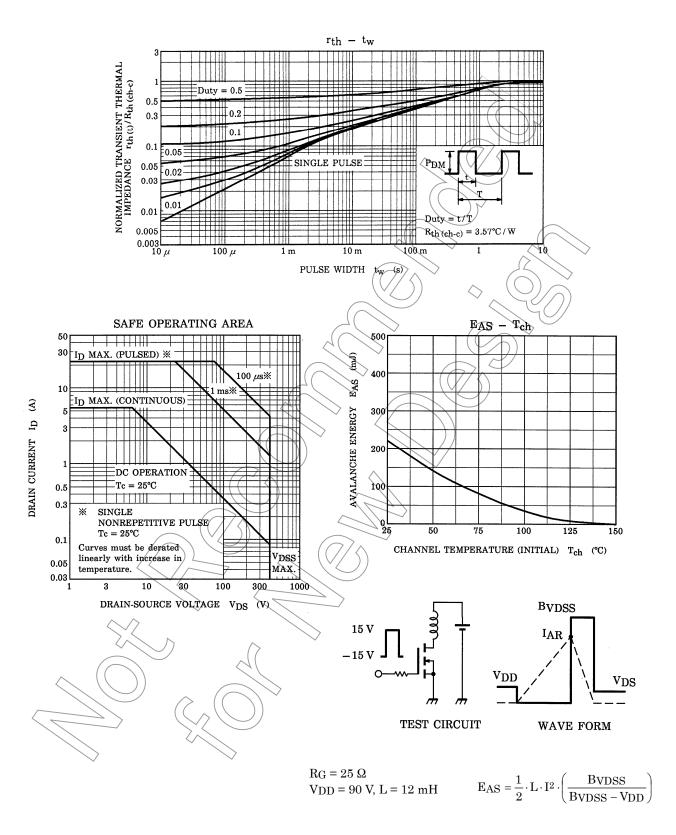
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