

Status: **Active**

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Low Skew, 1-To-2, Crystal Oscillator/LVCMOS-To-3.3V LVPECL Fanout Buffer

The 8532I-31 is a low skew, high performance 1-to-2 3.3V Crystal Oscillator/LVCMOS-to-3.3V LVPECL fanout buffer and a member of the HiPerClockS™ family of High Performance Clock Solutions from IDT. The 8532I-31 has selectable single ended clock or crystal inputs. The single ended clock input accepts LVCMOS or LVTTTL input levels and translate them to 3.3V LVPECL levels. The output enable is internally synchronized to eliminate runt pulses on the outputs during asynchronous assertion/deassertion of the clock enable pin. Guaranteed output and part-to-part skew characteristics make the 8532I-31 ideal for those applications demanding well defined performance and repeatability.

- Two differential 3.3V LVPECL outputs
- Selectable LVCMOS/LVTTL CLK or crystal inputs
- CLK can accept the following input levels: LVCMOS, LVTTL
- Maximum output frequency: 266MHz
- Output skew: TBD
- Part-to-part skew: TBD
- Propagation delay: TBD
- Additive phase jitter, RMS: TBD
- 3.3V operating supply
- -40°C to 85°C ambient operating temperature
- Available in standard (RoHS 5) and lead-free (RoHS 6) packages

ADDITIVE PHASE JITTER RMS (PS)	OUTPUT SKEW (PS)	CORE VOLTAGE (V)	INPUTS (#)	INPUT SIGNAL	INPUT FREQ (MHZ)	OUTPUT VOLTAGE (V)	OUTPUTS (#)	OUTPUT TYPE	OUTPUT FREQ RANGE (MHZ)
		3.3	2	LVC MOS, Crystal	0.000000 - 266.000000	3.3	2	LVPECL	0.000000 - 266.000000

ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
8532AKI-31LF	Active	NLG20	VFQFPN	20	I	Y	Check Inventory

There are no public documents available for this product. [Click here to request documentation.](#)

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