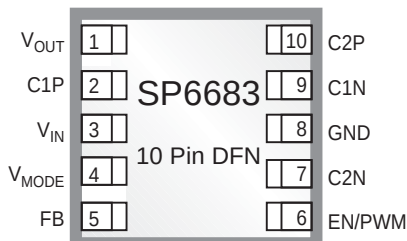


1.5x Charge Pump LED Driver for Parallel Configuration

FEATURES

- 200mA Output Using 1 μ F Ceramic Caps
- Drives up to 8 white LED's
- 0.5 μ A Shutdown Current
- 1x/1.5x Mode Charge Pump
- +2.7V to +5.5V Input Voltage Range
- Built-in 1.2MHz Oscillator
- 0.5mA Quiescent Current
- Programmable Output Current or Voltage
- PWM Dimming Control via Enable Pin
- Soft Start to limit In-Rush Current
- Space Saving 10-pin 3 x 3mm DFN Package
- Three Options for Optimizing Efficiency:
 - SP6683 - MOSFET discharge in shutdown
 - SP6683-03 - High Impedance in shutdown
 - SP6683-LV - Low Voltage (150mV)V_{FB} Reference



Now Available in Lead Free Packaging

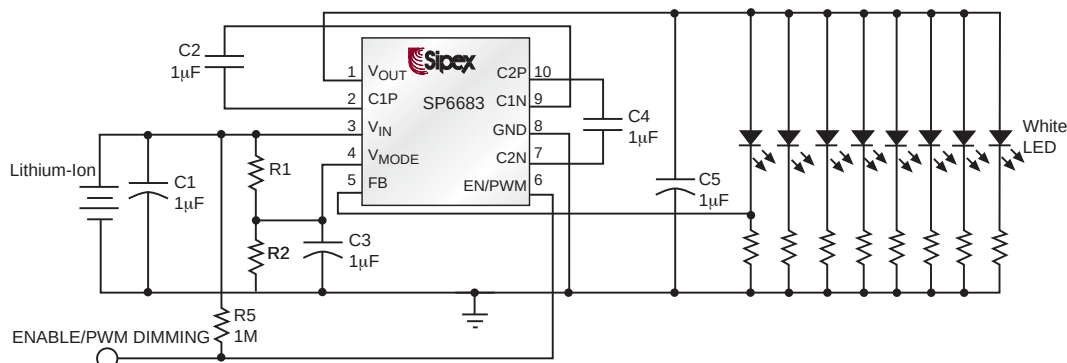
APPLICATIONS

- Mobile Phone
- Camera Flash Driver
- PDA
- Digital Still Camera
- Digital Camcorder
- Color LCD Module

DESCRIPTION

The SP6683 is a high power current regulated charge pump ideal for converting a Li-Ion battery input for driving up to 8 white LED's used in backlighting color displays. The SP6683 operates with an internal 1.2MHz clock, enabling the use of small external components. Output current can be accurately regulated by modulating the switcher between the charge pump and output capacitor. In shutdown mode, the SP6683 discharges the output to ground and draws less than 500nA current. The SP6683 utilizes 1 μ F capacitors to deliver up to 200mA current regulated WLED drive capability. The SP6683 family includes three options that allow the user to optimize LED driving circuit; SP6683-LV with low voltage reference, SP6683-03 with high impedance output in shutdown, and SP6683 with pulled down output in shutdown. The SP6683 is offered in 10-pin DFN package.

TYPICAL APPLICATION SCHEMATIC



ABSOLUTE MAXIMUM RATINGS

V_{IN} , V_{MODE} , V_{OUT} and EN/PWM -0.3V to 6V
 Output Current (I_{OUT}) 300mA
 Power Dissipation per Package - 10-pin DFN
 (derate 8.84mW/°C above +70°C) 1.2W
 Junction Temperature +125°C
 Storage Temperature -65°C to +150°C
 ESD Rating. 2kV HBM

These are stress ratings only and functional operation of the device at these ratings or any other above those indicated in the operation sections of the specifications below is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

ELECTRICAL CHARACTERISTICS

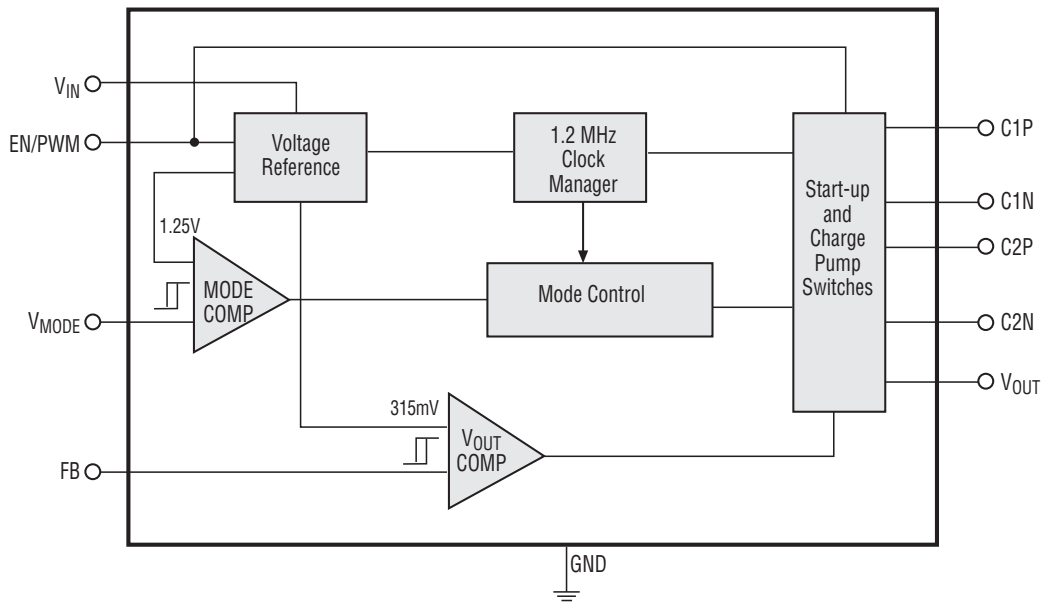
Unless otherwise specified: $V_{IN} = 3.6V$, $C1=C2=C4=C5=1.0\mu F$ (Ceramic, ESR=0.03 Ω) and $T_{AMB} = 25^\circ$. The ♦ denotes specifications which apply over full operating temperature range -40°C to +85°C.

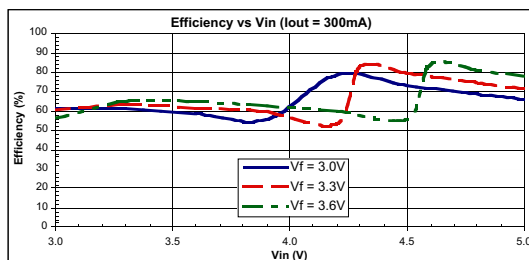
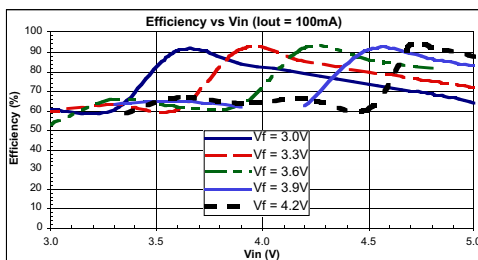
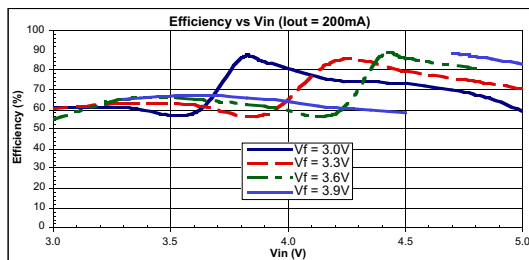
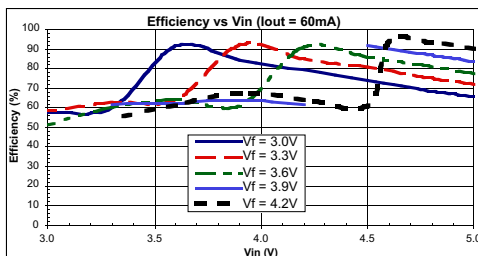
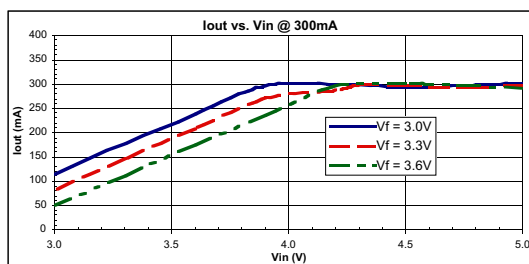
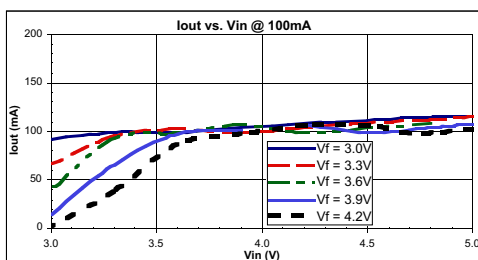
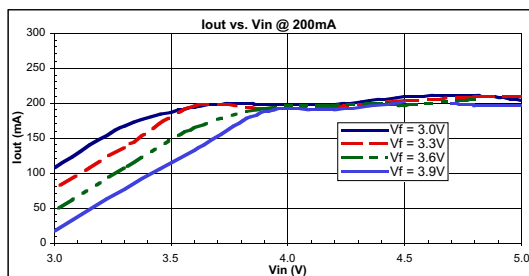
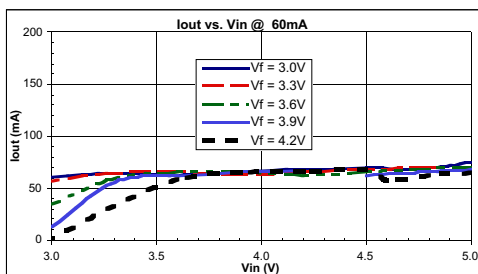
Parameter	Min.	Typ.	Max.	Units		Conditions
Input Voltage	2.7		5.5	V	♦	
Quiescent Current		0.5	3	mA	♦	$V_{IN} = 3.0 - 5.5V$, $V_{OUT} = 3.6V$, $I_{OUT} = 100\mu A$
Shutdown Current		0.5	1.5	μA	♦	$V_{EN}/PWM = 0V$, $V_{IN} = 5.5V$
Maximum Load Current		200		mA		$V_{IN} = 4.2V$, $V_{OUT} = 3.6V$
Oscillator Frequency	0.7	1.2	1.5	MHz	♦	$V_{IN} = 2.7 - 5.5V$
V_{FB} Reference Voltage	0.283	0.315	0.347	V	♦	$I_{OUT} = 20mA$, $V_{OUT} = 3.6V$, 1.5X Mode
Output Resistance		8	13		♦	1.5X Mode, 100mA Load
		3	6		♦	1.0X Mode, 100mA Load
V_{MODE} Threshold Voltage	1.00	1.25	1.50	V		V_{IN} Falling
V_{MODE} Hysteresis		30		mV		
V_{MODE} Pin Current		0.01	0.5	μA	♦	$V_{MODE} = 1.25 V$
EN/PWM Logic Low			0.4	V	♦	
EN/PWM Logic High	1.6			V	♦	
EN/PWM Pin Current		0.01	0.5	μA	♦	$V_{EN}/PWM = 4.2V$
FB Pin Current		0.01	0.5	μA	♦	$V_{FB} = 1V$
V_{OUT} Ripple		80		mV		$V_{OUT} = 4V$, $I_{OUT} = 100mA$, 1.5x Mode
V_{OUT} Turn-On Time		250	500	μs	♦	FB withing 90% regulation 1.5X Mode, $V_{OUT} = 3.6$, $I_{OUT} = 100\mu A$

PIN DESCRIPTION

PIN NUMBER	PIN NAME	DESCRIPTION
1	V_{OUT}	Regulated charge pump output.
2	C1P	Positive terminal to the charge pump flying capacitor C2.
3	V_{IN}	Input supply voltage.
4	V_{MODE}	Charge pump mode program pin. When V_{MODE} is greater than 1.25V, a X1 charge pump is used. Otherwise, charge pump switches to X1.5 mode. A voltage divider shown in the typical application circuit programs the V_{IN} threshold for charge pump mode switching.
5	FB	This is the feedback pin for output current or voltage regulation. The voltage of this pin is compared with an internal 315mV reference.
6	EN/PWM	Enable and PWM dimming control input. Pull this pin low to disconnect V_{OUT} from V_{IN} and shutdown the SP6683. V_{OUT} is pulled to ground in shutdown.
7	C2N	Negative terminal to the charge pump flying capacitor, C4.
8	GND	Ground reference.
9	C1N	Negative terminal to the charge pump flying capacitor, C2.
10	C2P	Positive terminal to the charge pump flying capacitor C4.

FUNCTIONAL DIAGRAM





General Overview

The SP6683 is a current regulated charge pump ideal for converting a Li-Ion battery input for driving white LEDs used in backlighting color displays in cellular phones, PDAs, digital cameras and MP3 players. The SP6683 is able to efficiently drive up to eight 20mA white LEDs in parallel and maintain a constant brightness over a very wide operating voltage range (2.7V to 5.5V). The SP6683 operates with an internal 1.2MHz clock, enabling the use of small external components. Other features of SP6683 include PWM dimming control as well as complete input/output disconnect in shutdown. In shut down mode the IC typically draws 500nA current. The output regulation is achieved by sensing the voltage at the feedback pin and modulating the switch between the charge pump and output capacitor.

Theory of Operation

The SP6683 regulated charge pump block diagram consists of four main blocks (Voltage Reference, Mode Control, Clock Manager, Start-up and Charge-Pump Switches) and two comparators (V_{MODE} Comparator and V_{OUT} Comparator).

1) Voltage Reference. This block provides the 315mV and 1.25V reference voltages needed for the two comparators.

2) Mode Control. An external voltage divider connected to the V_{MODE} pin will define an input voltage to the mode comparator which sets the logic state of the mode selection outputs to the X1 or X1.5 modes. V_{MODE} is compared to a 1.25V bandgap voltage. For example, if one makes a 158K/100K divider, the mode will change at $2.58 \times 1.25 \text{ V} = 3.23\text{V}$. A comparator-based cycle by cycle regulation ensures that no mode change occurs during cycles.

3) Clock Manager. An internal 1.2MHz clock is generated in this block. Depending on the mode control, the appropriate clock phasing is generated here and sent to the start-up and charge-pump switches block.

4) V_{OUT} Comparator and Output Control. A 315mV reference voltage is compared to feedback voltage to control the V_{out} needed for the application. Output current is set by a bias resistor from the FB pin to the GND pin chosen by the relationship:

$$I_{OUT} = \frac{V_{FB}}{R_{FB}}$$

where $V_{FB} = 315\text{mV}$.

Configuring the SP6683 as Voltage or Current Source

The white LED load configuration used by customers can be discrete white LEDs or a white LED module. Inside the white LED module, there may or may not be resistors in series with the white LEDs. According to the different application requirements, the SP6683 can be configured as either a voltage source or a current source to provide solutions for these different applications, as shown in figures 9-12. Figure 9 shows the SP6683 driving discrete white LEDs as a current source.

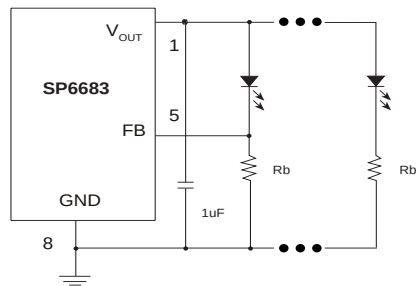


Figure 9. Driving discrete white LEDs as current source

The current in one white LED current is set by the ratio of the feedback pin voltage (315mV) and the bias resistor R_B . To set the operating current, R_B can be selected by:

$$R_B = \frac{V_{FB}}{I_{LED}}$$

The current of the remaining white LEDs is set according to the similarity of the white LEDs. 3-wire white LED modules with internal series

resistors (as shown in figure 10) can also be driven in this way.

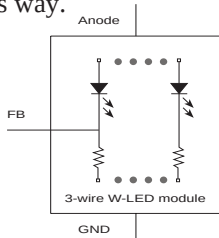


Fig 10. 3-wire white LED module

In figure 11, SP6683 was used to drive a 2-wire white LED module (without internal series resistors) as a current source. The bias resistor R_B is selected to regulate the total current of the white LED module instead of the current of a single LED as in figure 9.

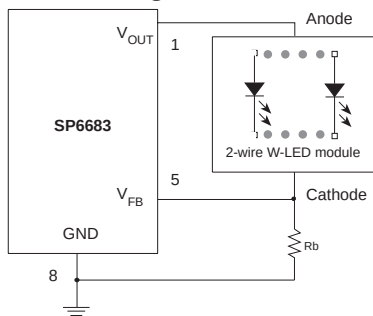


Figure 11. Driving 2-wire white LED module as current source

In this application, the bias resistor can be selected by:

$$R_B = \frac{V_{FB}}{I_{LED(TOTAL)}}$$

where $I_{LED(TOTAL)}$ is the total operating current of all the white LEDs.

To use SP6683 as a voltage source for fixed voltage applications, a voltage divider is need to program the output voltage, as shown in figure 12.

The output voltage is set by the ratio of the two resistors and the feedback control voltage as shown by:

$$V_{OUT} = \left(1 + \frac{R_5}{R_6}\right) \cdot V_{FB}$$

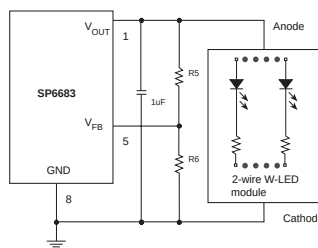


Figure 12. Driving 2-wire white LED module as voltage source

Programming The Operating Mode

The SP6683 can automatically change from X1 mode to X1.5 to maximize efficiency. To use this feature, divider resistors should be chosen according to the specific application, as shown in figure 13.

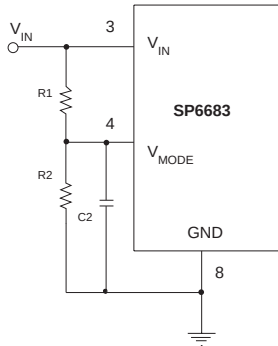


Figure 13. Programming the Vmode Resistors

The guideline for divider resistor selections is as follows. For high input voltage, the SP6683 will work in X1 mode. When the input voltage drops to V_{TH} threshold voltage, it will switch to X1.5 mode automatically. The V_{TH} threshold voltage for mode change can be calculated by:

$$V_{TH} = (V_F + 0.315 + m \cdot I_{LED} \cdot R_{OUT})$$

Where V_F and m are the forward voltage and number of the white LEDs, R_{out} is the output resistance of the SP6683.

The equation for the voltage divider R_1 and R_2 with $V_{MODE} = 1.25V$ is:

$$V_{TH} = 1.25V \cdot (1 + R_1/R_2)$$

Which can be expressed as R_1 :

$$R_1 = [(V_{TH}/1.25) - 1] R_2$$

For the typical SP6683 application, using $V_F = 3.6V$, $m = 8$, $I_{LED} = 15mA$, $R_{OUT} = 6\Omega$, V_{TH} will be $4.63V$, Select $R_2 = 100k\Omega$, then $R_1 = 270k\Omega$

Capacitor Selection

Ceramic capacitors are recommended for their inherently low ESR, which will help produce low peak to peak output ripple, and reduce high frequency spikes.

The fly capacitor controls the strength of the charge pump. Selection of the fly capacitor is a trade-off between the output voltage ripple and the output current capability. Decreasing the fly capacitor will reduce the output voltage ripple because less charge will be delivered to the output capacitor. However, a smaller fly capacitor leads to larger output resistance, thus decreasing the output current capability and the circuits efficiency. Increasing the value of the input and output capacitors could further reduce the input and output ripple. For peak performance, place all capacitors as close to the SP6683 as possible.

Refer to table 1 for some suggested low ESR capacitors.

Table: 1

SUGGESTED LOW ESR CAPACITORS

Manufacturers/ Website	Part Number	Capacitance/ Voltage	Capacitor Size/Type/Thickness	ESR at 100KHz
TDK/www.tdk.com	C1005X5R0J105M	1uF/6.3V	0402/X5R/0.5mm	0.03
TDK/www.tdk.com	C1608X5R0J225K	2.2uF/6.3V	0603/X5R/0.9mm	0.02
Murata/www.murata.com	GRM155R60J105KE19B	1uF/6.3V	0402/X5R/0.55mm	0.03
Murata/www.murata.com	GRM188R61A225KE34B	2.2uF/6.3V	0603/X5R/0.9mm	0.02

Brightness Control Using PWM

Dimming control can be achieved by applying a PWM control signal to the EN/PWM pin. The brightness of the white LEDs are controlled by increasing and decreasing the duty cycle of the PWM signal. While operating frequency range is from 60Hz to 700Hz, the recommended maximum brightness frequency range is from 60Hz to 200Hz. A repetition rate of at least 60Hz is required to prevent flicker.

Brightness Matching

For white LEDs, the forward voltage drop is a function of the operating current. However, for a given current, the forward voltage drop across each LED does not always match exactly. This can cause uneven brightness in the white LEDs.

In figure 14, assuming high-precision bias resistors were used, the operating current ratio of two different branches can be easily derived as shown by:

$$\frac{I_1}{I_2} = \frac{V_{OUT} - V_{F1}}{V_{OUT} - V_{F2}}$$

where I_1 , I_2 are the operating current of the white LEDs, where V_{F1} , V_{F2} are the forward voltage of the white LEDs.

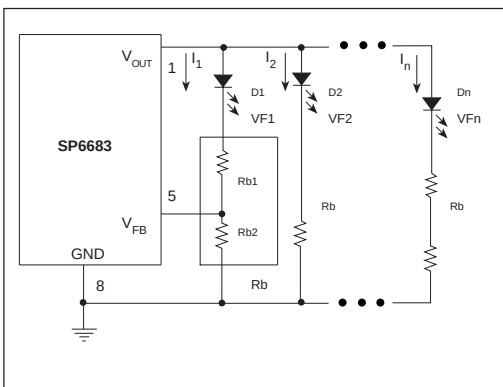


Figure 14. Improving brightness matching

Since the brightness of the white LED is proportional to the operating current, for better brightness matching, a higher output voltage could be used. This could be done by using a larger resistor, as shown in figure 14. Rb2 is used to bias the operating current of the white LED, Rb1 is used to increase the output voltage. Better brightness matching was achieved at the cost of the power wasted on the bias resistor.

Power Efficiency

The efficiency of driving the white LEDs can be calculated by

$$\eta = \frac{V_F \cdot I_F}{V_i \cdot I_i} = \frac{V_F \cdot I_F}{V_i \cdot (n \cdot I_F + I_Q)} \approx \frac{V_F}{V_i \cdot n}$$

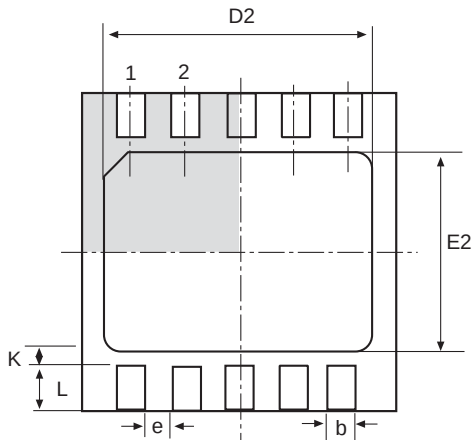
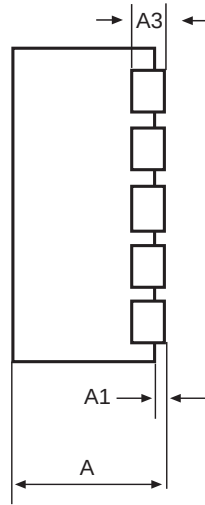
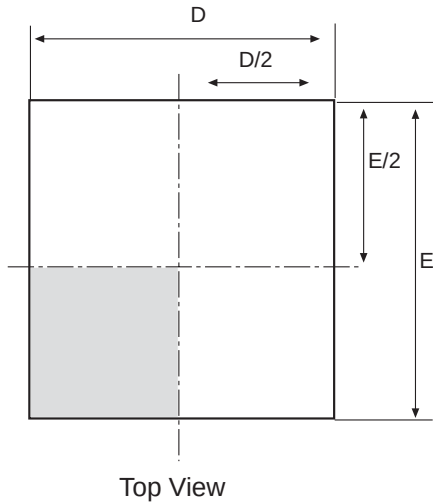
Where V_i , I_i are input voltage and current; V_F , I_F are the forward voltage and operating current of the White LED; I_Q is quiescent current, which is considered small compared with I_F .

Three Options for Optimization

The SP6683 family includes three options that allow the user to optimize the LED driving circuit. The SP6683 includes an on-board discharge switch that ensures that V_{OUT} is completely discharged in shutdown. This feature is very useful in systems using PWM dimming, when the SP6683 is periodically turned on and off. The discharge switch ensures that the falling and rising edges of the LED current have similar durations.

The SP6683-03 has the discharge MOSFET disabled ensuring that the output node is at high impedance during shutdown. This can be useful if the system is feeding LED current from another source while the SP6683 is in shutdown.

The SP6683-LV offers a lower reference voltage that allows to reduce efficiency loss occurring in the current sense resistor. This part is ideal for driving single LEDs for Flash applications.



10 Pin DFN (JEDEC MO-229, VEED-5 VARIATION)	DIMENSIONS in (mm)		
	MIN	NOM	MAX
SYMBOL			
A	0.80	0.90	1.00
A1	0	0.02	0.05
A3	0.20 REF		
b	0.18	0.25	0.30
D	3.00 BSC		
D2	2.20	-	2.70
e	0.50 PITCH		
E	3.00 BSC		
E2	1.40	-	1.75
K	0.20	-	-
L	0.30	0.40	0.50

Part Number	Operating Temperature Range	Package Type
SP6683ER	-40°C to +85°C	10 Pin DFN
SP6683ER/TR	-40°C to +85°C	10 Pin DFN
SP6683-03ER	-40°C to +85°C	10 Pin DFN
SP6683-03ER/TR	-40°C to +85°C	10 Pin DFN

***Contact factory for availability of the following parts**

SP6683LVER	-40°C to +85°C	10 Pin DFN
SP6683LVER/TR	-40°C to +85°C	10 Pin DFN

Available in lead free packaging. To order add "-L" suffix to part number.

Example: SP6683ER/TR = standard; SP6683ER-L/TR = lead free

/TR = Tape and Reel

Pack quantity is 3000 for DFN.



ANALOG EXCELLENCE

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