

# **Sound Processors for Home Theater Systems**

# 6ch / 9ch Stereo Input Selectors





BD3843FS,BD3841FS

No.10081EAT07

#### Description

BD3843FS is a function switch IC with two 6ch (9ch for BD3841FS) inputs. It can independently control outputs of 2 systems: 2ch REC output (3ch for BD3841FS) and 1ch line output. Therefore, it is can be used for 2nd room entertainment and 2nd source recording. BD3843FS has excellent cross-talk characteristics between channels and selectors. Low distortion, and low noise are realized by a basic pattern layout.

#### Features

- 1) 2 room entertainment / 2 source recording.
- 2) 6ch volume LSI BD3813KS/BD3815KS can be controlled simultaneously by the serial control bus.
- 3) 7ch volume LSI BD3816K1, BD3817KS with BD3843FS can be controlled simultaneously by the serial control bus.
- 4) 14ch input is available by combining 2chips (BD3843FS and BD3841FS).
- 5) Excellent cross-talk characteristics between channels by basic pattern layout.
- 6) Built-in buffer amplifier.
- 7) 2-line serial control bus (for both 3.3V and 5V).
- 8) Operated by single and dual power sources.

#### Applications

AV receivers, home theater systems, mini-audio systems, and TVs.

#### Product lineup

Parameter	BD3843FS	BD3841FS
Input selector	6	9
REC output	2	3
Package	SSOP-A24	SSOP-A32

#### ●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit	
Dawer Cupply Voltage	VCC	7.5 *1	V	
Power Supply Voltage	VEE	-7.5		
Input signal voltage	VIN	VCC+0.3∼VEE-0.3	V	
Power Dissipation	Pd	800 (BD3843FS) 950 (BD3841FS) *2	mW	
Operating temperature	Topr	<b>−20~+75</b>	သိ	
Storage temperature	Tastg	<b>−55~+125</b>	°C	

<sup>\*1</sup> Even in the specified range of Power Supply Voltage, applying voltage only to the VCC side may cause an excessive current may cause permanent damage to the IC.

When starting up power supplies, VEE and VCC should be powered on simultaneously, or VEE first followed by VCC.

<sup>\*2</sup> Reduced by 8 mW/°C over 25°C (BD3843FS), when installed on the standard board (size: 70 x 70 x 1.6mm). Reduced by 7.7 mW/°C over 25°C (BD3841FS), when installed on the standard board (size: 70 x 70 x 1.6mm).

# Operating range

Must function normally at  $Ta = 25^{\circ}C$ ,

Dort No.	Daramatar	Symbol		Unit		
Part No.	Parameter	Symbol	Min.	Тур.	Max.	Offic
	Action power source voltage	VCC	4	7	7.3	
BD3843FS	(both power sources)	VEE	-7.3	-7	-4	V
	Action power source voltage (single power source)	VCC- VEE	8	14	14.6	
	Action power source voltage	VCC	5	7	7.3	
BD3841FS	(both power sources)	VEE	-7.3	-7	-5	V
	Action power source voltage (single power source)	VCC- VEE	10	14	14.6	

# Electrical characteristics

Ta=25°C, VCC=7V, VEE=7V, f=1kHz, Vin=1Vrms, RL=10k $\Omega$ , Rg=600 $\Omega$ , unless otherwise noted.

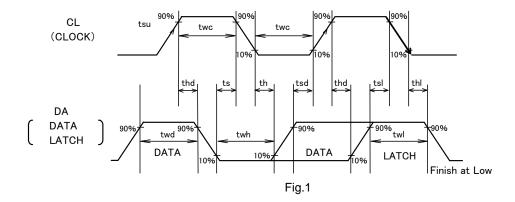
	Parame	Symbol		Limits		Unit	Conditions	
	Parame		Symbol	Min.	Тур.	Max.	Offic	Conditions
	Circuit current	VCC	IQ		3	10	mA	No signal
		VEE		-10	-3			
	Output voltage gain 1	ch	Gv1	-2	0	2	dB	
	Output voltage gain 2	ch	Gv2	-2	0	2	dB	
	Total harmonic distort	ion ratio 1ch	THD1	_	0.004	0.05	%	BW=400~30kHz
	Total harmonic distort	ion ratio 2ch	THD2	_	0.004	0.05	%	BW=400~30kHz
	Maximum output volta	age 1ch	Vomax1	3.4	4.2	_	Vrms	THD=1%
Output	Maximum output volta	age 2ch	Vomax2	3.4	4.2	_	Vrms	THD=1%
no	Output noise voltage	1ch	Vno1	_	1	5	μVrms	Rg=0Ω, BW=IHF-A
	Output noise voltage	Vno2	_	1	5	μVrms	Rg=0Ω, BW=IHF-A	
	Cross-talk between control 1ch→2ch	Cross-talk between channels 1ch→2ch		_	-95	-80	dB	Rg=0Ω, BW=IHF-A
	Cross-talk between c 2ch→1ch	hannels	CTC21	_	-95	-80	dB	Rg=0Ω, BW=IHF-A
	Cross-talk between s	electors 1ch	CTS1	_	-95	-80	dB	Rg=0Ω, BW=IHF-A
	Cross-talk between s	electors 2ch	CTS2	_	-95	-80	dB	Rg=0Ω, BW=IHF-A
	R voltage gain 1ch		GVR1	-2	0	2	dB	RL=47kΩ,
	R voltage gain 2ch		GVR2	-2	0	2	dB	RL=47kΩ,
	R Total harmonic distort	on ratio 1ch	THDR1		0.01	0.09	%	RL= $47k\Omega$ , BW= $400\sim30kHz$
REC output	R Total harmonic distortion ratio 2ch		THDR2		0.01	0.09	%	RL=47k $\Omega$ , BW=400 $\sim$ 30kHz
REC (	R output noise voltage 1ch		VnoR1	_	1	5	μVms	Rg=0Ω, BW=IHF-A
	R output noise voltage 2ch		VnoR2	_	1	5	μVms	Rg=0Ω, BW=IHF-A
	R output impedance	1ch	RoutR1	_	50	100	Ω	
	R output impedance 2	2ch	RoutR2	_	50	100	Ω	

<sup>\*</sup> Note: This IC is not designed to be radiation-resistant.

# ●Timing chart

- 1. Signal timing standards
  - Data is read at the rise of clock.
  - · Latch is read at the fall of clock.
  - End latch signal at LOW.
    - \* To avoid malfunctions, clock and data signals must terminate with the LOW state.

# 1byte=17bit



Parameter	Cymbol		Limit					
Parameter	Symbol	Min.	Тур.	Max.	Unit			
Minimum clock width	twc	2.0	_	_	μs			
Minimum data width	twd	2.0	_	_	μs			
Minimum latch width	twl	2.0	_	_	μs			
LOW hold width	twh	2.0	_	_	μs			
Data setup time (DATA→CLK)	tsd	1.0	_	_	μs			
Data hold time (CLK→DATA)	thd	1.0	_	_	μs			
Latch setup time (CLK→LATCH)	tsl	1.0	_	_	μs			
Latch hold time (DATA→LATCH)	thl	1.0	_	_	μs			
Latch low setup time	ts	1.0	_	_	μs			
Latch low hold time	th	1.0	_	_	μs			

2. Voltage standards of control signal

z. voltago otariaarao or o	oriti or orginal				
Parameter	Conditions		Unit		
Farameter	Conditions	Min.	Тур.	Max.(≦Vcc)	Offic
"H" input voltage	Vcc=5~7.3V	2.2	_	5.5	V
"L" input voltage	VEE=-5~-7.3V	0	_	1.0	V

# 3. Control data format list

(1) BD3843FS control data format

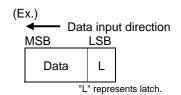
	MSB																LSB
	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
Data	INP	UT FU	NCTIC	)N 1	INP	UT FU	NCTIO	N 2	RECA 0:OFF 1:ON		*	0	0	0	Sele	ct Add	ress

# (2) BD3841FS control data format

◆ Data input direction

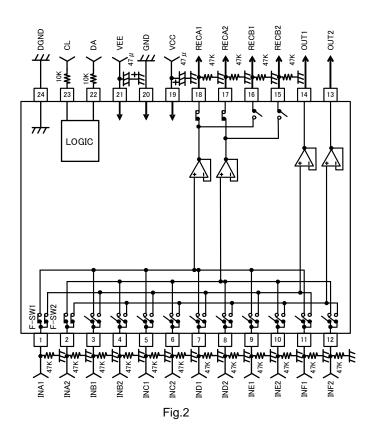
	MSB																LSB
	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
Data	INP	UT FU	NCTIO	)N 1	INP	UT FU	NCTIO	N 2		RECB 0:OFF 1:ON		*	*	*	1	0	1

<sup>\*:</sup>Don't care.

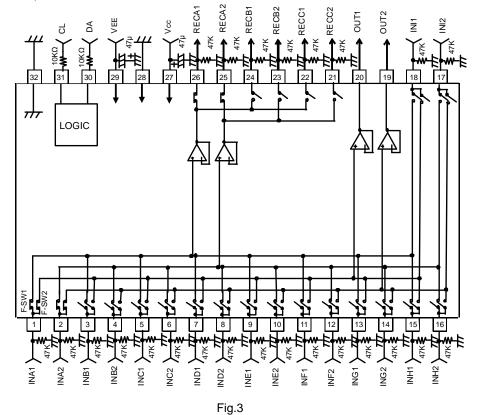


# ●Block diagram, application circuit

1)BD3843FS) (both power sources)

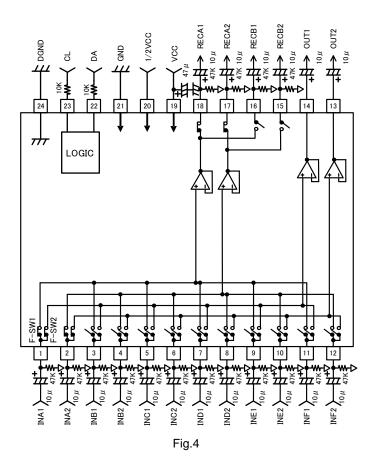


2)BD3841FS (both power sources)



UNIT RESISTANCE :  $\Omega$  CAPACITOR : F

\* F-SW1: INPUT FUNCTION1 F-SW2: INPUT FUNCTION2 3)BD3843FS (single power source)



4)BD3841FS (single power source) 7 LOGIC ING2 Ĭ H INH2 INB1 IN D2 ING1 NC1 INC2 INE2 IND1 UNIT

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\* F-SW1: INPUT FUNCTION1

F-SW2: INPUT FUNCTION2

Fig.5

RESISTANCE :  $\Omega$ 

 $\mathsf{RESISTANCE}: \Omega$ 

CAPACITOR : F

#### ■Reference data

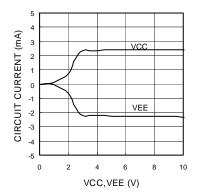


Fig.6 Circuit Current - Supply Voltage

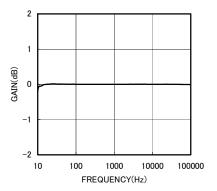


Fig.9 Voltage Gain - Frequency

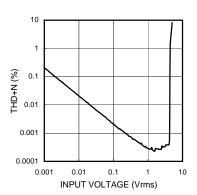


Fig.12 REC Total Harmonic Distortion
- Input Voltage

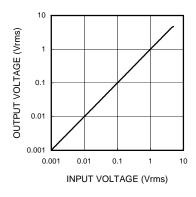


Fig.7 Output Voltage - Input Voltage

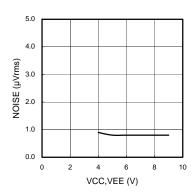


Fig.10 Output Noise Voltage – Supply Voltage

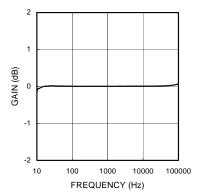


Fig.13 REC Voltage Gain - Frequency

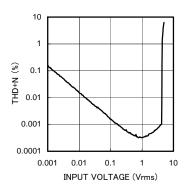


Fig.8 Total Harmonic Distortion - Input Voltage

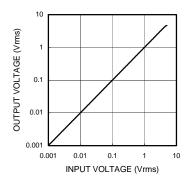


Fig.11 REC Output Voltage - Input Voltage

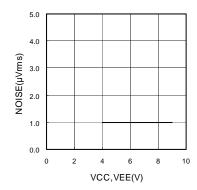


Fig.14 REC Output Noise -Supply Voltage

#### Notes for use

- 1. Numbers and data in entries are representative design values and are not guaranteed values of the items.
- 2. Although ROHM is confident that the example application circuit reflects the best possible recommendations, be sure to verify circuit characteristics for your particular application. Modification of constants for other externally connected circuits may cause variations in both static and transient characteristics for external components as well as this Rohm IC. Allow for sufficient margins when determining circuit constants.
- 3. Absolute maximum ratings

Use of the IC in excess of absolute maximum ratings, such as the applied voltage or operating temperature range (Topr), may result in IC damage. Assumptions should not be made regarding the state of the IC (short mode or open mode) when such damage is suffered. A physical safety measure, such as a fuse, should be implemented when using the IC at times where the absolute maximum ratings may be exceeded.

VEE potential

Make the VEE pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the VEE pin, including transient phenomena.

Thermal design

Perform thermal design, in which there are adequate margins, by taking into account the power dissipation (Pd) in actual states of use.

- 6. Short circuit between terminals and erroneous mounting
  - Pay attention to the assembly direction of the ICs. Wrong mounting direction or shorts between terminals, GND, or other components on the circuits, can damage the IC.
- 7. Operation in strong electromagnetic field

Using the ICs in a strong electromagnetic field can cause operation malfunction.

8. Operating Voltage Range and Operating Temperature Range

Circuit function actions are guaranteed within the action voltage range and the action temperature range. However, the specific values of the electrical characteristics are guaranteed under the specified conditions of the electrical characteristics. Therefore, carry out set considerations in sufficient consideration of IC characteristic fluctuations.

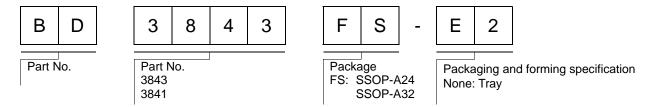
- 9. Power ON/OFF
  - (a) At power ON/OFF, a shock sound will be generated. Therefore, use MUTE on the set.
  - (b) When turning on power supplies, VEE and VCC should be powered on simultaneously, or VEE first followed by VCC. If the VCC side is started up first, an excessive current may flow from VCC to VEE.
- 10. Serial control

For the CL and DA terminals, the patterned and other wirings should be routed as not to cause interference with the analog-signal-related lines.

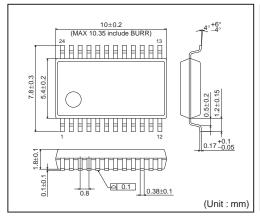
11. Function switching

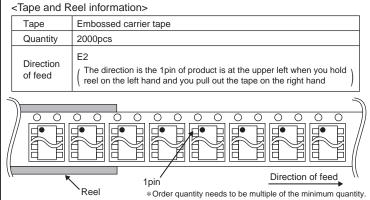
For all functions, other than Master Volume, Treble, and Bass Gain Settings, use MUTE on the set.

# Ordering part number

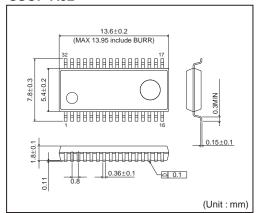


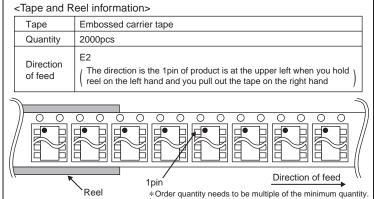
#### SSOP-A24





#### SSOP-A32





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JÁPAN	USA	EU	CHINA
CLASSI	СГУССШ	CLASS II b	СГУССШ
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  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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  - the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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