TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74LCX04FN

#### Low-Voltage Hex Inverter with 5-V Tolerant Inputs and Outputs

The TC74LCX04 is a high-performance CMOS inverter. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

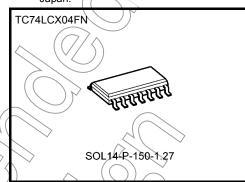
The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs.

All inputs are equipped with protection circuits against static discharge.

#### **Features**

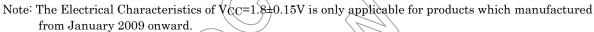
- Low-voltage operation: VCC = 1.65 to 3.6 V
- High-speed operation:  $t_{pd} = 5.2 \text{ ns (max)} (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Output current:  $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: >±500 mA
- Available in JEDEC SOP
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 04 type

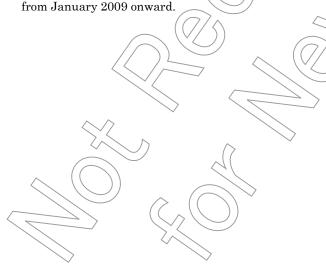
Note: xxxFN (JEDEC SOP) is not available in Japan.



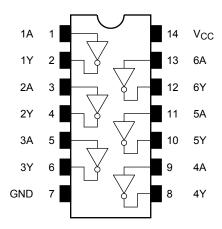
Weight SOL14-P-150-1.27

: 0.12 g (typ.)

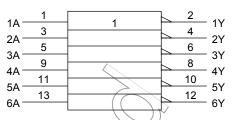




## Pin Assignment (top view)



## IEC Logic Symbol



#### **Truth Table**

Inputs	Outputs
А	Y
L	Н
Н	L

# Absolute Maximum Ratings (Note 1)

	^		
Characteristics	Symbol	Rating	Unit
Power supply voltage	Vcc	-0.5 to 7.0	\/v
DC input voltage	VIN	-0.5 to 7.0	
DC output voltage	Voui	-0.5 to 7.0 (Note 2) -0.5 to Voc + 0.5 (Note 3)	V
Input diode current	<b>UIK</b>	-50	mA
Output diode current	J lok <	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	PD	180	mW
DC V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA
Storage temperature	Tstg	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2:  $V_{CC} = 0 V$ 

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4:  $V_{OUT} < GND$ ,  $V_{OUT} > V_{CC}$ 

## **Operating Ranges (Note 1)**

Characteristics	Symbol	Rating	Unit
Power supply voltage	Voc	1.65 to 3.6	V
Power supply voltage	V <sub>CC</sub>	1.5 to 3.6 (Note 2)	V
Input voltage	V <sub>IN</sub>	0 to 5.5	V
Output voltage	V <sub>OUT</sub>	0 to 5.5 (Note 3)	V
		0 to V <sub>CC</sub> (Note 4)	V
Output current	I <sub>OH</sub> /I <sub>OL</sub>	±24 (Note 5)	mA (
		±12 (Note 6)	
Operating temperature	T <sub>opr</sub>	-40 to 85	ǰC \
Input rise and fall time	dt/dv	0 to 10 (Note 7)	(ns/V)

- Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V<sub>CC</sub> or GND.
- Note 2: Data retention only
- Note 3:  $V_{CC} = 0 V$
- Note 4: High or low state ( However, it can not exceed IOUT of absolute maximum ratings.)
- Note 5:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
- Note 6:  $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$
- Note 7:  $V_{IN} = 0.8 \text{ to } 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$

## **Electrical Characteristics**

## DC Characteristics (Ta = -40 to 85°C)

Characteris	etics	Symbol	Test Condition		V 00	Min	Max	Unit
					V <sub>CC</sub> (V)	V <sub>CC</sub> × 0.9	_	
H-level		$V_{IH}$	_		2.3 to 2.7	1.7	_	
					2.7 to 3.6	2.0	_	
Input voltage					1.65 to 2.3		V <sub>CC</sub> × 0.1	V
	L-level	$V_{IL}$		- <	2.3 to 2.7/	$\bigcirc$	0.7	
					2.7 to 3.6	<u> </u>	0.8	
				$I_{OH} = -100 \mu A$	1.65 to 3.6	V <sub>CC</sub> -0.2		
				I <sub>OH</sub> = -4 mA	1.65	1.05	1	
	I I I I I I I I I I I I I I I I I I I		V V	$I_{OH} = -8 \text{ mA}$	2.3	1.7	$\rightarrow$	
	H-level	V <sub>OH</sub>	$V_{IN} = V_{IL}$	I <sub>OH</sub> = -12 mA	2.7	2.2	<u></u>	
				I <sub>OH</sub> = -18 mA	3.0	2.4	$\bigcirc)-$	
Output voltage				OH = -24 mA	3.0	2.2	<u> </u>	V
Output voltage			20	1οι = 100 μΑ	1.65 to 3.6	$\bigcirc$	0.2	V
		VoL		loL = 4 mA	1.65		0.45	
	L-level		Vin = VIH	) <sub>OL</sub> = 8 mA	(2.3/ )	_	0.7	
	Licyci			I <sub>OL</sub> = 12 mA	2.7	_	0.4	
				I <sub>OL</sub> = 16 mA	3.0		0.4	
			I <sub>OL</sub> = 24 mA	3.0		0.55		
Input leakage current			V <sub>IN</sub> = 0 to 5.5 V		1.65 to 3.6	_	±5.0	μА
Power-off leakage curre	ent	((OFF))	V <sub>IN</sub> /V <sub>OUT</sub> = 5.5 V		0	_	10.0	μΑ
Quiescent supply current		V <sub>IN</sub> = V <sub>CC</sub> or GND		1.65 to 3.6	_	10.0		
da.cooni cappiy dune	Suicoccin supply surrent		V <sub>IN</sub> = 3.6 to 5.5 V		1.65 to 3.6	_	±10.0	μΑ
Increase in Icc per inpu	ıt// )	Δlcc	VIH = VCC/	0.6 V	2.7 to 3.6	_	500	



#### AC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time	<sup>t</sup> pLH t <sub>pHL</sub>	Figure 1, Figure 2	1.8 ± 0.15	_	20.0	
			2.5 ± 0.2	_	7.0	
			2.7	_	6.0	ns
			3.3 ± 0.3	1.5	5.2	
Output to output skew	t <sub>osLH</sub>	(Note)	2.7	))_		ns
	t <sub>osHL</sub>	(Note)	3.3 ± 0.3		1.0	110

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{PLHm} - t_{PLHn}|, t_{OSHL} = |t_{PHLm} - t_{PHLn}|)$ 

## Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500$ $\Omega$ )

Characteristics	Symbol	Test Condition	Vcc (V) Typ.	Unit
Quiet output maximum dynamic V <sub>OL</sub>	V <sub>OLP</sub>	V <sub>IH</sub> = 3.3 V, V <sub>IL</sub> = 0 V	3.3 0.8	V
Quiet output minimum dynamic V <sub>OL</sub>	V <sub>OLV</sub>	$V_{IH} = 3.3 V, V_{IL} = 0 V$	3.3 0.8	V

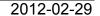
## **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>		3.3	7	pF
Output capacitance	Cout		0	8	pF
Power dissipation capacitance	CPD f <sub>IN</sub> = 10 MHz	(Note)	3.3	25	pF

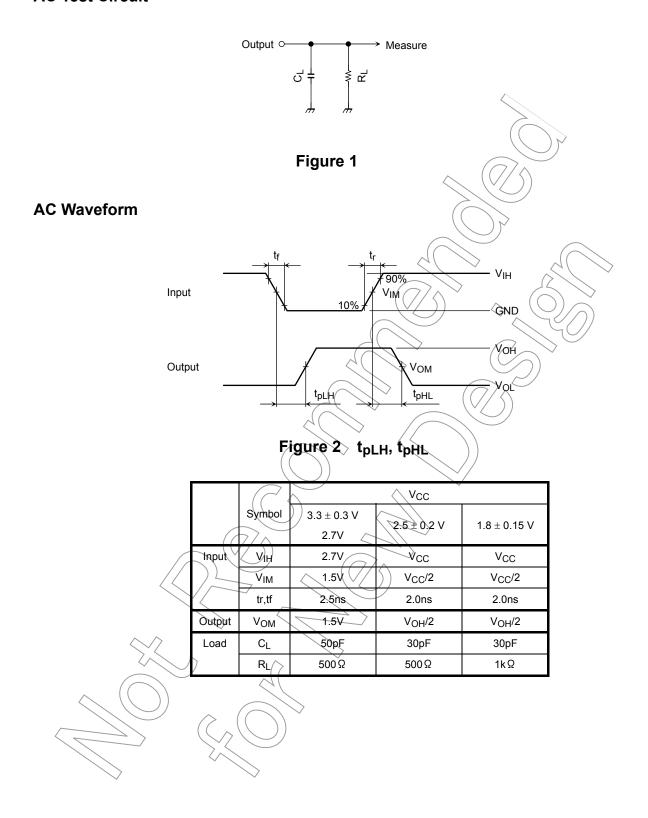
Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

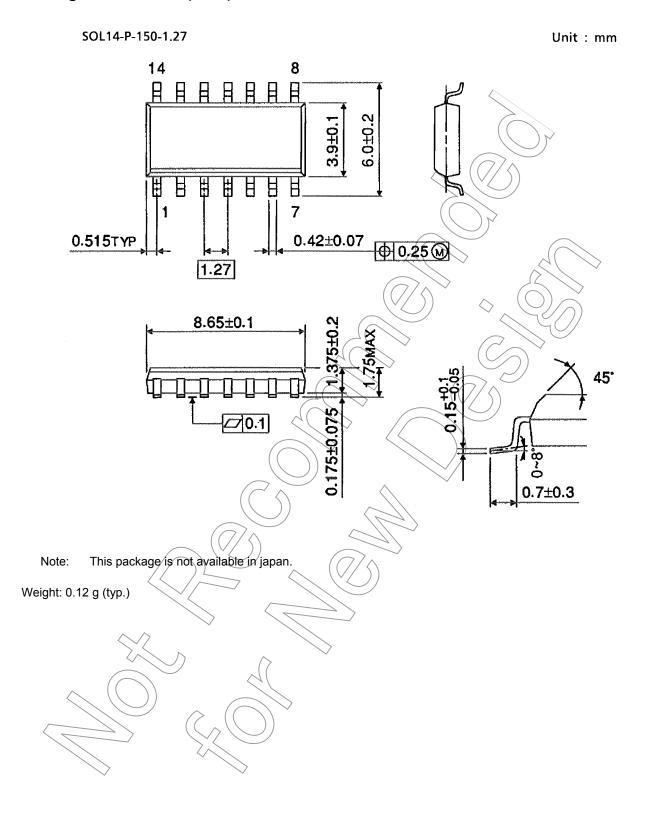
ICC (opr) = CPD·VCC·f<sub>IN</sub> + I<sub>C</sub>C/6 (per gate)



## **AC Test Circuit**



## **Package Dimensions (Note)**



7 2012-02-29

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8