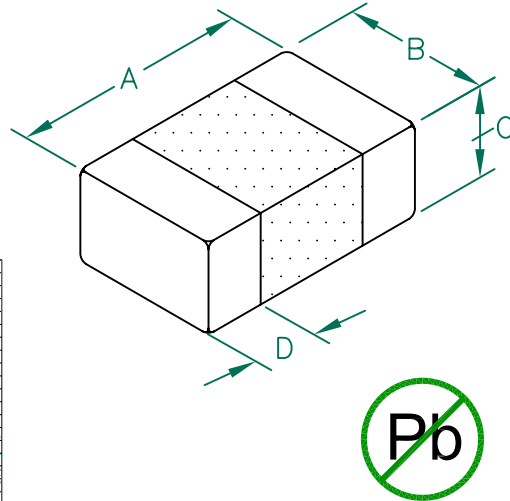
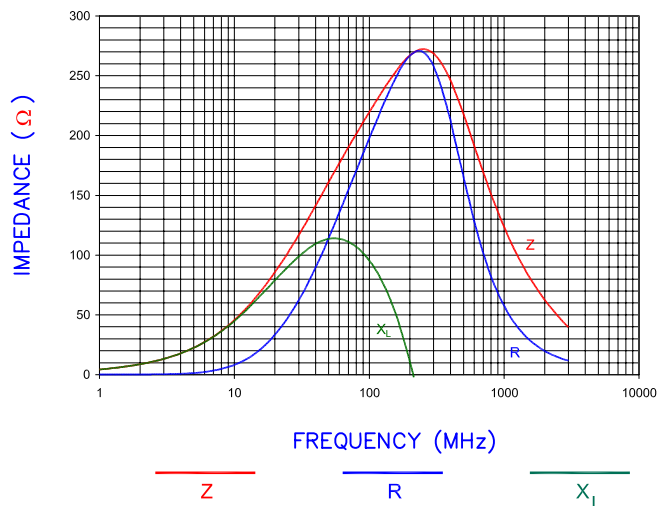
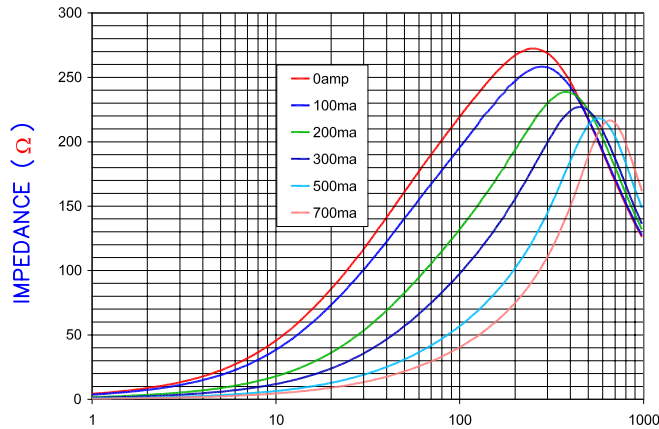


**UNCONTROLLED
DOCUMENT**

A	2.00	[.079]		0.20	[.008]
B	1.25	[.049]		0.20	[.008]
C	0.90	[.035]		0.20	[.008]
D	0.51	[.020]		0.25	[.010]



A diagram showing a 2D grid with dimensions and tolerances. The grid consists of two squares. The left square has a width of 1.47 with a tolerance of [.058]. The right square has a width of 0.66 with a tolerance of [.026]. The total width of the two squares is 3.23 with a tolerance of [.127]. The height of the squares is 1.47 with a tolerance of [.058].

(For wave soldering, add 0.763
[.030] to this dimension.)

Z @ 100MHz (Ω)		DCR (Ω)	Rated Current
Nominal	200		
Minimum	150		
Maximum	250	0.3	700 mA

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER TAPE.
2. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
3. TERMINATION FINISH IS 100% TIN.

REFLOW SOLDERING

PRE-HEATING

SOLDERING

NATURAL COOLING

TEMPERATURE (°C) [°F]

250 [482]

130 [266]

60 SECONDS

5 - 10 SECONDS

100 SECONDS

60 SECONDS



DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.		Laird TECHNOLOGIES®		
E	CHANGE TO PAPER TAPE	03/03/10	JUN	PROJECT/PART NUMBER:		REV	PART TYPE:	DRAWN BY:
D	UPDATE COMPANY LOGO	06/16/09	JRK	LI0805G201R-10		E	CO-FIRE	JRK
C	ADD ROHS SYMBOL MODIFY NOTE 1	11/28/06	JRK	DATE: 04/19/04		SCALE: NTS		SHEET:
B	CORRECT TEST REF #	01/03/06	JRK	CAD #		TOOL #		
A	ORIGINAL DRAFT	04/19/04	JRK	LI0805G201R-10-E				2 of 2
REV	DESCRIPTION	DATE	INT					