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ADC1610S125F1 Obsolete

ADC1610S125F1 Demo board, LVCMOS output

IDT's ADC1610S125F1 demo board is suitable for dynamic performance evaluations from low to high IF configuration with LVCMOS output. A data-acquisition board can be used during design and prototype to analyze ADC performance.

Features

- Single power supply (on-board regulators)
- SMA connector for clock and analog input signals
- CMOS (buffered outputs)

Product Options

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Buy Sample
ADC1610S125F1-DB	Obsolete	BD0	BOARD	0	I	No	Availability

Technical Documentation

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User Guides & Manuals			
Demo Board Software ADC_1_r04	ZIP	2.33 MB	Jul 18, 2012

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ADC1610S125HN	Single 16-bit ADC 125 Mps CMOS or LVDS DDR digital outputs

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ADC1610S125F1 - Evaluation Board

