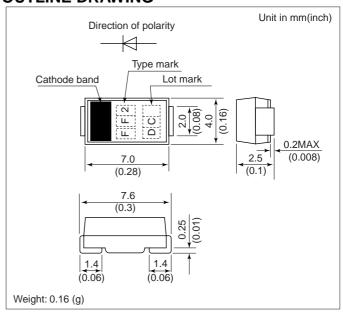
### TENTATIVE SPECIFICATION

# **DFM3MF**

#### **FEATURES**

- For high speed switching
- Soft recovery, low noise.
- Low loss, high efficiency.

#### **OUTLINE DRAWING**



#### **ABSOLUTE MAXIMUM RATINGS**

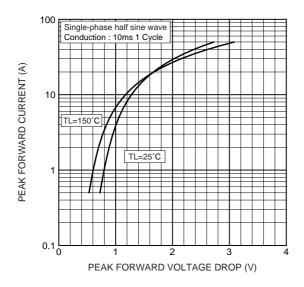
Item	Туре		DFM3MF2			
Repetitive Peak Reverse Voltage	$V_{RRM}$	V	200			
Average Forward Current	I <sub>F(AV)</sub>	А	3.0 (Single-phase half sine wave 180° conduction)			
Surge(Non-Repetitive) Forward Current	I <sub>FSM</sub>	А	50 (Without PIV, 10ms, conduction Tj = 40°C start)			
Operating Junction Temperature	T <sub>j</sub>	°C	-40 ~ +150			
Storage Temperature	T <sub>stg</sub>	°C	-40 ~ +150			

CHARACTERISTICS(T, =25°C)

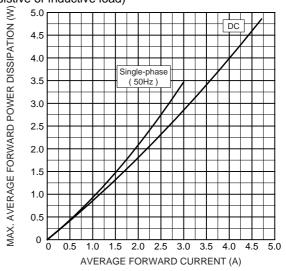
011A(A01E(101100(1[-20 0]								
Item	Symbols	Units	Min.	Тур.	Max.	Test Conditions		
Peak Reverse Current	I <sub>RRM</sub>	μΑ	_	_	10	$V_R = V_{RRM}$		
Peak Forward Voltage	V <sub>FM</sub>	V	_	_	0.95	I <sub>FM</sub> =3.0Ap, Single-phase half sine wave 1 cycle		
Reverse Recovery Time	Trr	ns	_	_	35	I <sub>F</sub> =0.5A, I <sub>rp</sub> =1.0A, 25%recovery		
Steady State Thermal Impedance	$R_{th(j-a)}$ $R_{th(j-l)}$	°C/W	_	_	90 15	On glass-epoxi substrate ( ☐ 50mm) Soldering land( ☐ 10mm)		

# **DFM3MF**

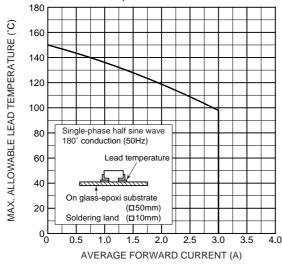
#### Forward characteristic



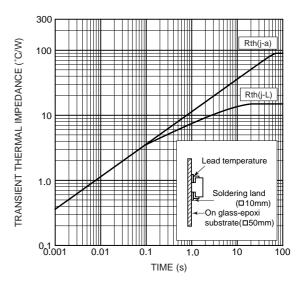
Max. average forward power dissipation (Resistive or inductive load)



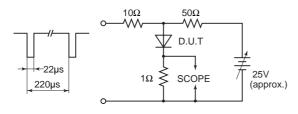
# Max. allowable lead temperature (Resistive or inductive load)

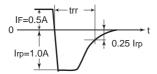


#### Transient thermal impedance



#### Reverse recovery time(trr) test circuit





## HITACHI POWER SEMICONDUCTORS

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