Photologic[®] Slotted Optical Switch

OPB930 and OPB940 (L and W Series)

Features:

- Choice of aperture size
- Choice of output configurations
- Choice of opaque or IR transmissive shell
- Choice of pins (L) or wires (W)
- 0.125" (3.18 mm) slot width
- 0.320" (8.128 mm) lead spacing for PCBoard (side mounting)
- Data rates to 250 kBaud



Description:

The **OPB930** and **OPB940** series of Photologic® photo integrated circuit switches provide optimum flexibility for the design engineer. Building from a standard housing with a 0.125" (3.18 mm) wide slot, a user can specify the type and polarity of TTL output, discrete shell material, aperture width and either 0.350" (8.9 mm) long pins (L Series) or 24" (610 mm) AWG, UL listed wire leads (W Series).

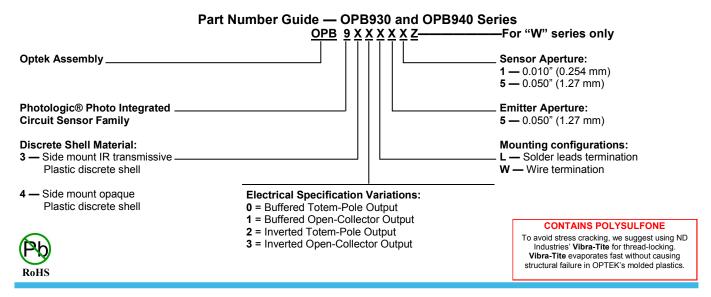
All housings are made from an opaque grade of injection-molded plastic that minimizes the assembly's sensitivity to both visible and near-infrared ambient radiation. Discrete shells (exposed on the parallel faces inside the device throat) are either IR transmissive plastic (for applications where aperture contamination may occur) or opaque plastic (for maximum protection against ambient light).

Electrical output can be specified as either TTL Totem Pole or TTL Open Collector, either of which can be supplied with buffer or inverter output polarity. All devices have the added stability of a built-in hysteresis amplifier.

Custom electrical, wire and cabling and connectors are available. Contact your local representative or OPTEK for more information.

Applications:

- Mechanical switch replacement
- Speed indication (tachometer)
- Mechanical limit indication



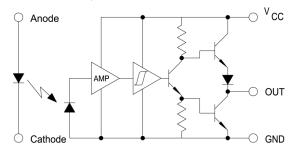
General Note

TT Electronics reserves the right to make changes in product specification without notice or liability. All information is subject to TT Electronics' own data and is considered accurate at time of going to print.

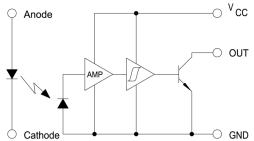
OPTEK Technology, Inc. 1645 Wallace Drive, Carrollton, TX 75006 | Ph: +1 972 323 2200 www.optekinc.com | www.ttelectronics.com



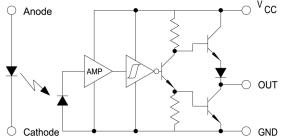
OPB930, OPB940 Buffered Totem-Pole



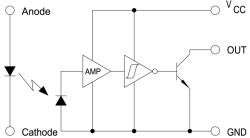
OPB931, OPB941 Buffered Open-Collector



OPB932, OPB942 Inverted Totem-Pole



OPB933 & OPB943 Inverted Open-Collector

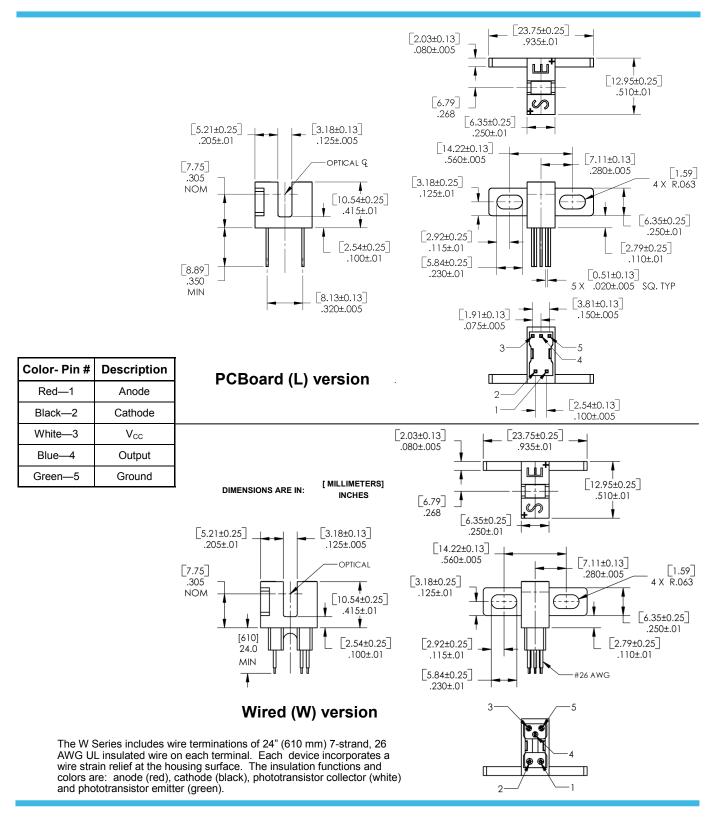


O Cathode GND Cathode Cathode	● GND				
Absolute Maximum Ratings (T _A = 25° C unless otherwise noted)					
Supply Voltage, V _{CC} (not to exceed 3 seconds)	10 V				
Operating Temperature Range	-40°C to +70° C				
Storage Temperature Range	-40°C to +85° C				
Lead Soldering Temperature (1/16 inch (1.6mm) from the case for 5 sec. with soldering iron (1)	260° C				
Input Infrared LED					
Input Diode Power Dissipation ⁽²⁾	100 mW				
Output Photologic® Power Dissipation (3)	200 mW				
Total Device Power Dissipation (4)	300 mW				
Output Photologic®					
Voltage at Output Lead (Open Collector Output)	35 V				
Diode Forward DC Current	40 mA				
Diode Reverse DC Voltage	2 V				

Notes:

- (1) RMA flux is recommended. Duration can be extended to 10 seconds maximum when flow soldering.
- (2) Derate linearly 2.22 mW/°C above 25°.
- (3) Derate linearly 4.44 mW/°C above 25°.
- (4) Derate linearly 6.66 mW/°C above 25°.
- (5) OPB930L/OPB940L series devices are terminated with 0.020" square leads designed for PCBoard mounting.
- (6) Methanol and isopropanol are recommended as cleaning agents. Plastic housing is soluble in chlorinated hydrocarbons and ketones.
- (7) All parameters tested using pulse technique.







Electrical Characteristics (T _A = 25° C unless otherwise noted)						
SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Input Diode						
V_{F}	Forward Voltage	-	-	1.7	V	I _F = 20 mA
I _R	Reverse Current	-	-	100	μΑ	V _R = 2.0 V
Output Pho	Output Photologic® Sensor					
V _{cc}	Operating D.C. Supply Voltage	4.75	-	5.25	V	-
I _{CCL}	Low Level Supply Current: Totem Pole & Open-Collector	-	-	15	mA	V _{CC} = 5.25, I _F = 0 mA ⁽¹⁾
	Inverted Totem-Pole & Inverted Open-Collector	-	-	15	mA	V _{CC} = 5.25, I _F = 15 mA
	High Level Supply Current: Totem Pole & Open-Collector	-	-	15	mA	V _{CC} = 5.25, I _F = 15 mA
I _{CCH}	Inverted Totem-Pole & Inverted Open-Collector	-	-	15	mA	$V_{CC} = 5.25, I_F = 0 \text{ mA}^{(1)}$
V _{OL}	Low Level Output Voltage: Totem Pole & Open-Collector	_	-	0.4	V	V _{CC} = 4.75, I _{OL} = 12.8 mA, I _F = 0 mA
01	Inverted Totem-Pole & Inverted Open-Collector	-	-	0.4	V	V _{CC} = 4.75, I _{OL} = 12.8 mA, I _F = 15 mA
	High Level Output Voltage: Totem-Pole & Open-Collector	2.4	-	-	V	$V_{CC} = 4.75$, $I_{OH} = -800 \mu A$, $I_F = 15 \text{ mA}$
V _{OH}	Inverted Totem-Pole & Inverted Open-Collector	2.4	-	-	V	$V_{CC} = 4.75$, $I_{OH} = -800 \mu A$, $I_F = 0 mA$
I _{OH}	High Level Output Current: Totem Pole & Open-Collector	-	-	100	μΑ	$V_{CC} = 4.75$, $V_{OH} = 30$ V, $I_F = 15$ mA,
	Inverted Totem-Pole & Inverted Open-Collector	-	-	100	μΑ	$V_{CC} = 4.75$, $V_{OH} = 30$ V, $I_F = 0$ mA ⁽¹⁾
I _F (+)	LED Positive-Going Threshold Current	-	-	15	mA	V _{CC} = 5.0 V
I _F (+), I _F (-)	Hysteresis	-	2.0	-	V	V _{cc} = 5.0 V
l _{os}	Short Circuit Output Current: Totem Pole & Open-Collector	-15	-	-60	mA	V _{CC} = 5.25 V, I _F = 15 mA, Output = GND
	Inverted Totem-Pole & Inverted Open-Collector	-15	-	-60	mA	V _{CC} = 5.25 V, I _F = 0 mA ⁽¹⁾ , Output = GND

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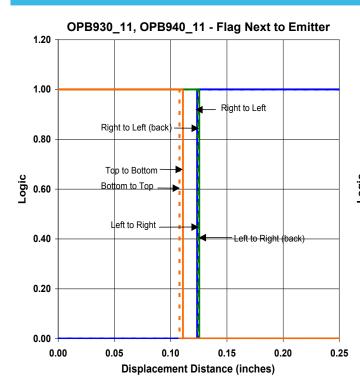


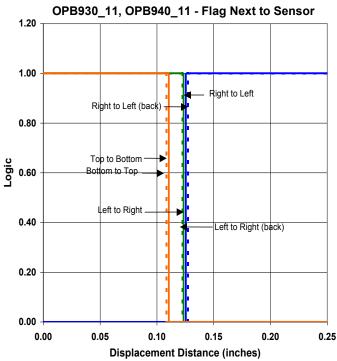
Electrical Characteristics (T _A = 25° C unless otherwise noted)						
SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
t _r t _r	Output Rise Time, Output Fall Time	-	70	-	ns	V _{CC} = 5 V, I _F = 0 or 15 mA
T_PLH	Propagation Delay Low-High	-	5.0	-	μs	R _L = 8TTL loads (Totem Pole)
T _{PHL}	Propagation Delay High-Low	-	5.0	-	μs	R_L = 360 Ω (Open-Collector)

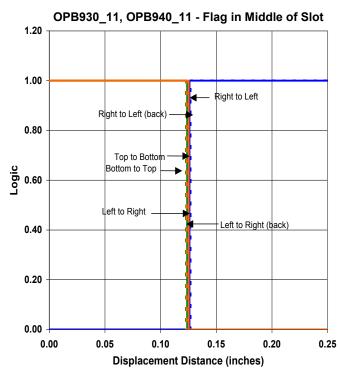
Notes:

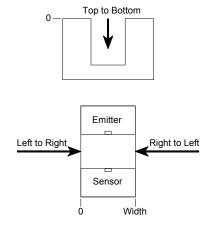
- (1) Normal application would be with light source blocked, simulated by $I_F = 0$ mA.
- (2) All parameters are tested using pulse techniques.



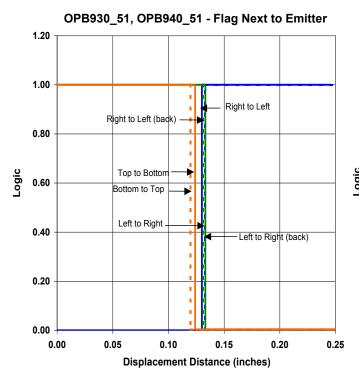


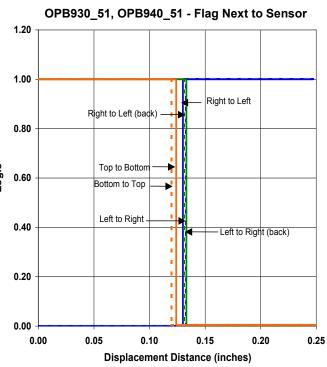




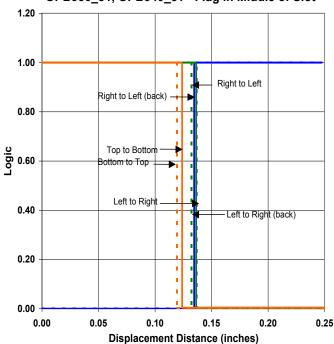


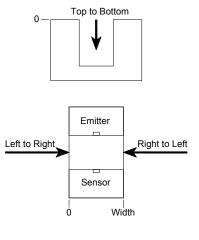




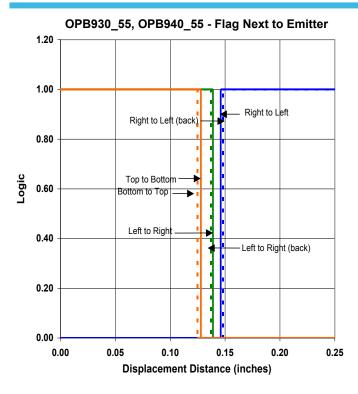


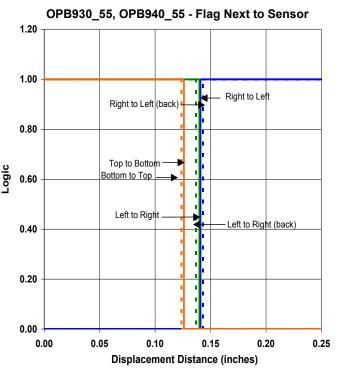
OPB930_51, OPB940_51 - Flag in Middle of Slot

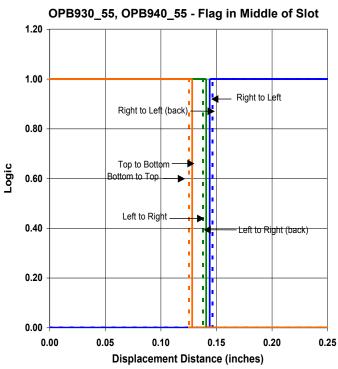


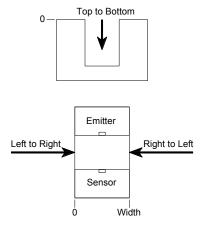












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Issue	Change Description	Approval	Date
	Initial Issue (2 unnumbered PDFs - 1 for L series, 1 for W series)		July 1996
А	Revised and put into new template. Required changes on all pages.	Steve Coble	12/21/05
A.1	Updated table on page 4 with proper information	Steve Coble	5/16/07
A.2	Updated table on page 4 with proper nomenclature Icch, Vol, Voh	Bob Procsal	7/22/07
A.3	Added Polysulfone caution information	Bob Procsal	02/20/08
В	Transferred to the new TT Electronics template	L. Timpa	10/7/2016