

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO (P6-P8-P3): (J6-J3)

: 1CT : 1 ± 3%

(P2-P7-P1) : (J2-J1)

: 1CT : 1.4 ± 3%

2.0 INDUCTANCE (P6-P3)=(J6-J3)

: 200uH MIN. @ 0.01V, 10KHz

(P2-P1)

: 100uH MIN. @ 0.01∨, 10KHz

3.0 LEAKAGE INDUCTANCE P6-P3 (WITH J6 AND J3 SHORT)

: 0.3uH MAX. @ 1MHz

P2-P1 (WITH J2 AND J1 SHORT)

: 0.3uH MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE (P6,P3) TO (J6,J3)

: 20pf MAX @ 1MHz

(P2,P1) TO (J2,J1)

: 20pf MAX. @ 1MHZ

5.0 DC RESISTANCE (J6-J3)=(J2-J1)

: 1.0 ohms Max.

(P6-P8)=(P8-P3)

: 0.5 ohms Max.

(P2-P7)=(P7-P1)

: 0.3 ohms Max.

6.0 DIELECTRIC WITHSTAND (P6,P3) TO (J6,J3)

: 1500VAC for 60 seconds

(P2,P1) TO (J2,J1)

: 1500VAC for 60 seconds

NOTES

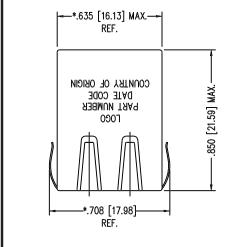
1.0 PINS WITHOUT ELECTRIAL CONNECTIONS ARE OMITTED.

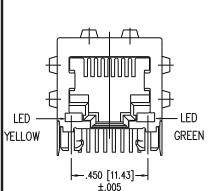
InNet Technologies Inc. http://www.innet-tech.com

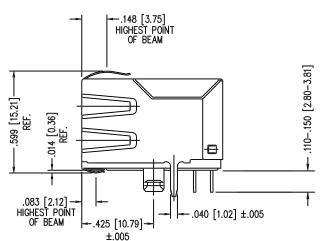
Stewart Connector Systems

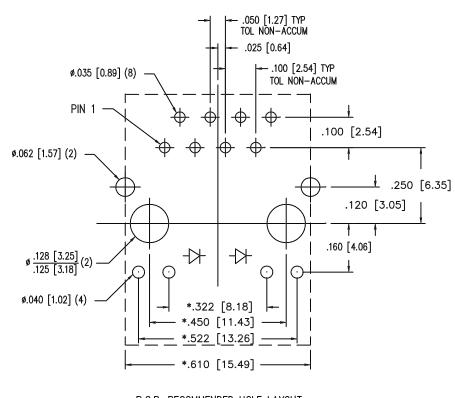
http://www.stewartconnector.com

SHEET 1 DF 3 DRAWING NO.









P.C.B. RECOMMENDED HOLE LAYOUT SEEN FROM COMPONENT SIDE TOLERANCE ±.003 [0.08] UNLESS OTHERWISE SPECIFIED

NOTES:

- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS
- DIMENSIONS SHOWN WITH "*" TO BE CENTRAL ABOUT CENTER LINE
- DIMENSIONS SHOWN ARE SUBJECT TO CHANGE WITHOUT NOTICE.
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
 SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- STANDARD 50 MICRO-INCH SELECTIVE GOLD PLATING.

LED SPECIFICATIONS

- COLOR: YELLOW GREEN
- FORWARD VOLTAGE(20mA): 2.5v (MAX) 2.5v (MAX)
- FORWARD VOLTAGE(20mA): 2.1v (TYP) 2.2v (TYP)
- POWER DISSIPATION: 105mW 105mW
- WAVE LENGTH: 590nm 565nm

8-32 MCD

- LUMINOUS INTENSITY (10mA): 2-8 MCD

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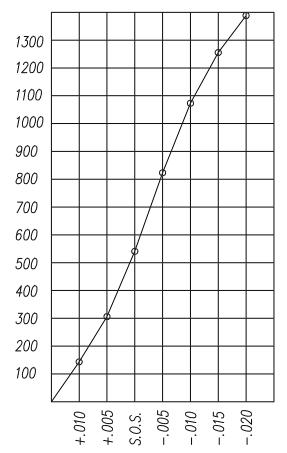
http://www.innet-tech.com

Stewart Connector Systems

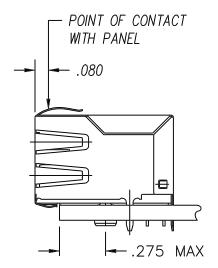
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SHEET 2 of 3 DRAWING NO. SI-40013 REV. 03

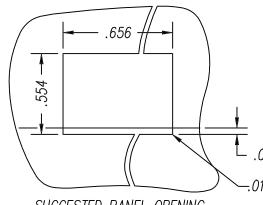
CT720034X1/24-001302



PANEL GROUNDING BEAM DEFLECTION S.O.S. = SUGGESTED OPENING SIZE



THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YFT MAINTAIN RFI IABI F GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLERANCES. FORCE VALUES ON THE GRAPH ARE GENERAL AVERAGES TAKEN AT THE POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 CLEARANCE ON THE SIDES AND TOP AND .013 ON THE BOTTOM, AT PANEL OPENING.



.000 (TOP OF PCB TO BOTTOM OF OPENING)

-.010 MAX. RADIUS(4)

SUGGESTED PANEL OPENING

CT720034X1/24-001302

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SHEET DRAWING NO. 3 of 3

-40013