

Signetics

Integrated Circuits Schottky TTL

Schottky TTL 74S Series

Introduction

Schottky TTL uses a diode clamp design to ensure the highest speed possible at TTL logic levels typically 3ns gate propagation delay and 90 MHz flip flop toggle rate. However they remain compatible with most of the popular saturated logic families and completely compatible with all 74 series families.

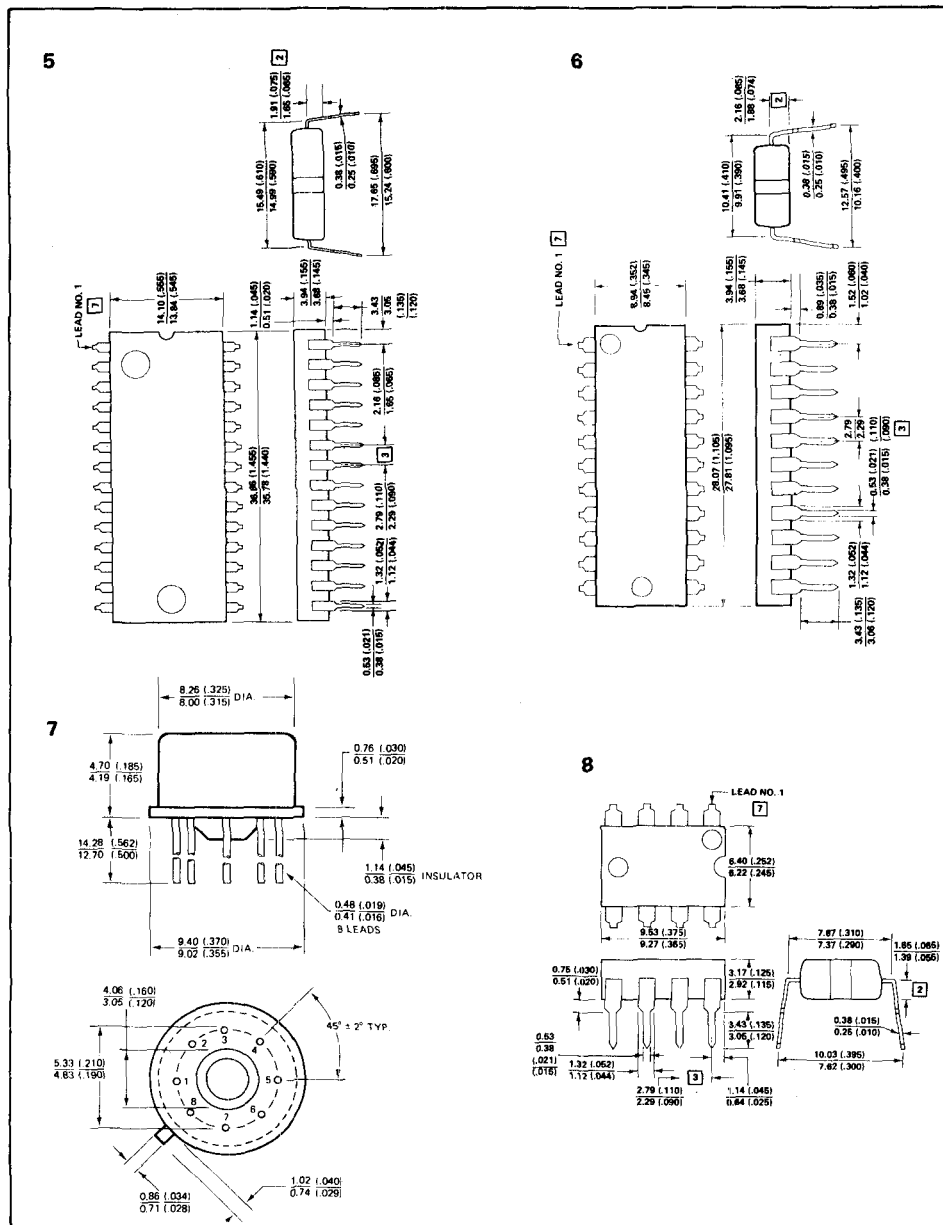
TYPE NO.	DESCRIPTION	STOCK NO.	OUTLINE DRWG. NO.	CONNECTION DIAG. NO.
N74S00N	Quad 2-Input NAND Gate	55376D	1	B1
N74S02N	Quad 2-Input NOR Gate	55377B	1	B3
N74S03N	Quad 2-Input NAND (o/c)	55378X	1	B4
N74S04N	Hex Inverter	55379R	1	B5
N74S05N	Hex Inverter (o/c)	55380A	1	B6
N74S08N	Quad 2-Input AND Gate	55381X	1	B9
N74S09N	Quad 2-Input AND Gate (o/c)	55382H	1	B10
N74S10N	Triple 3-Input NAND Gate	55383F	1	B11
N74S11N	Triple 3-Input NAND Gate	55384D	1	B12
N74S15N	Triple 3-Input AND Gate (o/c)	55385B	1	B175
N74S20N	Dual 4-Input NAND Gate	55386X	1	B19
N74S22N	Dual 4-Input NAND Gate (o/c)	55387R	1	B164
N74S32N	Quad 2-Input OR Gate	55388G	1	B26
N74S37N	Quad 2-Input NAND Buffer	55389E	1	B28
N74S38N	Quad 2-Input NAND Buffer (o/c)	55580G	1	B29
N74S40N	Dual 4-Input NAND Buffer	55581E	1	B30
N74S51N	Dual 2-Wide 2-Input AOI Gate	55582C	1	B39
N74S64N	4-2-3-2 Input AOI Gate	55583A	1	B177
N74S65N	4-2-3-2 Input AOI Gate	55584X	1	B177
N74S74N	Dual D-Type Edge Triggered F/F	55585H	1	B53
N74S85N	4-Bit Magnitude Comparator	55586F	2	B59
N74S86N	Quad 2-Input Exclusive OR	55587D	1	B60
N74S89BN	64-Bit Ram	55588B	2	B179
N74S112N	Dual J-K Negative Edge Triggered F/F	55589X	2	B180
N74S113N	Dual J-K Negative Edge Triggered F/F	55590C	1	B181
N74S114N	Dual J-K Negative Edge Triggered F/F	55591A	1	B182
N74S133N	13-Input NAND Gate	55592X	2	B183
N74S134N	12-Input NAND Gate Tri-State Outputs	55593H	2	B184
N74S135N	Quad Exclusive OR/NOR Gate	55594F	2	B185
N74S138N	3 to 1 of 8 Line Decoder/Demux	55595D	2	B186
N74S139N	Dual 2 to 100F4 Line Decoder Demux	55596B	2	B187
N74S140N	Dual 14-Input NAND Line Driver	55597X	1	B188
N74S151N	8-Line to 1-Line Mux	55598R	2	B83
N74S153N	Dual 4-Line to 1-Line Mux	55599G	2	B84
N74S157N	Quad 2-Input Data Selector	55600R	2	B88
N74S158N	Quad 2-Input Data Selector	55601G	2	B89
N74S172N	16-Bit Multiple Port Shift Register	55602E	3	B189
N74S174N	Hex D-Type F/F with Clear	55603C	2	B99
N74S175N	Quad D-Type Edge Triggered F/F	55604A	2	B100
N74S181N	4-Bit ALU	55605X	3	B106
N74S182N	Look Ahead Carry Generator	55606H	2	B107
N74S189N	64-Bit Ram	55607F	2	B190
N74S194N	4-Bit Bidirectional Universal Shift Reg.	55608D	2	B112
N74S195N	4-Bit Parallel Access Shift Reg.	55609B	2	B113
N74S200N	285-Bit Ram 3-State o/p	55610E	2	B191
N74S209N		55611C		B192
N74S251N	Data Selector/Mux-3 State o/p	55612A	2	B193
N74S253N	Dual 4-Line to 1-Line Data Selector/Mux	55613X	2	B194
N74S257N	Quad 2-Line to 1-Line Data Selector/Mux	55614H	2	B195
N74S258N	Quad 2-Line to 1-Line Data Selector/Mux	55615F	2	B196



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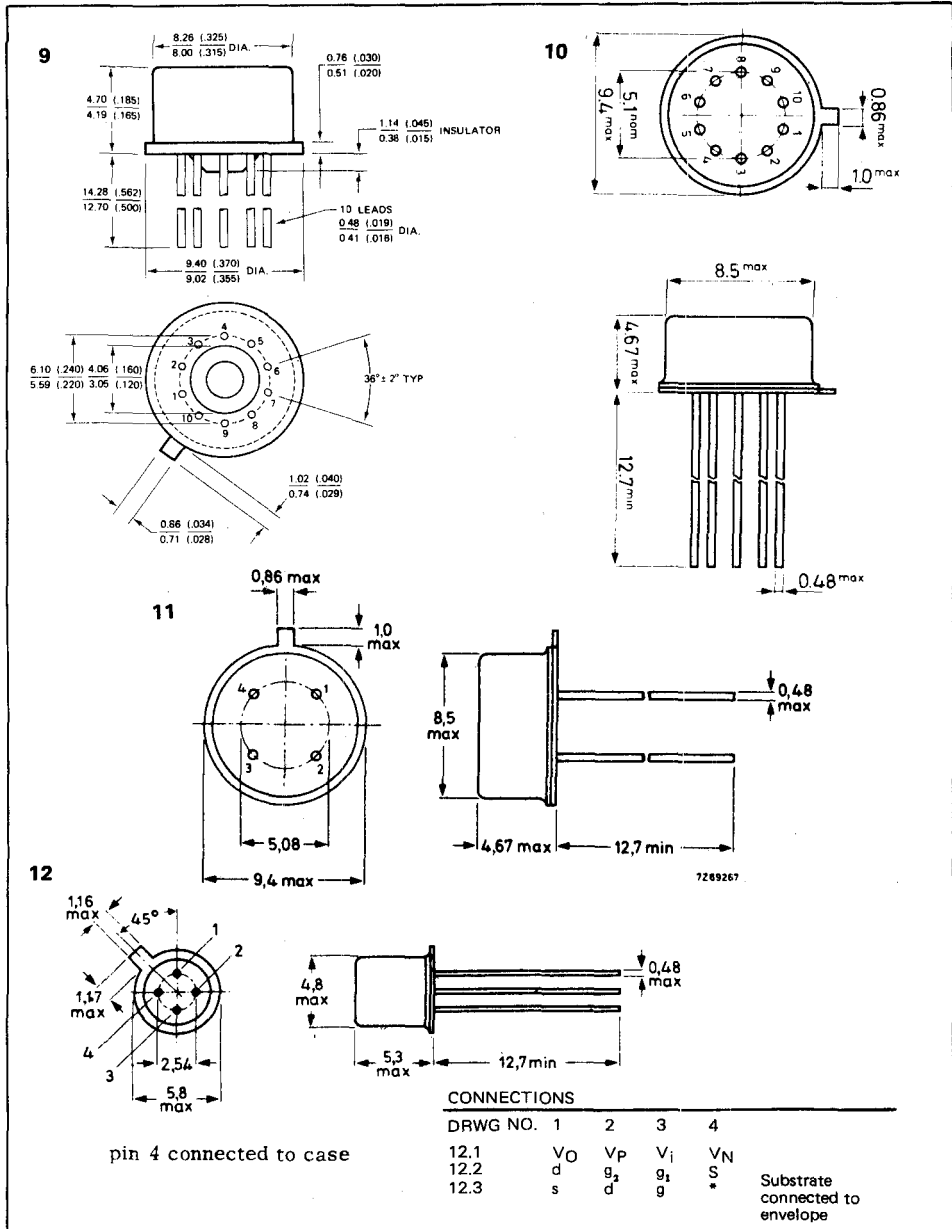
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Outline Drawings



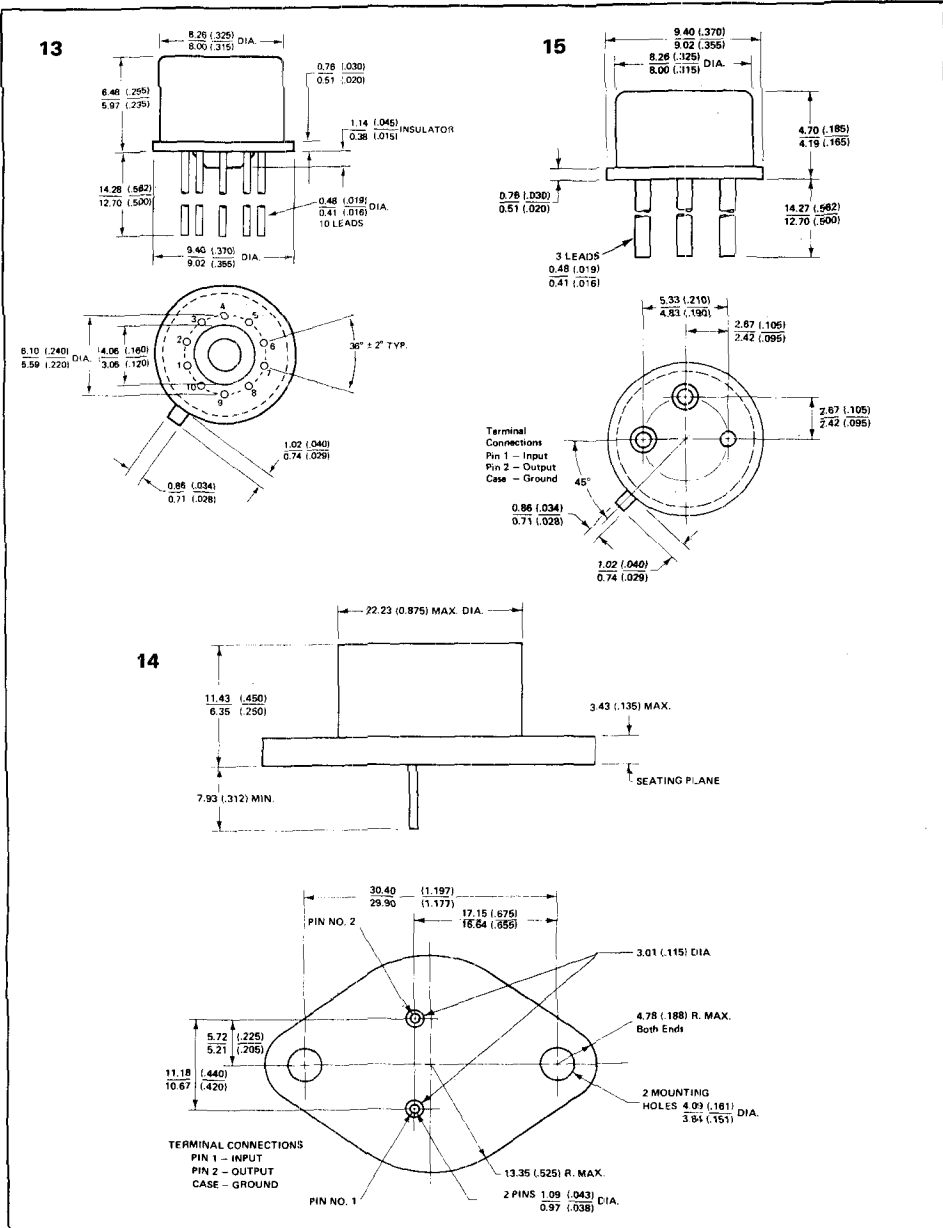
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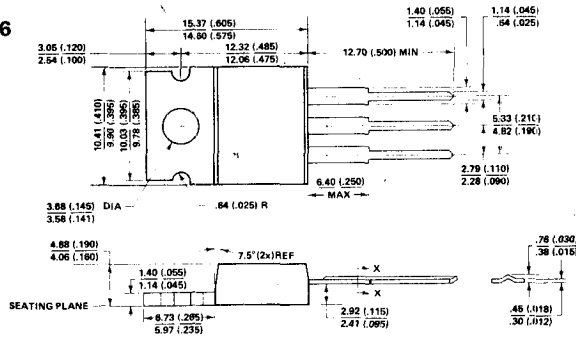


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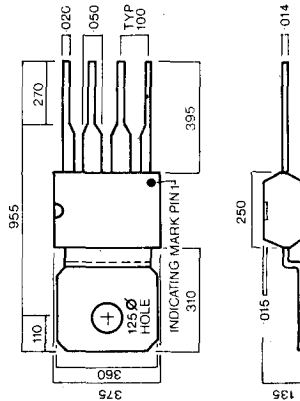
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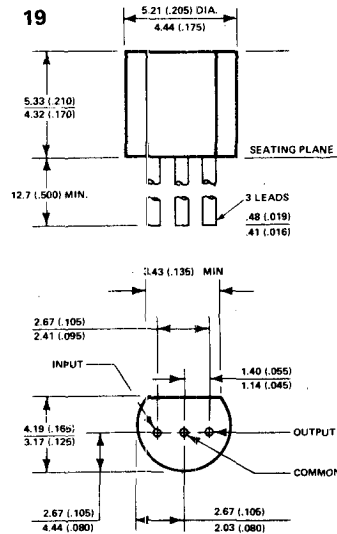
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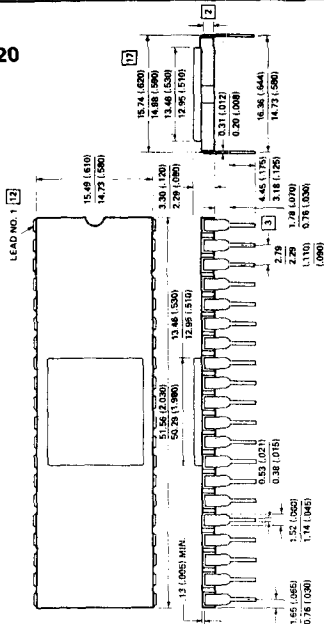
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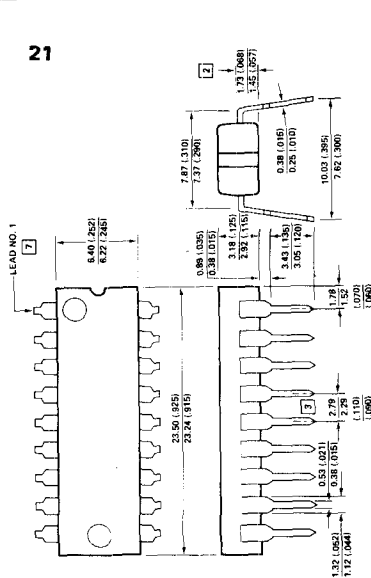
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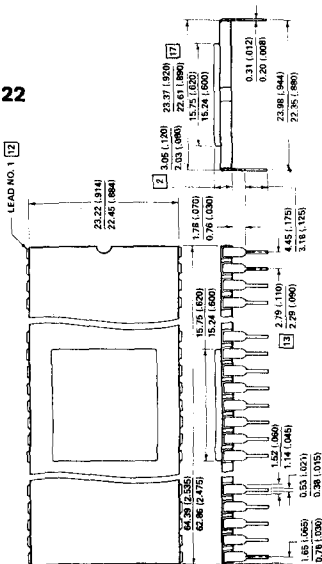
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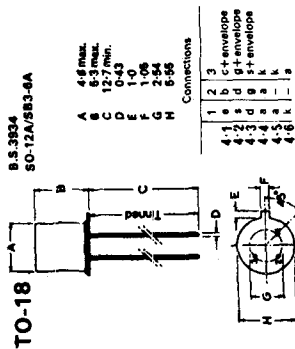
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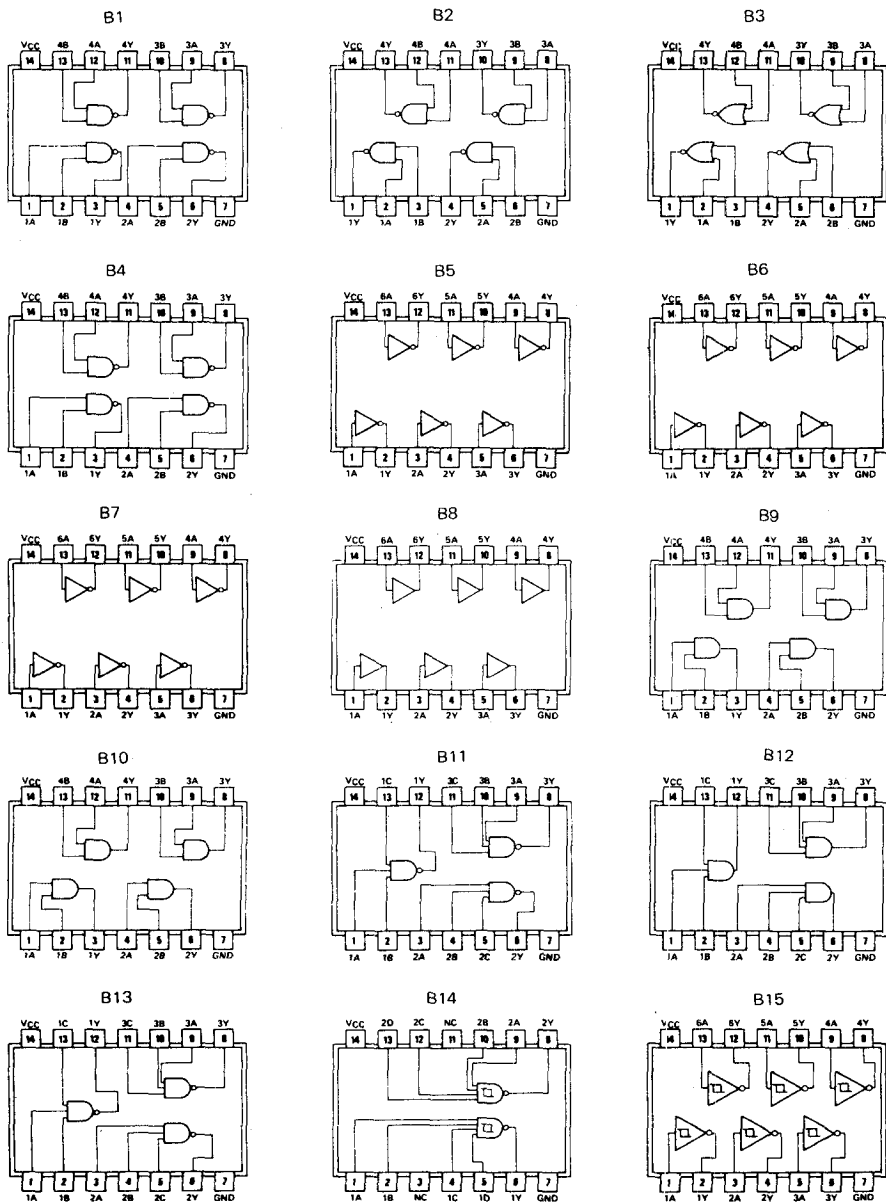


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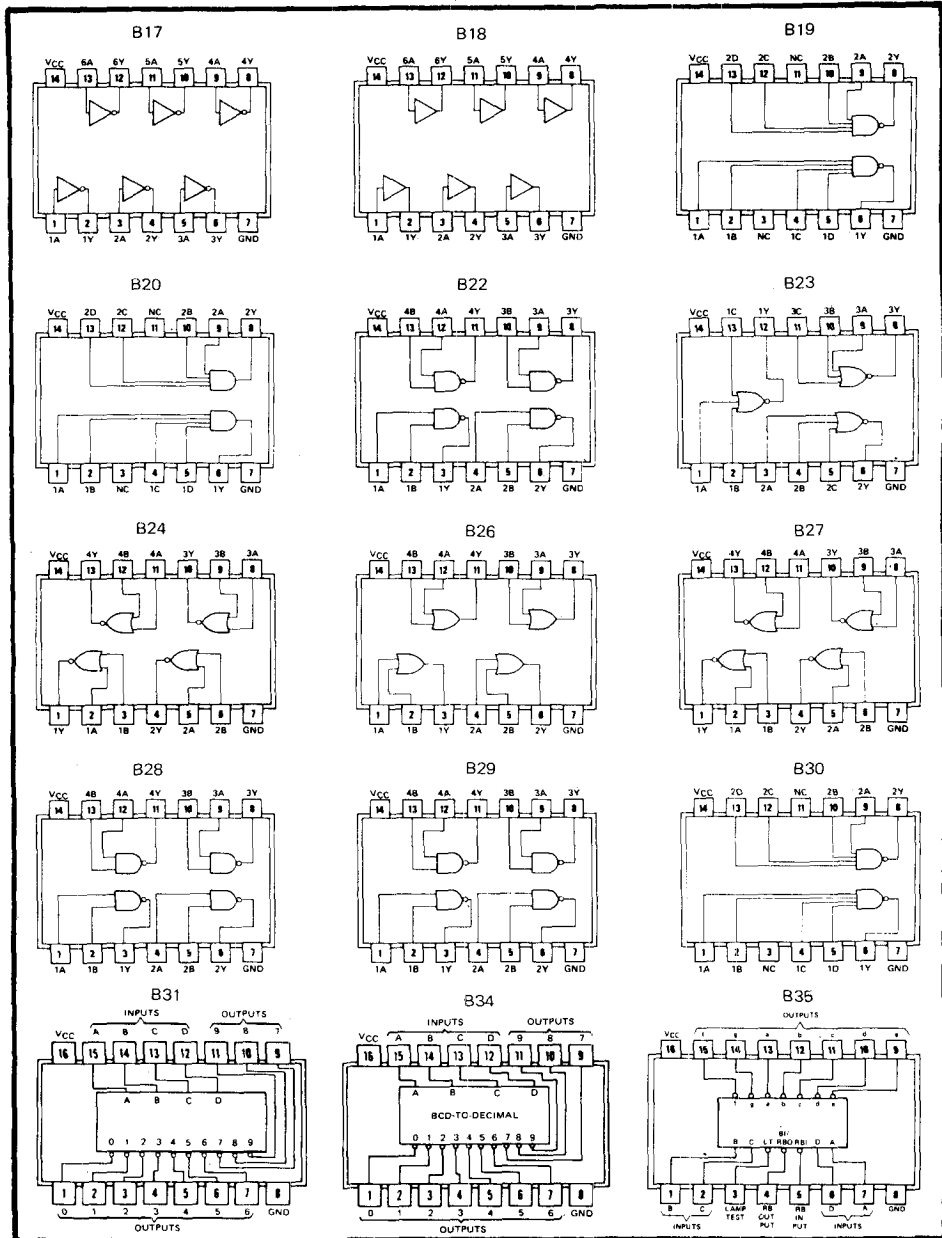
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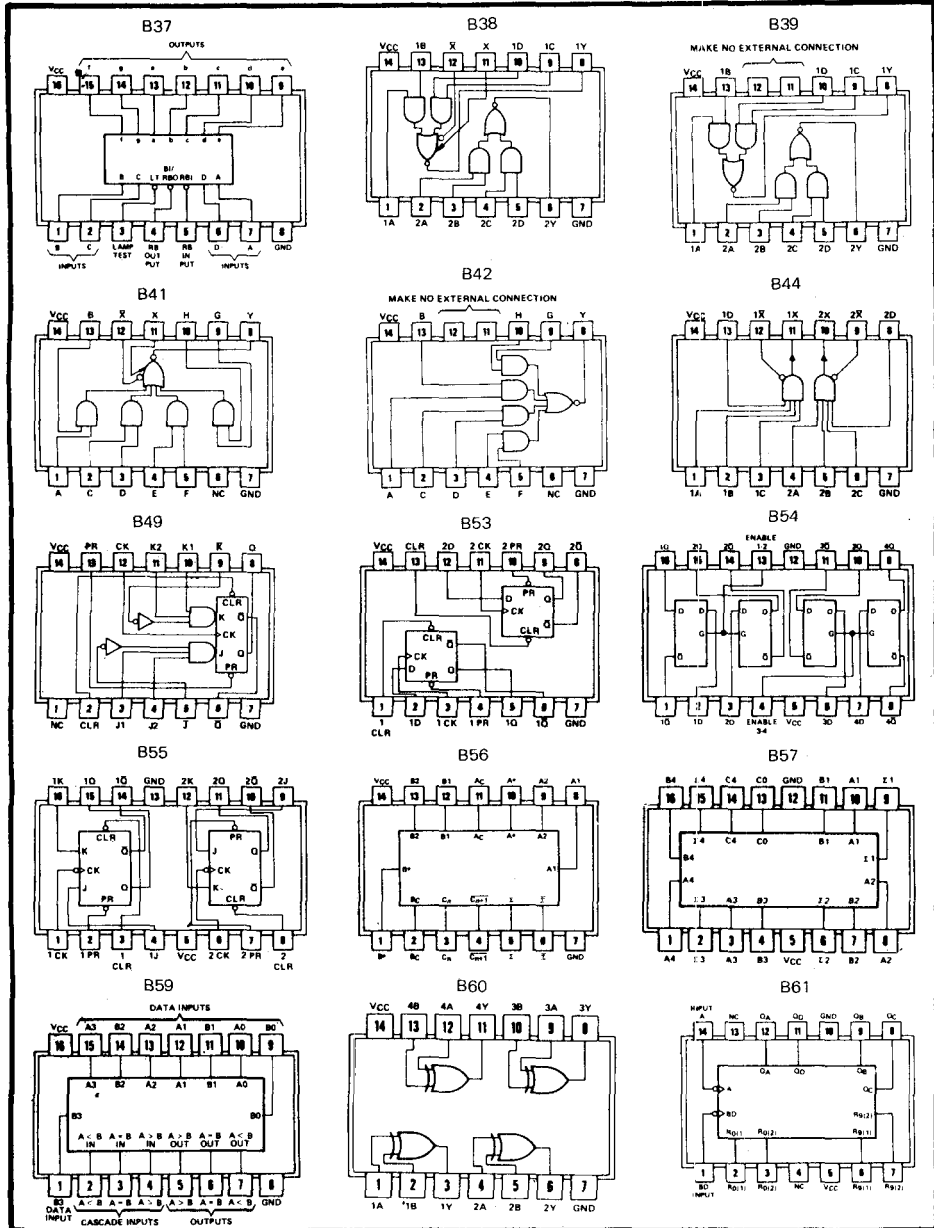
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Connection Diagram



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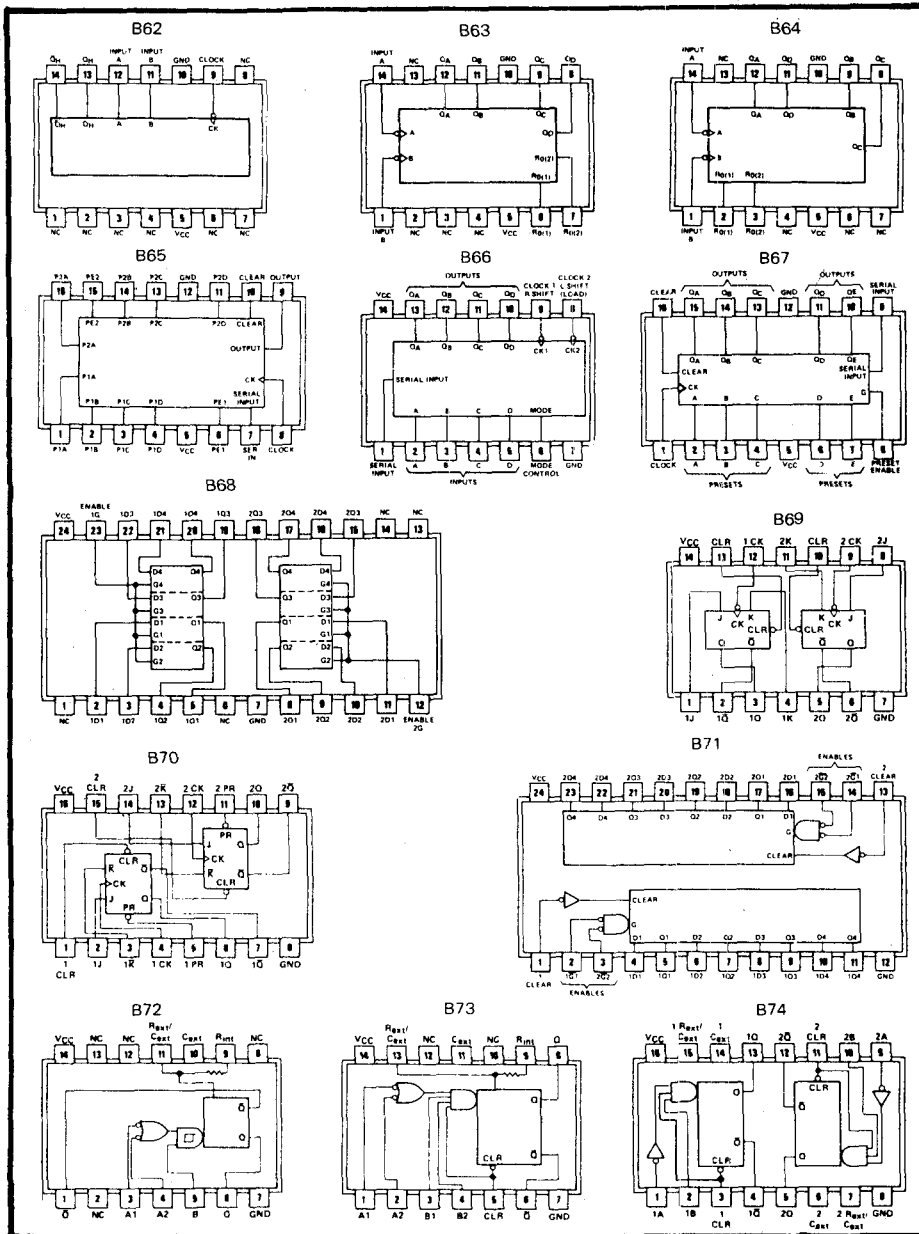


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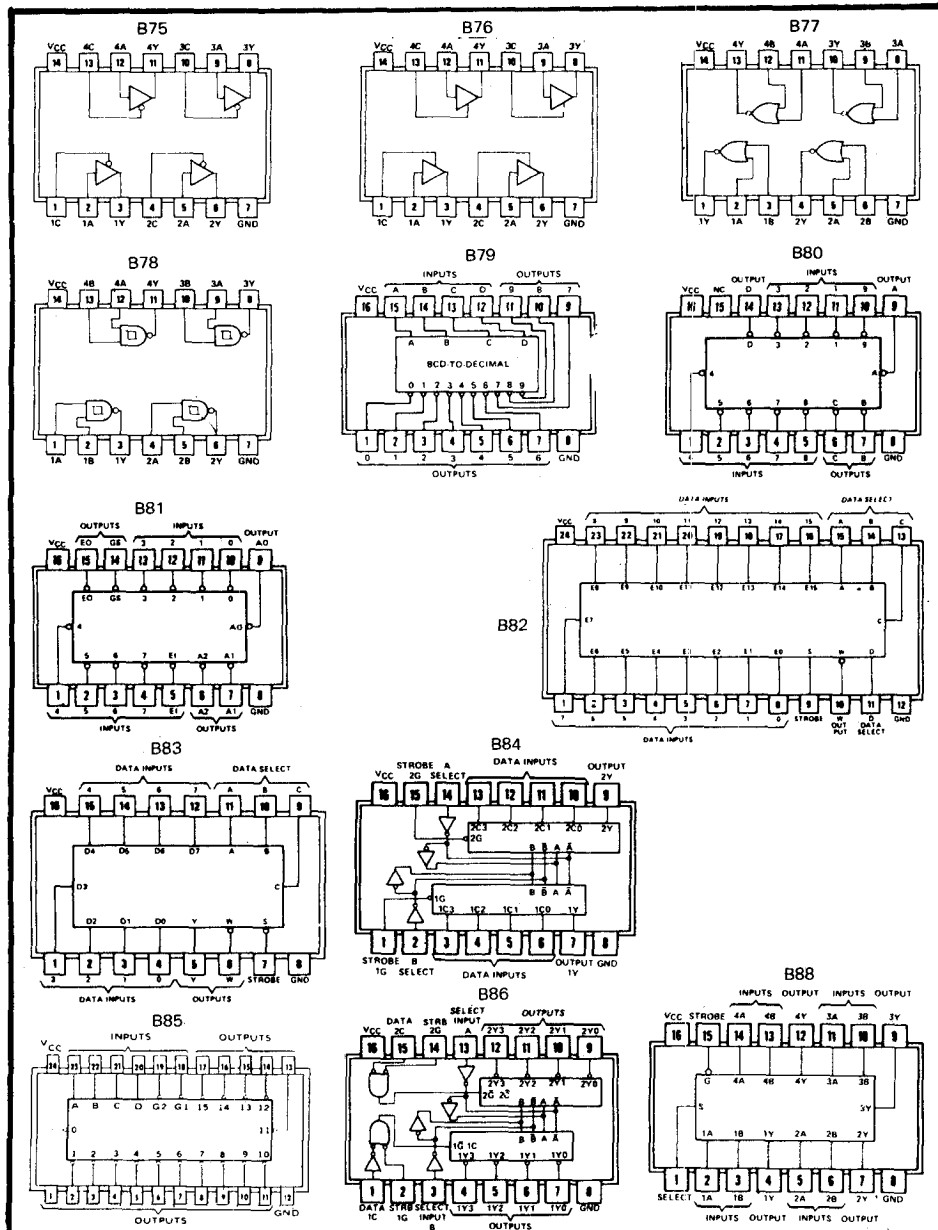


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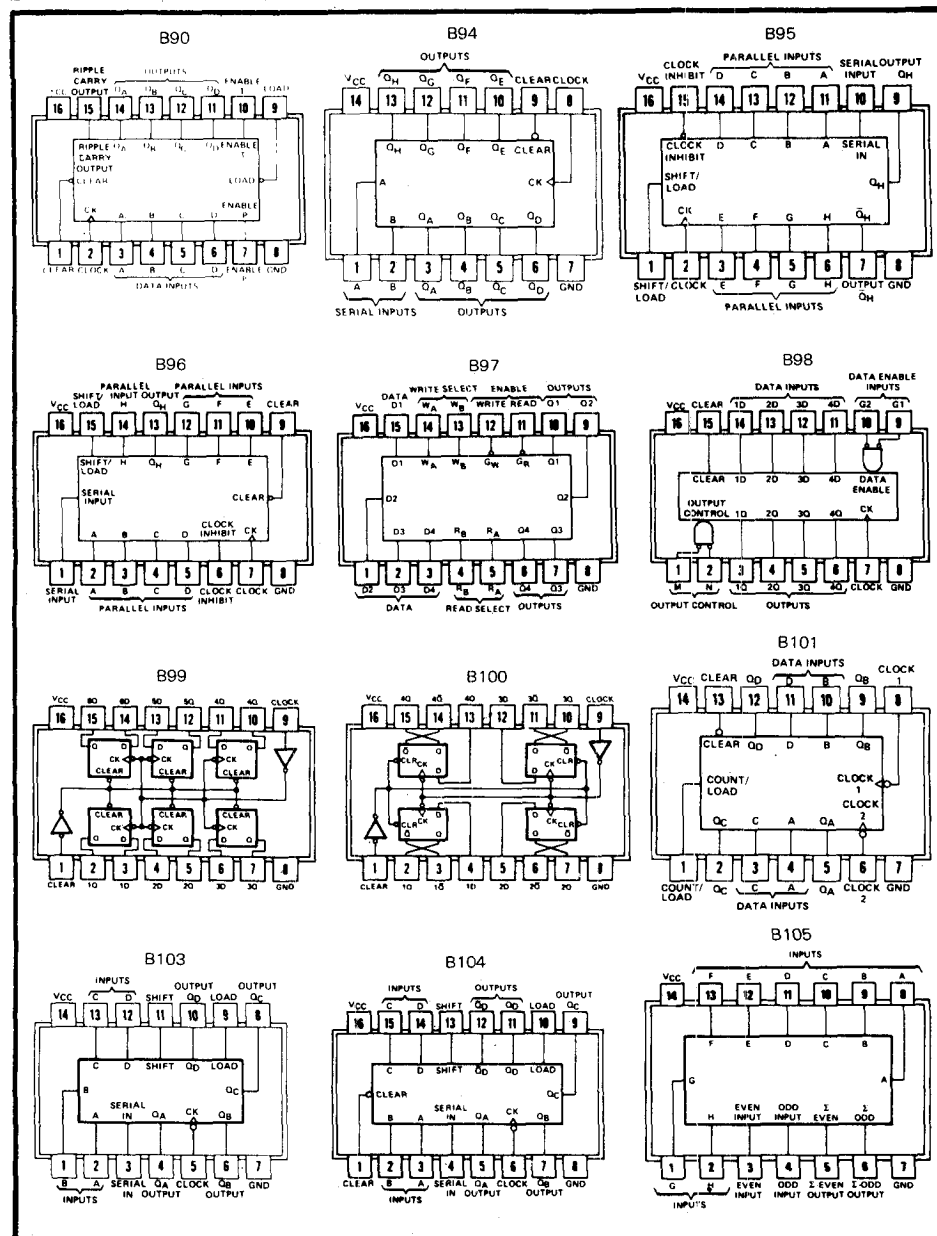
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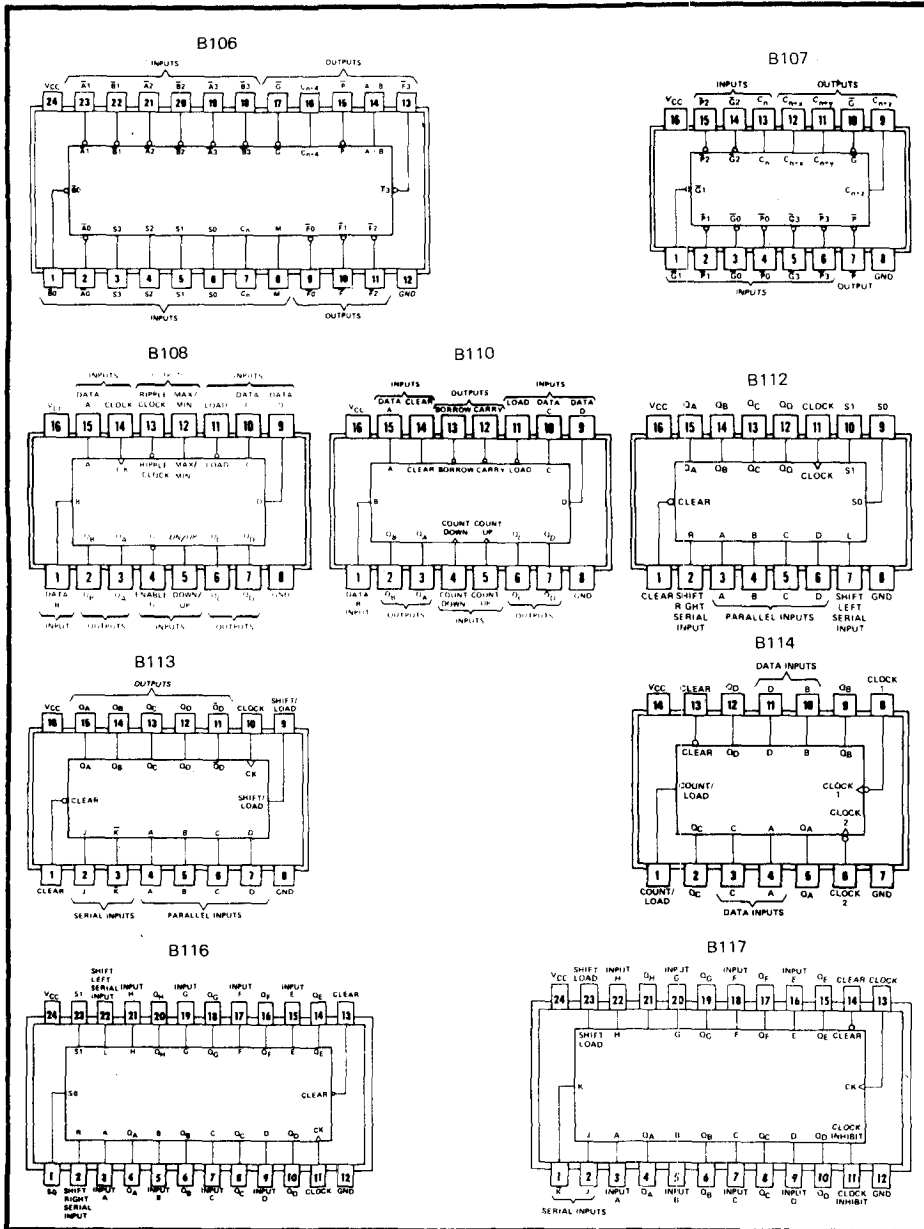
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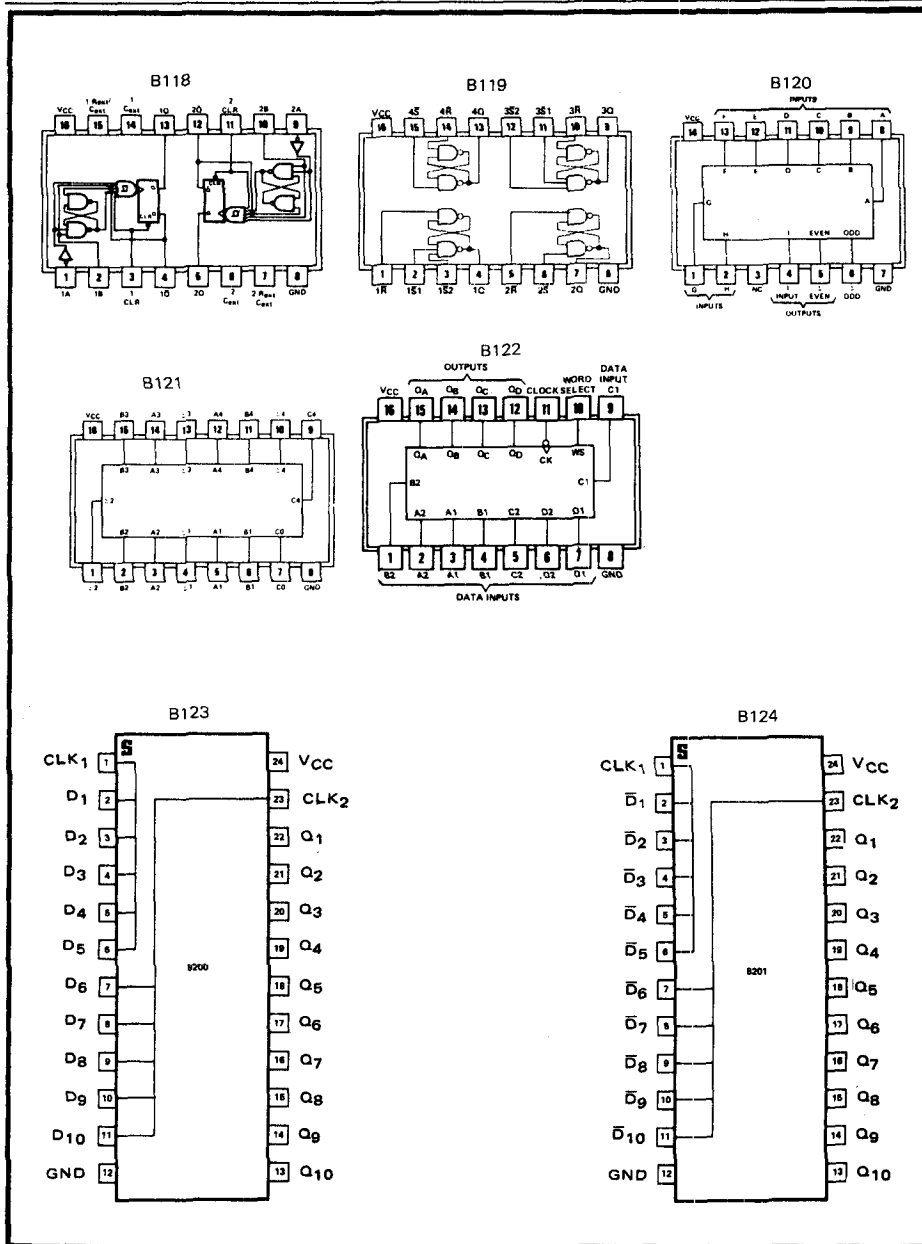
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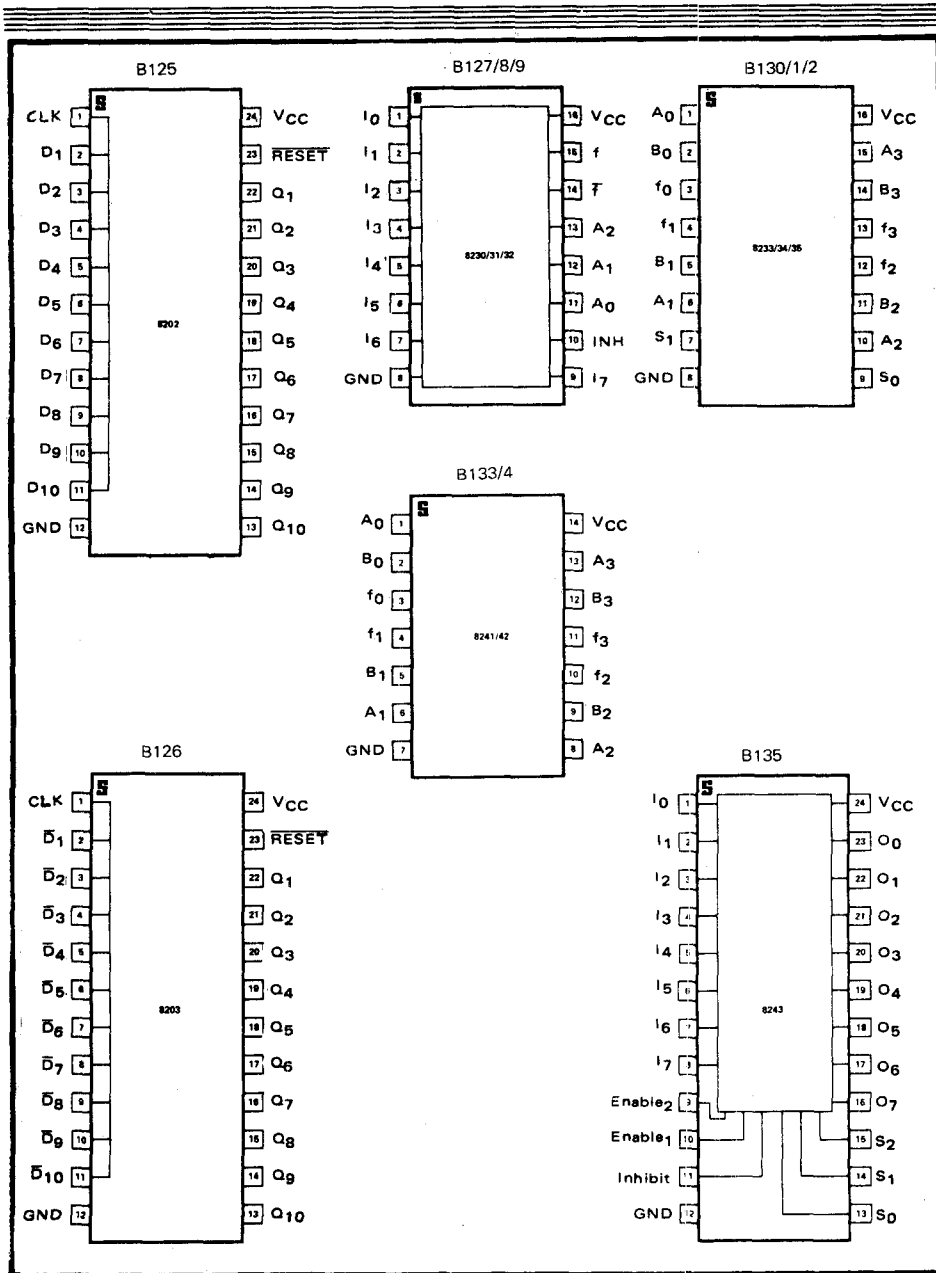
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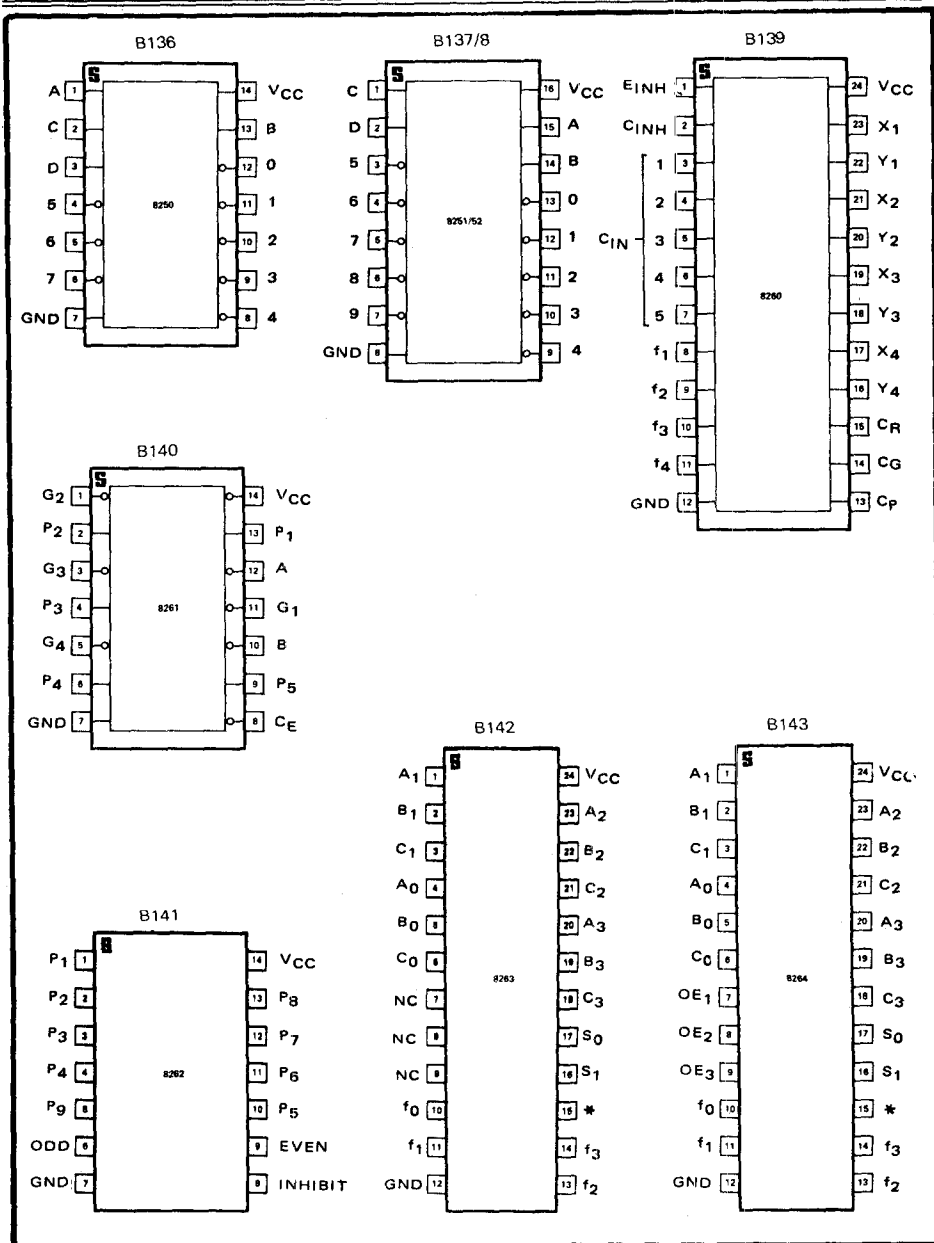
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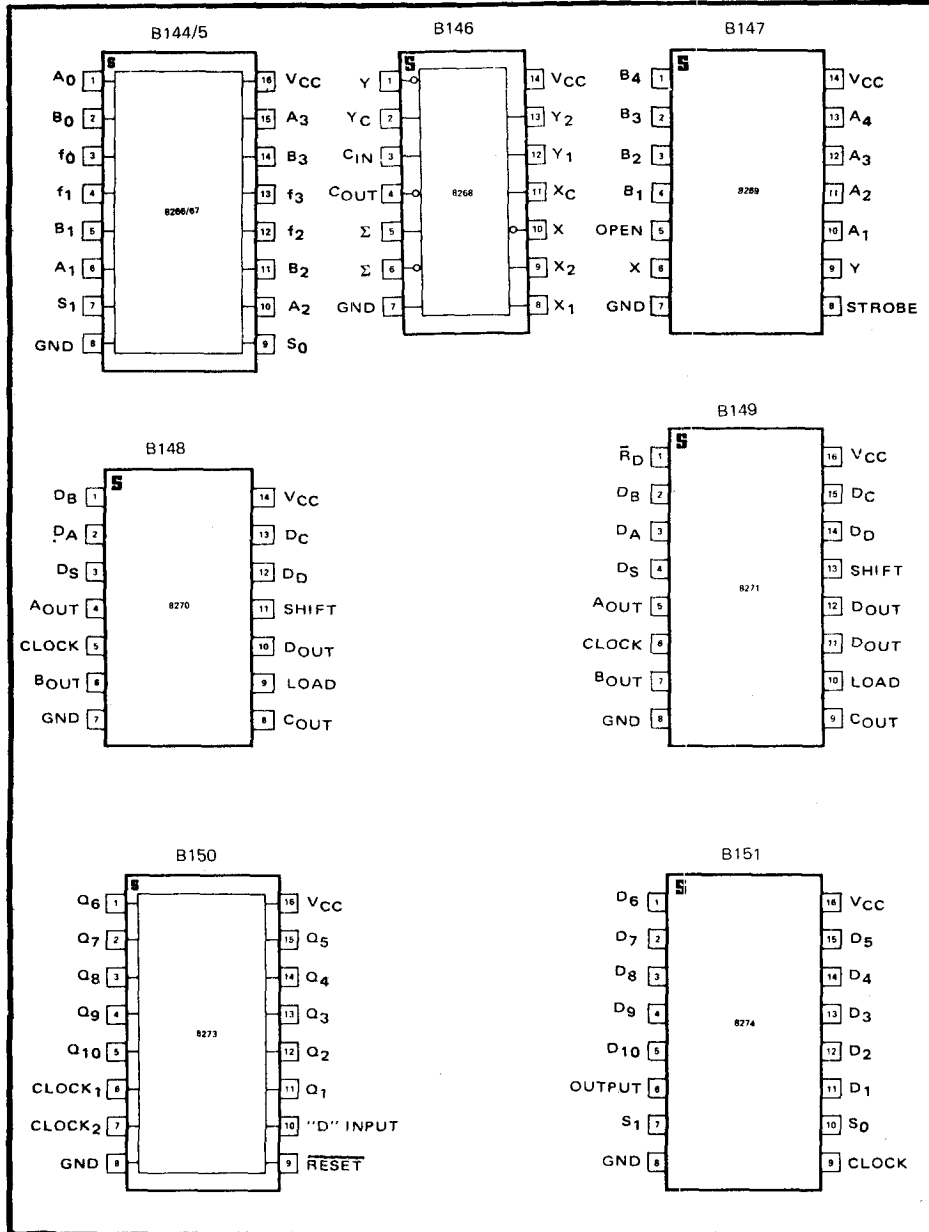
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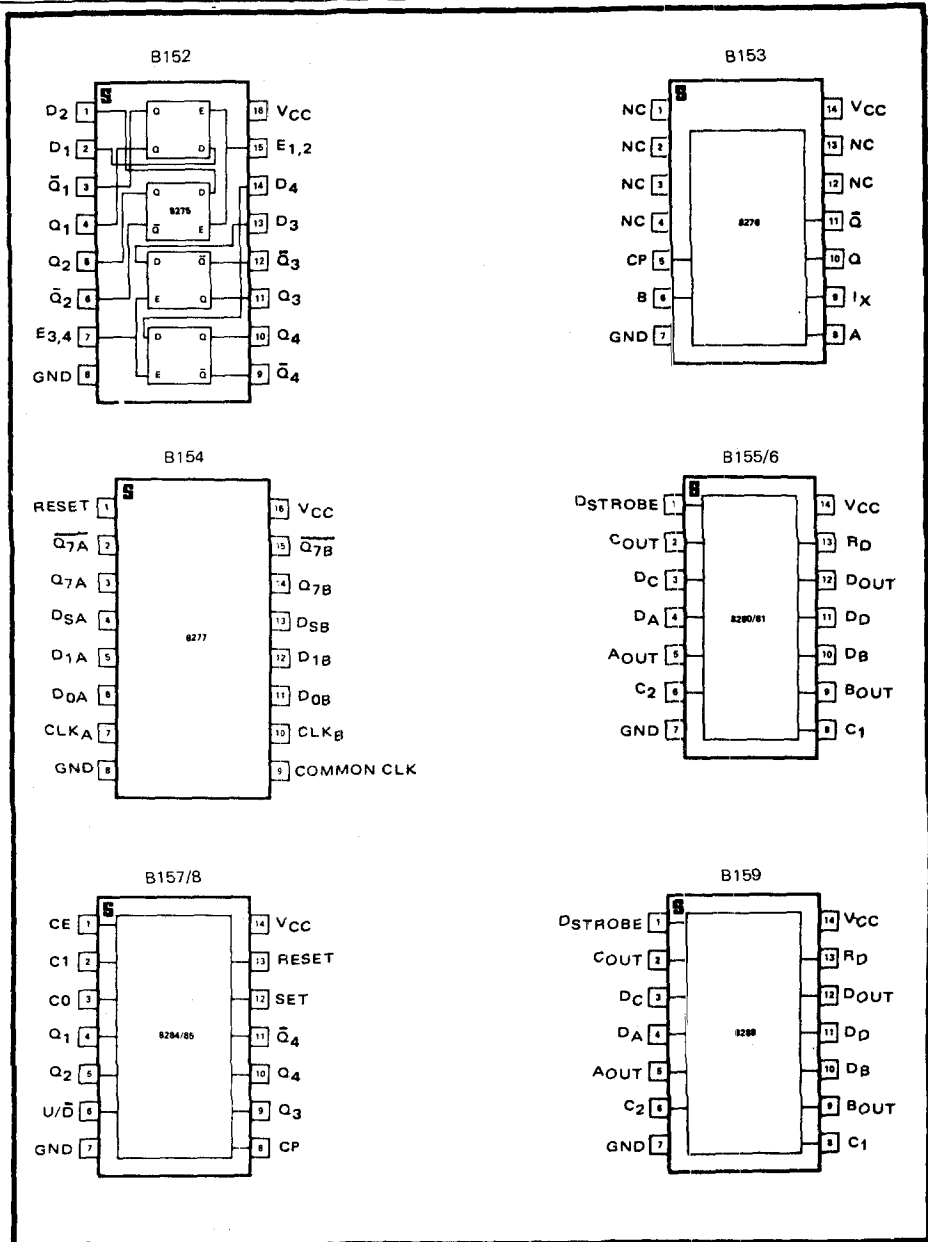
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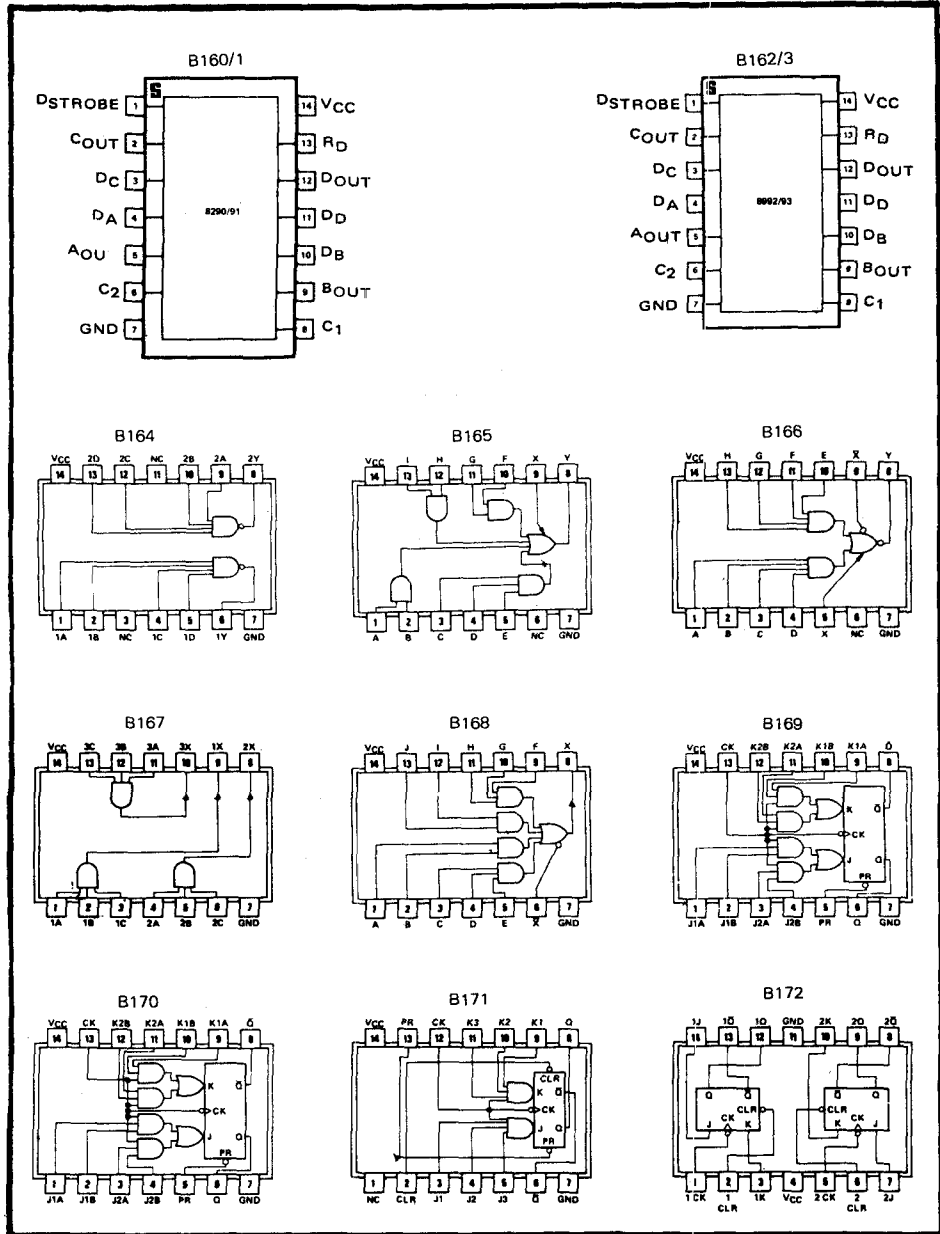
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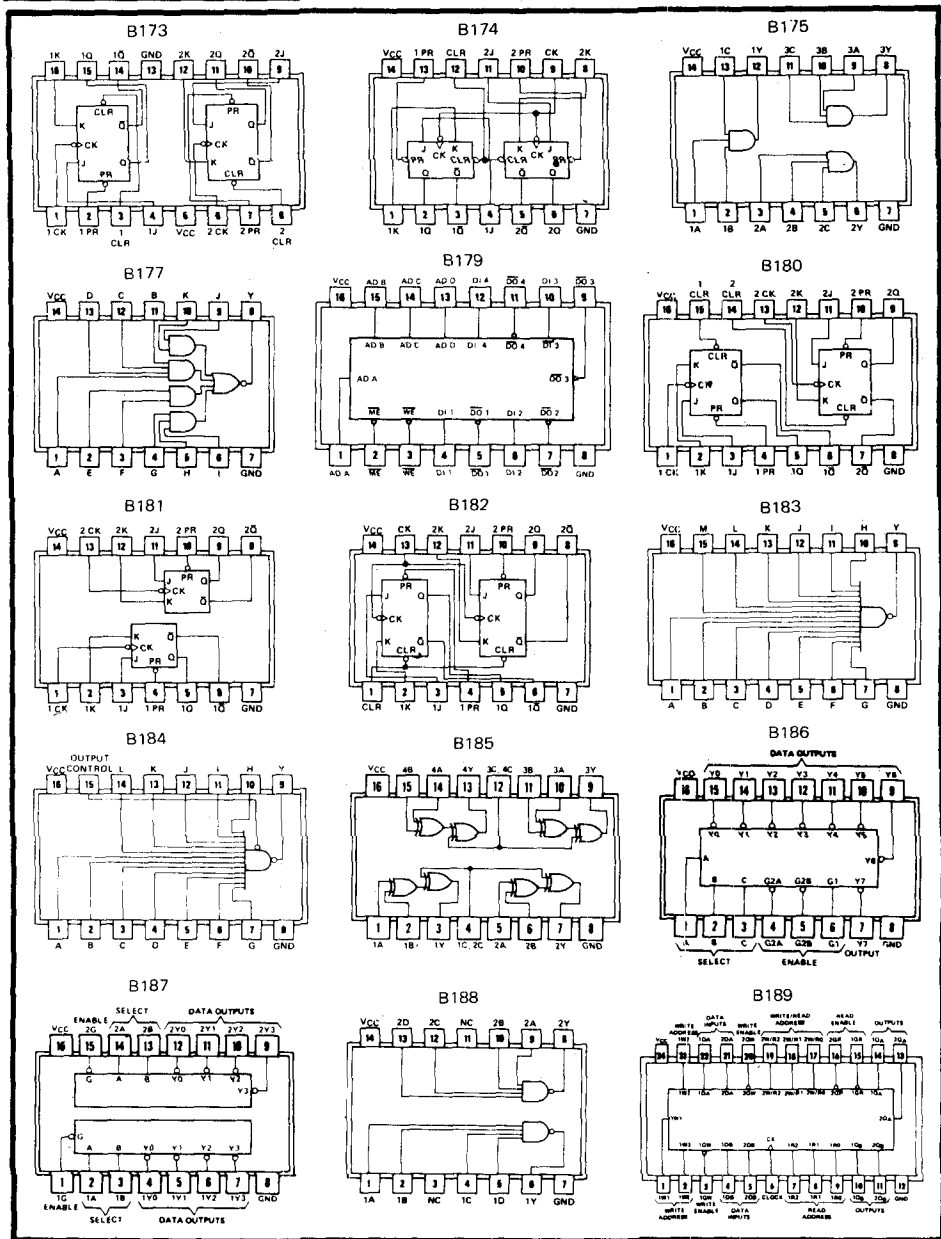


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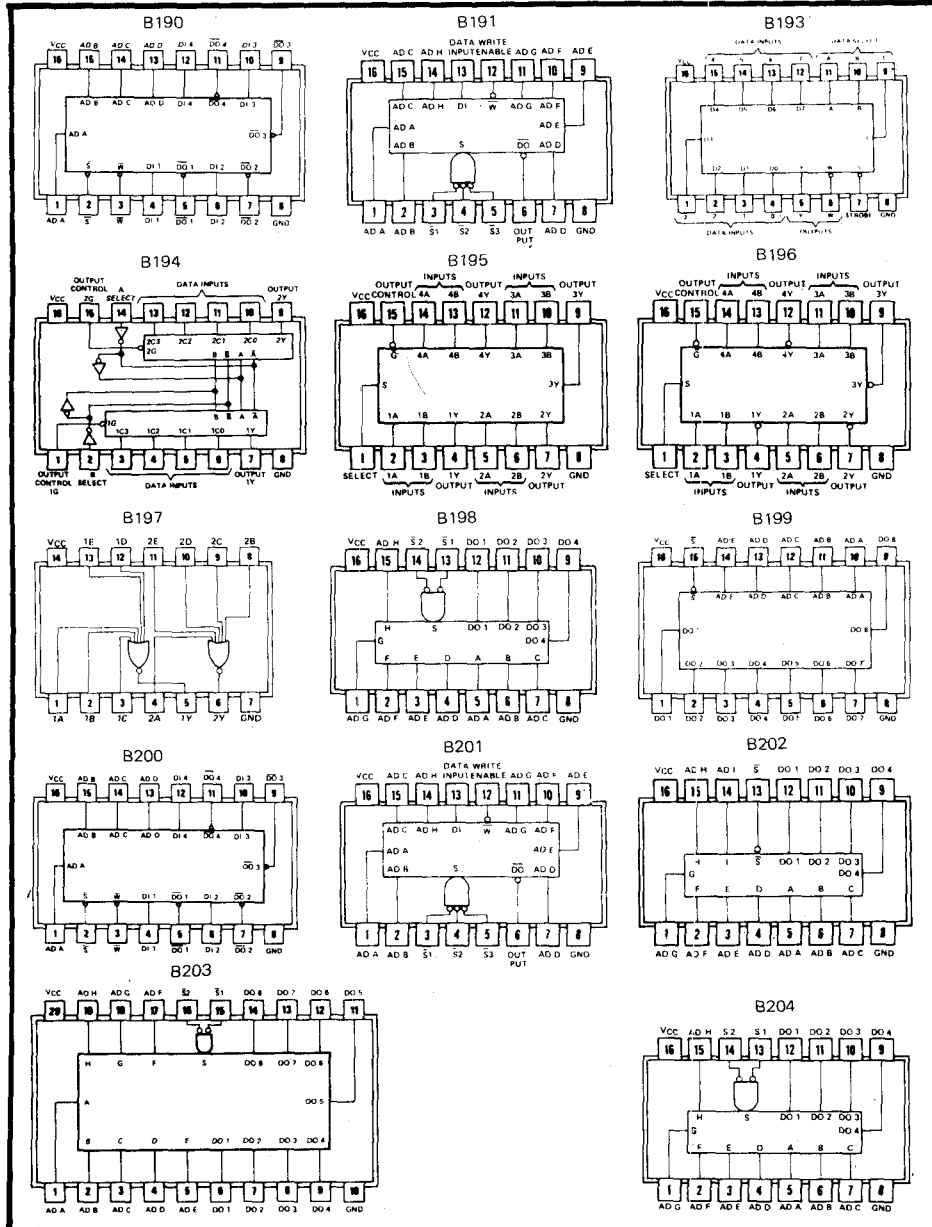
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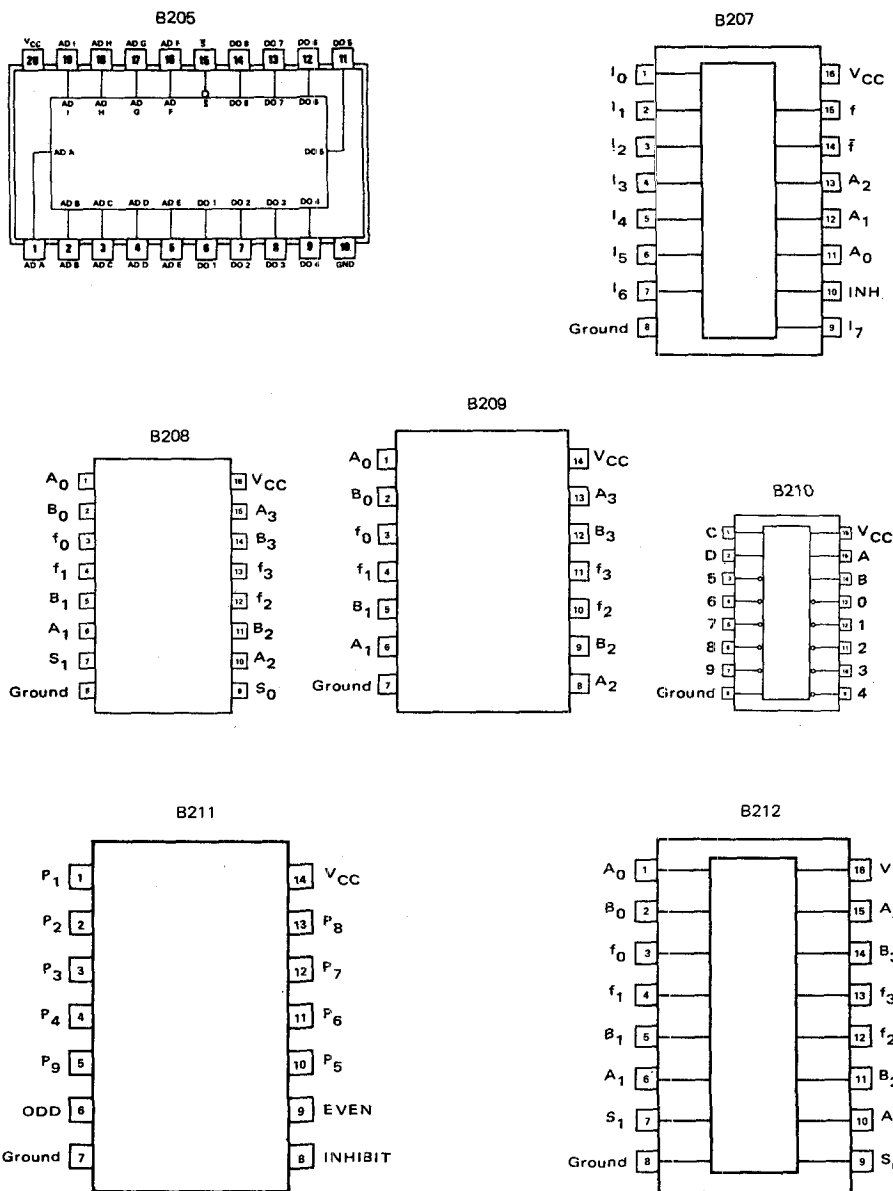
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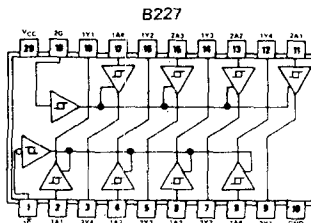
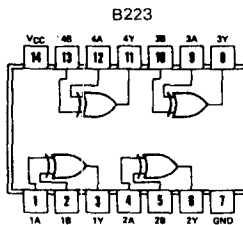
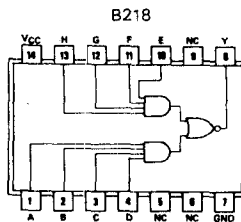
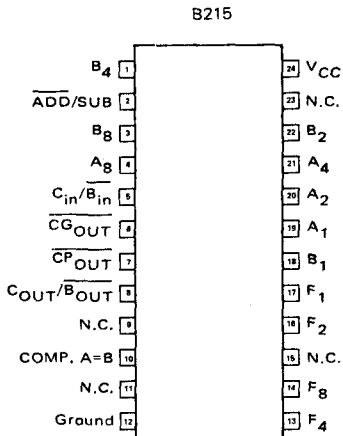
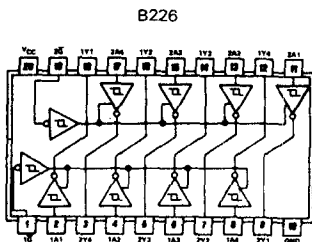
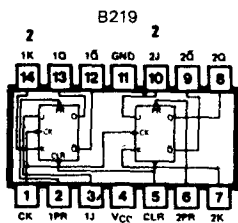
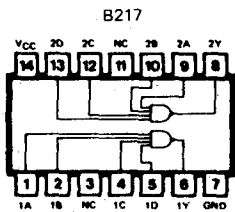
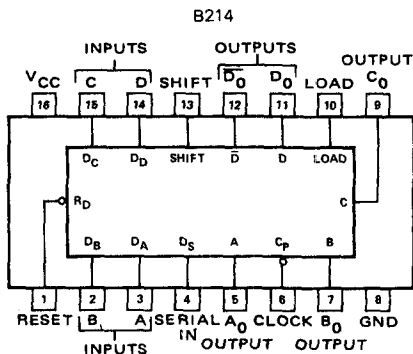
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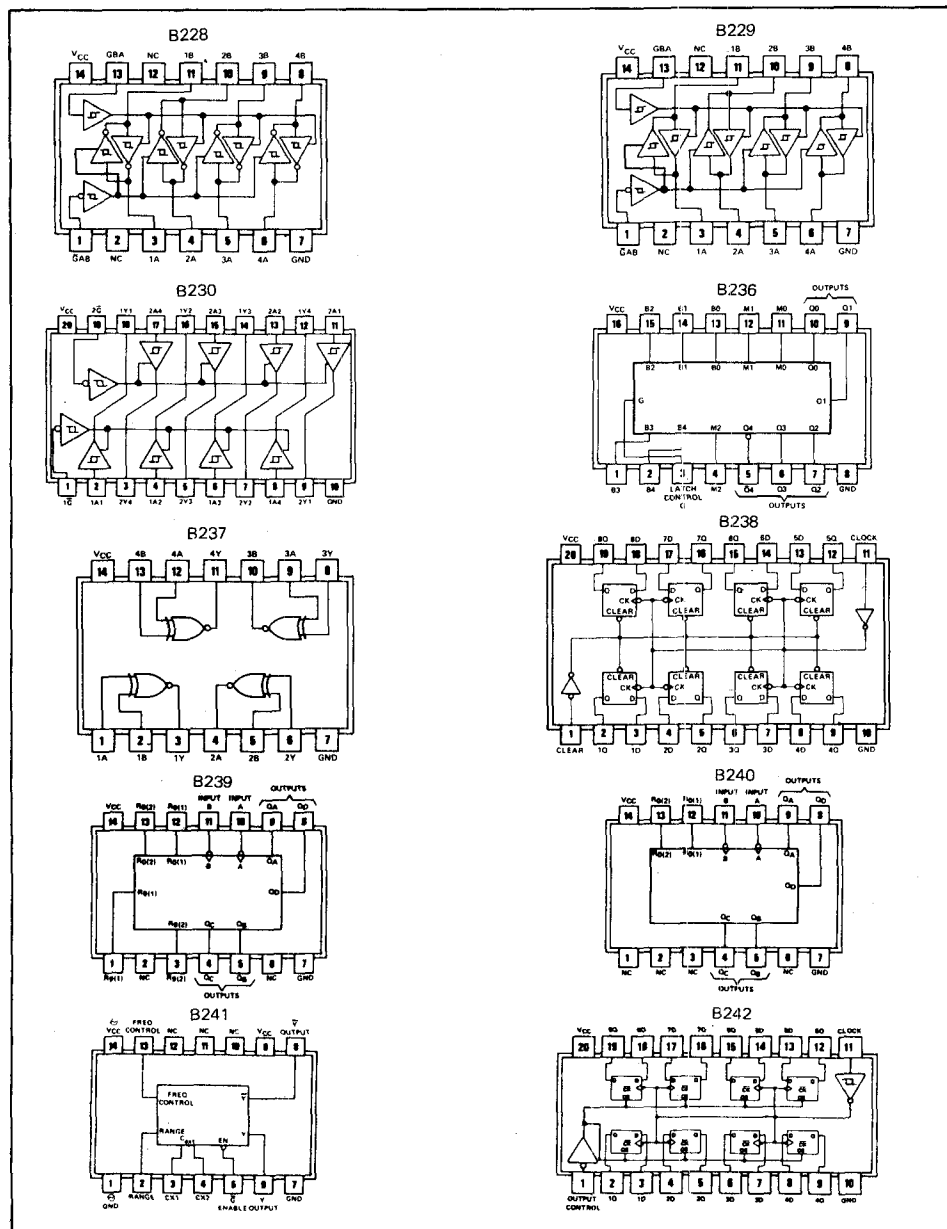
Connection Diagram

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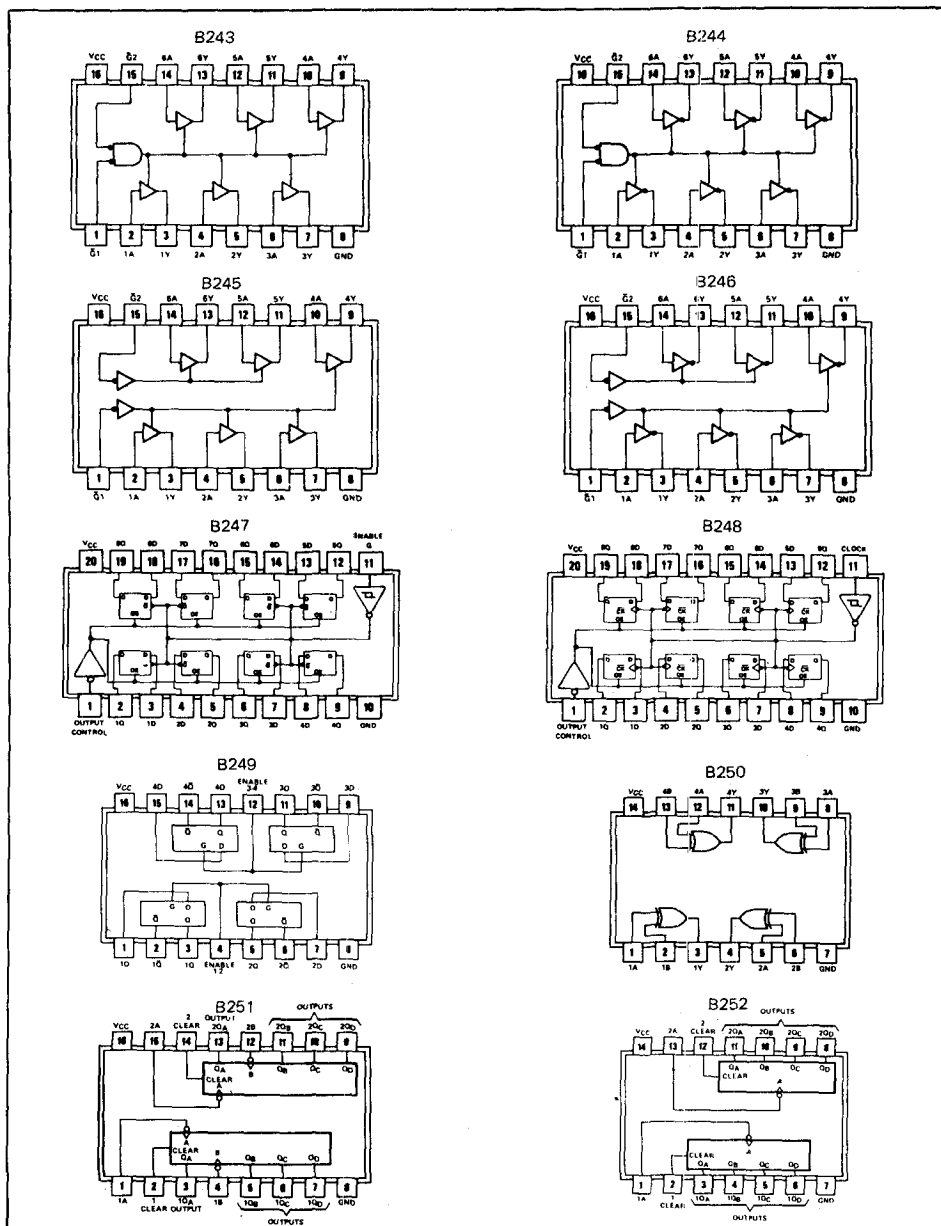


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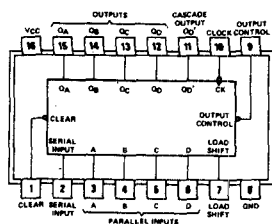
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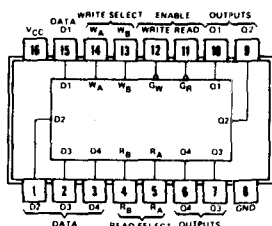
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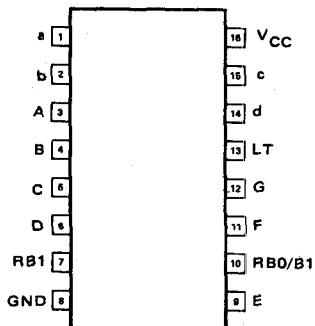
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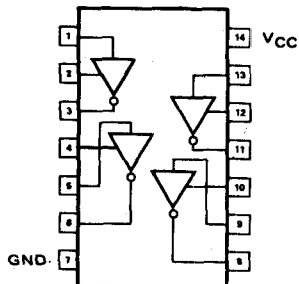
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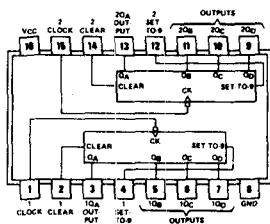
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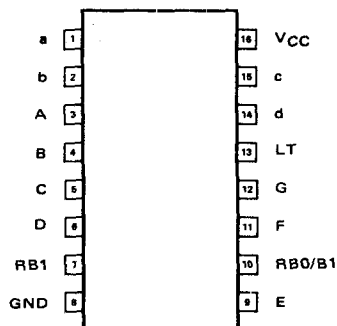
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