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DAC1408D750W1

DAC1408D750W1 demo board with Virtex 5
FPGA

IDT's DAC1408D750W1 demo board is suitable for dynamic performance evaluations from low to high outputs frequency configuration. The FPGA eases the evaluation and analysis of the DAC dynamic performance and enables usage of the JESD204A full features set (e.g. Multiple DAC Synchronization)

Features

- SW interface to easily program DAC parameters (NCO, offset, interpolation factor, PLL, etc.)On-board AQM + LO synthesizerSMA connector for DAC and AQMexternal clock input signalsOn-board clock generator for DAC and FPGAOn-board Virtex 5 FPGA for data generation2 DACs on same PCB to easily evaluate Multi-Devices Synchronization



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	Demo Board Software DAC1x08DW1	Manual - Software	ZIP	2.26 MB	07/18/2012

Related Products

Product ID	Title
DAC1408D750HN	Dual 14-bit DAC; up to 750 Msps; 2x, 4x or 8x interpolating with JESD204A interface

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