

August 2003 Revised December 2004

# FSA4157 • FSA4157A Low Voltage 1 Ω SPDT Analog Switch

#### **General Description**

FSA4157 and FSA4157A are high performance Single Pole/Double Throw (SPDT) analog switches. Both devices feature ultra low  $R_{\text{ON}}$  of 1.15  $\Omega$  maximum at 4.5V  $V_{\text{CC}}$  and will operate over the wide  $V_{\text{CC}}$  range of 1.65V to 5.5V for FSA4157, and 2.7V to 5.5V for FSA4157A. The device is fabricated with sub-micron CMOS technology to achieve fast switching speeds and is designed for break-before-make operation. The select input is TTL level compatible.

The FSA4157A features very low quiescent current even when the control voltage is lower than the  $\rm V_{CC}$  supply. This feature services the mobile handset applications very well allowing for the direct interface with baseband processor general purpose I/Os.

#### **Features**

- $\blacksquare$  FSA4157A features lower  $I_{CC}$  when the S input is lower than  $V_{CC}$
- $\blacksquare$  Maximum 1.15  $\Omega$  On Resistance (R\_ON) at 4.5V V\_CC
- $\blacksquare 0.3~\Omega$  max R<sub>ON</sub> flatness at 4.5V V<sub>CC</sub>
- Space saving MicroPak<sup>™</sup> and SC70 6-lead surface mount packages
- $\blacksquare$  Broad  $V_{CC}$  operating range:
  - FSA4157: 1.65V to 5.5V
  - FSA4157A: 2.7V to 5.5V
- Fast turn-on and turn-off time
- Break-before-make enable circuitry
- Over-voltage tolerant TTL compatible control circuitry

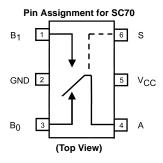
#### **Ordering Code:**

Order Number	Package Number	Product Code Top Mark	Package Description	Supplied As
FSA4157P6	MAA06A	A57	6-Lead SC70, EIAJ SC88, 1.25mm Wide	250 Units on Tape and Reel
FSA4157P6X	MAA06A	A57	6-Lead SC70, EIAJ SC88, 1.25mm Wide	3k Units on Tape and Reel
FSA4157P6X_NL (Note 1)	MAA06A	A57	6-Lead SC70, EIAJ SC88, 1.25mm Wide	3k Units on Tape and Reel
FSA4157L6X	MAC06A	EG	6-Lead MicroPak, 1.0mm Wide	5k Units on Tape and Reel
FSA4157AP6	MAA06A	B57	6-Lead SC70, EIAJ SC88, 1.25mm Wide	250 Units on Tape and Reel
FSA4157AP6X	MAA06A	B57	6-Lead SC70, EIAJ SC88, 1.25mm Wide	3k Units on Tape and Reel
FSA4157AL6X	MAC06A	EU	6-Lead MicroPak, 1.0mm Wide	5k Units on Tape and Reel

Note 1: "\_NL" indicates lead-free package (per JEDEC J-STD-020B). Device available in Tape and Reel only.

 $\label{eq:microPak} \mbox{MicroPak}^{\mbox{\tiny TM}} \mbox{ is a trademark of Fairchild Semiconductor Corporation}.$ 

# **Analog Symbols**



# Pin Assignment for MicroPak S 1 6 B1 VCC 2 5 GND A 3 4 B0

(Top Through View)

## **Truth Table**

Control Input (S)	Function
L	B <sub>0</sub> Connected to A
Н	B <sub>1</sub> Connected to A

H = HIGH Logic Level L = LOW Logic Level

# **Pin Descriptions**

Pin Name	Description
A, B <sub>0</sub> , B <sub>1</sub>	Data Ports
S	Control Input

1.65V to 5.5V

2.7V to 5.5V

0V to V<sub>CC</sub>

0V to  $V_{CC}$ 

350°C/W

-40°C to 85°C

330°C/W (estimated)

## **Absolute Maximum Ratings**(Note 2)

**Recommended Operating** Conditions (Note 4)

Supply Voltage (V<sub>CC</sub>)

Operating Temperature

SC70 6L Package

MicroPak 6L Package

Thermal Resistance  $\theta_{\text{JA}}$  in still air

Supply Voltage (V<sub>CC</sub>) -0.5V to +6.0V DC Switch Voltage (Note 3)

–0.5V to  $V_{CC}$  +0.5V DC Input Voltage (V<sub>IN</sub>) (Note 3) -0.5V to +6.0V

FSA4157 DC Input Diode Current -50 mA FSA4157A Switch Current 200 mA Control Input Voltage Peak Switch Current Switch Input Voltage

400 mA

(Pulse at 1 mS duration,

<10% Duty Cycle)

Power Dissipation (PD) @ 85°C

SC70 6L Package 180 mW MicroPak 6L Package 180 mW

Storage Temperature Range (T<sub>STG</sub>) -65°C to +150°C +150°C Maximum Junction Temperature (T<sub>J</sub>)

Lead Temperature  $(T_L)$ 

(Soldering, 10 seconds) +260°C

ESD (Human Body Model) FSA4157A

Note 2: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum rating. The "Recommended Operating Conditions" table will define the conditions

for actual device operation.

Note 3: The input and output negative ratings may be exceeded if the input and output diode current ratings are observed

Note 4: Control input must be held HIGH or LOW and it must not float.

## DC Electrical Characteristics (all typical values are at 25°C unless otherwise specified)

7500V

Symbol	Parameter	v <sub>cc</sub>	T <sub>A</sub> = +25 °C		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		Units	Conditions	
	Farameter	(V)	Min	Тур	Max	Min	Max	Omits	Conditions
V <sub>IH</sub>	Input Voltage High	2.7 to 3.6				2.0		V	
		4.5 to 5.5				2.4		V	
V <sub>IL</sub>	Input Voltage Low	2.7 to 3.6					0.4		(FSA4157A Only)
		2.7 to 3.6					0.6	V	
		4.5 to 5.5					0.8		
I <sub>IN</sub>	Control Input Leakage	2.7 to 3.6				-1.0	1.0	μА	$V_{IN} = 0V \text{ to } V_{CC}$
		4.5 to 5.5				-1.0	1.0	μА	VIN = 0 V to VCC
I <sub>NO(OFF)</sub> , I <sub>NC(OFF)</sub>	OFF Leakage Current of Port B <sub>0</sub> and B <sub>1</sub>	5.5	-2.0		2.0	-20.0	20.0	nA	A = 1V, 4.5V B <sub>0</sub> or B <sub>1</sub> = 4.5V, 1V
I <sub>A(ON)</sub>	ON Leakage Current of Port A	5.5	-4.0		4.0	-40.0	40.0	nA	A = 1V, 4.5V $B_0$ or $B_1 = 1V, 4.5V$ or Floating
R <sub>ON</sub>	Switch ON Resistance	2.7		2.6	4.0		4.3	Ω	$I_{OUT} = 100 \text{mA}, B_0 \text{ or } B_1 = 1.5 \text{V}$
	(Note 5)	4.5		0.95	1.15		1.3	32	$I_{OUT} = 100 \text{mA}, B_0 \text{ or } B_1 = 3.5 \text{V}$
$\Delta R_{ON}$	On Resistance Matching Between Channels (Note 6)	4.5		0.06	0.12		0.15	Ω	$I_{OUT} = 100 \text{mA}, B_0 \text{ or } B_1 = 1.5 \text{V}$
R <sub>FLAT(ON)</sub>	On Resistance Flatness	2.7		1.4					I <sub>OUT</sub> = 100mA,
	(Note 7)							Ω	$B_0$ or $B_1 = 0V$ , 0.75V,1.5V
		4.5		0.2	0.3		0.4		$I_{OUT} = 100$ mA, $B_0$ or $B_1 = 0$ V, 1V, 2V
I <sub>CC</sub>	Quiescent Supply Current	3.6		0.1	0.5		1.0	μА	$V_{IN} = 0V$ or $V_{CC}$ , $I_{OUT} = 0V$
		5.5		0.1	0.5		1.0	μΛ	$V_{IN} = 0V$ or $V_{CC}$ , $I_{OUT} = 0V$
Δl <sub>CC</sub>	Increase in I <sub>CC</sub> per Input	4.3		0.2			10.0	μА	One Input at 2.7V, others at V <sub>CC</sub> or GND (FSA4157A only)

Note 5: Measured by the voltage drop between A and B pins at the indicated current through the switch. On Resistance is determined by the lower of the voltage on the two (A or B Ports).

Note 6:  $\Delta R_{ON} = R_{ON \, max} - R_{ON \, min}$  measured at identical  $V_{CC}$ , temperature and voltage.

Note 7: Flatness is defined as the difference between the maximum and minimum value of On Resistance over the specified range of conditions.

# AC Electrical Characteristics (all typical value are at 25°C unless otherwise specified)

Symbol	Parameter	V <sub>CC</sub>	$T_A = +25  ^{\circ}C$		$T_A = -40^{\circ}C$ to $+85^{\circ}C$		Units	Conditions	Figure	
Symbol	- arameter	(V)	Min	Тур	Max	Min	Max	Omico	Conditions	Number
t <sub>ON</sub>	Turn ON Time	2.7 to 3.6			60.0		65.0		$B_0$ or $B_1$ = 1.5V, $R_L$ = $50\Omega,C_L$ = 35 pF (FSA4157A only)	
		2.7 to 3.6			50.0		60.0	ns	$B_0$ or $B_1 = 1.5V$ , $R_L = 50\Omega$ , $C_L = 35 pF$	Figure 3
		4.5 to 5.5			35.0		40.0		$B_0$ or $B_1 = 3V$ , $R_L = 50\Omega$ , $C_L = 35$ pF	1
t <sub>OFF</sub>	Turn OFF Time	2.7 to 3.6			20.0		30.0	ns	$B_0$ or $B_1=1.5$ V, $R_L=50\Omega$ , $C_L=35$ pF	Eiguro 2
		4.5 to 5.5			15.0		20.0	115	$B_0$ or $B_1=3V$ , $R_L=50\Omega$ , $C_L=35$ pF	Figure 3
t <sub>B-M</sub>	Break Before	2.7 to 3.6								
	Make Time	4.5 to 5.5		20.0				ns		Figure 4
		4.5 to 5.5		25.0					(FSA4157A only)	
Q	Charge	2.7 to 3.6		10.0				рС	$C_L = 1.0 nF, V_{GE} = 0V,$	Figure 6
	Injection	4.5 to 5.5		20.0				ро	$R_{GEN} = 0\Omega$	i igule o
OIRR	OFF- Isolation	2.7 to 3.6		-70.0				dB	$f = 1MHz$ , $R_1 = 50\Omega$	Figure 5
		4.5 to 5.5		-70.0				ub	1 - 11VII 12, IXL - 3052	i igule 3
Xtalk	Crosstalk	2.7 to 3.6		-70.0				dB	$f = 1MHz, R_1 = 50\Omega$	Figure 5
		4.5 to 5.5		-70.0				uБ	1 = 11VIFIZ, R <sub>L</sub> = 30\$2	rigule 5
BW	-3db Bandwidth	2.7 to 3.6		350				NAL 1-	$R_L = 50\Omega$	Eiguro 0
		4.5 to 5.5		350				IVIITZ	KL = 3022	Figure 8
THD	Total Harmonic	2.7 to 3.6		0.002				%	$R_L = 600\Omega$ , $V_{IN} = 0.5V$ P.P,	Figure 9
	Distortion	4.5 to 5.5		0.002				/0	f = 20 Hz to 20 k Hz	i igule 9

# Capacitance

Symbol	Parameter	v <sub>cc</sub>	$T_A = +25^{\circ}C$		T <sub>A</sub> = 40°C to +85°C		Units	Conditions	Figure	
Cymbol	T di dinotoi	(V)	Min	Тур	Max	Min	Max	Omico	Conditions	Number
C <sub>IN</sub>	Control Pin Input	0		3.5				pF	f = 1MHz	Figure 7
	Capacitance			5.5				рі		i iguic i
C <sub>OFF</sub>	B Port OFF	4.5		12.0				pF	f = 1MHz	Figure 7
	Capacitance	4.5		12.0				рі		i iguic i
C <sub>ON</sub>	On Capacitance	4.5		40.0				pF	f = 1MHz	Figure 7

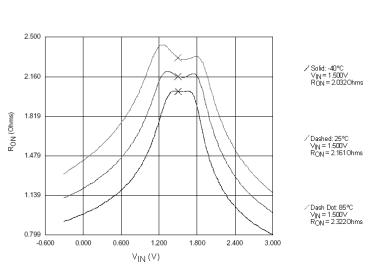


FIGURE 1.  $\rm R_{ON}$  Switch On Resistance,  $\rm I_{ON}=100mA,\,V_{CC}=2.7V$ 

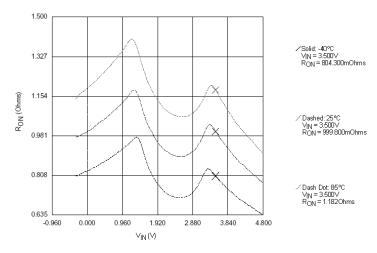
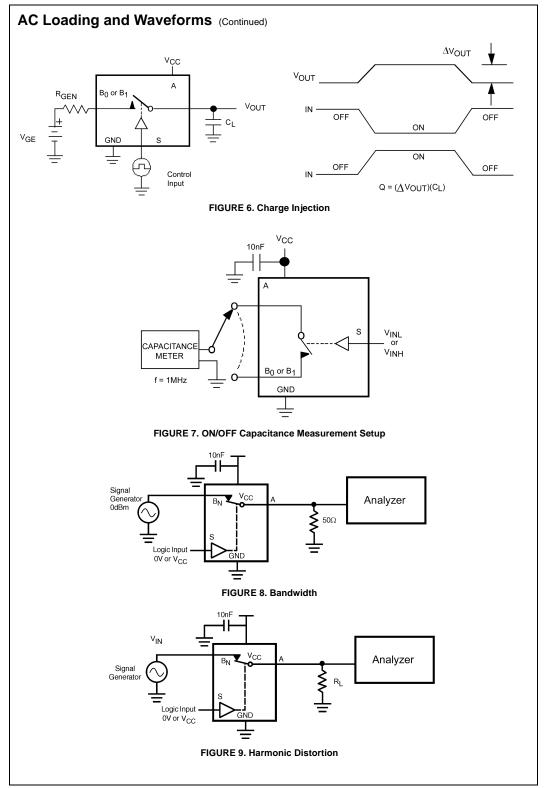


FIGURE 2.  $R_{\mbox{\scriptsize ON}}$  Switch On Resistance,  $I_{\mbox{\scriptsize ON}}=100\mbox{mA},\,V_{\mbox{\scriptsize CC}}=4.5\mbox{\scriptsize V}$ 

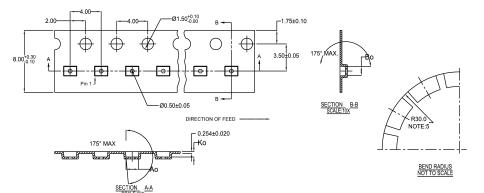
# **AC Loading and Waveforms** t<sub>r</sub> < 2.5ns t<sub>f</sub> < 2.5ns Vcc Control ${}^{V_{\hbox{\footnotesize INH}}}_{V_{\hbox{\footnotesize INL}}}$ $v_{\underset{or}{B0}}$ $V_{B1}$ 0.9 x VOUT 0.9 x V<sub>OUT</sub> Switch Output **C**<sub>L</sub> Includes Fixture and Stray Capacitance **Logic Input Waveforms Inverted for Switches** that have the Opposite Logic Sense FIGURE 3. Turn ON/OFF Timing Control Input $v_{\text{OUT}}$ $V_{\mathsf{Bn}}$ Control Input -0.9 x V<sub>OUT</sub> GND VOUT $\mathsf{T}_{\mathsf{B-M}}$ $\mathbf{C}_{\mathbf{L}}$ Includes Fixture and Stray Capacitance FIGURE 4. Break Before Make Timing V<sub>CC</sub> <sup>10nF</sup> OFF-ISOLATION = $20\log \frac{V_{OUT}}{V_{IN}}$ NETWORK ANALYZER 50Ω $ON\text{-}LOSS = 20log \frac{V_{OUT}}{V_{IN}}$ 0 or V<sub>CC</sub> v<sub>cc</sub> s В0 FSA4157 $CROSSTALK = 20log \frac{V_{OUT}}{V_{IN}}$ REF В1 GND FIGURE 5. OFF Isolation and Crosstalk



## **Tape and Reel Specification**

Tape Format For Micropak 6

Tape Format For Interopar o									
Package	Таре	Number	Cavity	Cover Tape					
Designator	Section	Cavities	Status	Status					
	Leader (Start End)	125 (typ)	Empty	Sealed					
L6X	Carrier	5000	Filled	Sealed					
	Trailer (Hub End)	75 (typ)	Empty	Sealed					



10	300056	2.30±0.05	1.78±0.05	0.68 ± 0.05
8	300038	1.78±0.05	1.78±0.05	0.68 ± 0.05
			4 45 . 0 05	0.70 . 0.05

NOTES: UNLESS OTHERWISE SPECIFIE

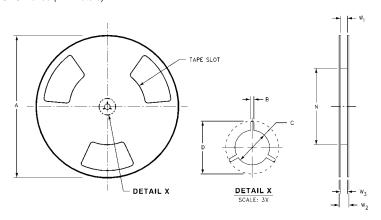
1. ACCUMULATED 50 SPROCKETS, SPROCKET HOLE PITCH IS 200.00 ±0.30MM

- 2. NO INDICATED CORNER RADIUS IS 0.127MM
- 3. CAMBER NOT TO EXCEED 1MM IN 100MM
- 4. SMALLEST ALLOWABLE BENDING RADIUS
- 5. POCKET POSITION RELATIVE TO SPROCKET HOLE MEASURED AS TRUE POSITION OF POCKET, NOT POCKET HOLE

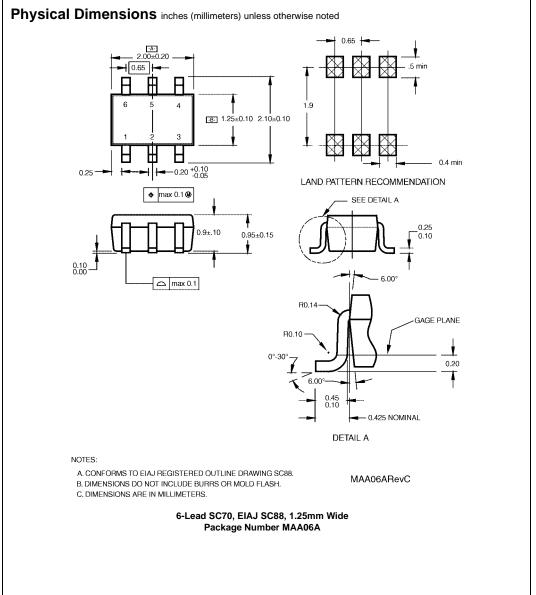


SCALE: 6X

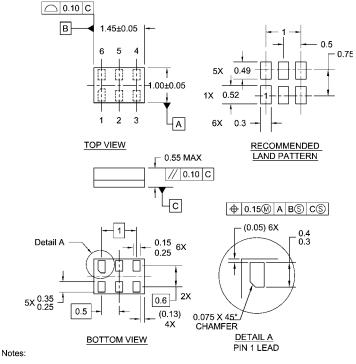
## REEL DIMENSIONS inches (millimeters)



Tape Size	Α	В	С	D	N	W1	W2	W3
8 mm	7.0	0.059	0.512	0.795	2.165	0.331 + 0.059/-0.000	0.567	W1 + 0.078/-0.039
	(177.8)	(1.50)	(13.00)	(20.20)	(55.00)	(8.40 + 1.50/-0.00)	(14.40)	(W1 + 2.00/-1.00)



## Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



- 1. JEDEC PACKAGE REGISTRATION IS ANTICIPATED 2. DIMENSIONS ARE IN MILLIMETERS 3. DRAWING CONFORMS TO ASME Y14.5M-1994

MAC06ARevB

6-Lead MicroPak, 1.0mm Wide Package Number MAC06A

## **Technology Description**

The Fairchild Switch family derives from and embodies Fairchild's proven switch technology used for several years in its 74LVX3L384 (FST3384) bus switch product.

Fairchild does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and Fairchild reserves the right at any time without notice to change said circuitry and specifications.

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- 2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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