

DS14196 EIA/TIA-232 5 Driver x 3 Receiver

Check for Samples: [DS14196](#)

FEATURES

- Replaces Two 1488s and One 1489
- Conforms to EIA/TIA-232-E and ITU-T V.28
- 5 Drivers and 3 Receivers
- Flow-Through Pinout
- Failsafe Receiver Outputs High when Inputs Open
- 20-Pin Wide SOIC Package
- LapLink® Compatible—230.4 kbps Data Rate
- Pin Compatible with: SN75196, GD75323

DESCRIPTION

The DS14196 is a five driver, three receiver device which conforms to the EIA/TIA-232-E and the ITU-T V.28 standards.

The flow-through pinout facilitates simple non-crossover board layout. The DS14196 provides a peripheral side one-chip solution for the common 9-pin serial RS-232 interface between data terminals and data communications equipment.

The DS14196 offers optimum performance when used with the DS14185 3 x 5 Driver/Receiver, a host side one-chip solution for the common 9-pin serial RS-232 interface between data terminals and data communications equipment.

CONNECTION DIAGRAM

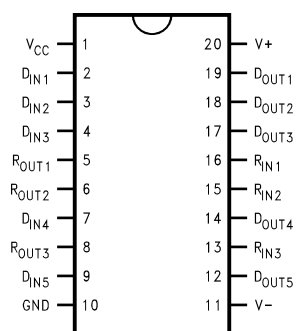
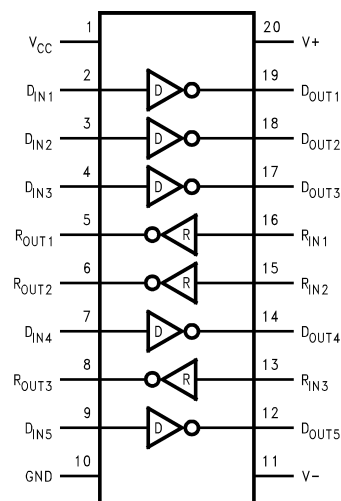


Figure 1. SOIC
See Package Number DW

FUNCTIONAL DIAGRAM



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.



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ABSOLUTE MAXIMUM RATINGS⁽¹⁾⁽²⁾

Supply Voltage (V_{CC})		+7V
Supply Voltage (V^+)		+15V
Supply Voltage (V^-)		-15V
Driver Input Voltage		0V to V_{CC}
Driver Output Voltage (Power Off)		±15V
Receiver Input Voltage		±25V
Receiver Output Voltage (R_{OUT})		0V to V_{CC}
Maximum Power Package Dissipation @ +25°C	DW Package	1524 mW
	Derate DW Package	12.2 mW/°C above 25°C
Storage Temperature Range		-65°C to +150°C
Lead Temperature Range (Soldering, 4 sec.)		+260°C
ESD Ratings (HBM, 1.5 kΩ, 100 pF)		≥1.5 kV

- (1) Absolute Maximum Ratings are those values beyond which the safety of the device cannot be ensured. They are not meant to imply that the devices should be operated at these limits. The table of Electrical Characteristics specifies conditions of device operation.
- (2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/ Distributors for availability and specifications.

RECOMMENDED OPERATING CONDITIONS

	Min	Nom	Max	Units
Supply Voltage (V_{CC})	+4.75	+5.0	+5.25	V
Supply Voltage (V^+)	+9.0	+12.0	+13.2	V
Supply Voltage (V^-)	-13.2	-12.0	-9.0	V
Operating Free Air Temperature (T_A)	0	+25	+70	°C

ELECTRICAL CHARACTERISTICS⁽¹⁾⁽²⁾

Over recommended operating supply and temperature ranges unless otherwise specified

Symbol	Parameter	Conditions		Min	Typ	Max	Units
DEVICE CHARACTERISTICS							
I _{CC}	V _{CC} Supply Current	No Load, All Inputs at +5V			13	22	mA
I ⁺	V ⁺ Supply Current	No Load, All Driver Inputs at 0.8V or +2V. All Receiver Inputs at 0.8V or 2.4V.	V ⁺ = +9V, V ⁻ = -9V		16	26	mA
			V ⁺ = +13.2V, V ⁻ = -13.2V		23	36	mA
I ⁻	V ⁻ Supply Current		V ⁺ = +9V, V ⁻ = -9V		-18	-26	mA
			V ⁺ = +13.2V, V ⁻ = -13.2V		-25	-36	mA
DRIVER CHARACTERISTICS							
V _{IH}	High Level Input Voltage			2.0			V
V _{IL}	Low Level Input Voltage					0.8	V
I _{IH}	High Level Input Current	V _{IN} = 5V				10	μA
I _{IL}	Low Level Input Current	V _{IN} = 0V			-1.1	-1.5	mA
V _{OH}	High Level Output Voltage	R _L = 3 kΩ, V _{IN} = 0.8V, V ⁺ = +9V, V ⁻ = -9V		6	7		V
		R _L = 3 kΩ, V _{IN} = 0.8V, V ⁺ = +12V, V ⁻ = -12V		8	9		V
		R _L = 7 kΩ, V _{IN} = 0.8V, V ⁺ = +13.2V, V ⁻ = -13.2V		10	11.5		V
V _{OL}	Low Level Output Voltage	R _L = 3 kΩ, V _{IN} = 2V, V ⁺ = +9V, V ⁻ = -9V			-7	-6	V
		R _L = 3 kΩ, V _{IN} = 2V, V ⁺ = +12V, V ⁻ = -12V			-10	-8	V
		R _L = 7 kΩ, V _{IN} = 2V, V ⁺ = +13.2V, V ⁻ = -13.2V			-11.5	-10	V

- (1) Current into device pins is defined as positive. Current out of the device pins is defined as negative. All voltages are referenced to ground unless otherwise specified. For current, minimum and maximum values are specified as an absolute value and the sign is used to indicate direction. For voltage logic levels, the more positive value is designated as maximum. For example, if -6V is a maximum, the typical value -6.8V is more negative.
- (2) All typicals are given for: $V_{CC} = +5V, V^+ = +12V, V^- = -12V, T_A = +25^\circ\text{C}$.

ELECTRICAL CHARACTERISTICS⁽¹⁾⁽²⁾ (continued)

Over recommended operating supply and temperature ranges unless otherwise specified

Symbol	Parameter	Conditions	Min	Typ	Max	Units
I _{OS} ⁺	Output High Short Circuit Current ⁽³⁾	V _{OUT} = 0V, V _{IN} = 0.8V	-6	-12	-18	mA
I _{OS} ⁻	Output Low Short Circuit Current ⁽³⁾	V _{OUT} = 0V, V _{IN} = 2.0V	6	12	18	mA
R _O	Output Resistance	-2V ≤ V _{OUT} ≤ +2V, V ⁺ = V ⁻ = V _{CC} = 0V	300			Ω
		-2V ≤ V _{OUT} ≤ +2V, V ⁺ = V ⁻ = V _{CC} = Open Circuit	300			Ω
RECEIVER CHARACTERISTICS						
V _{TH}	Input High Threshold(Recognized as a High Signal)	V _{OUT} ≤ 0.4V, I _O = 3.2 mA		1.85	2.4	V
V _{TL}	Input Low Threshold (Recognized as a Low Signal)	V _{OUT} ≥ 2.5V, I _O = -0.5 mA	0.7	1.0		V
R _{IN}	Input Resistance	V _{IN} = ±3V to ±15V	3.0	3.8	7.0	kΩ
I _{IN}	Input Current	V _{IN} = +15V	2.1	4.0	5.0	mA
		V _{IN} = +3V	0.43	0.7	1.0	mA
		V _{IN} = -15V	-2.1	-4.0	-5.0	mA
		V _{IN} = -3V	-0.4 3	-0.7	-1.0	mA
V _{OH}	High Level Output Voltage ⁽⁴⁾	I _{OH} = -0.5 mA, V _{IN} = -3V	2.6	4.0		V
		I _{OH} = -10 μA, V _{IN} = -3V	4.0	4.9		V
		I _{OH} = -0.5 mA, V _{IN} = Open Circuit	2.6	4.0		V
		I _{OH} = -10 μA, V _{IN} = Open Circuit	4.0	4.9		V
V _{OL}	Low Level Output Voltage	I _{OL} = 3.2 mA, V _{IN} = +3V		0.2	0.4	V
I _{OSR}	Short Circuit Current	V _{OUT} = 0V, V _{IN} = 0V ⁽⁵⁾	-1.7	-2.7	-4	mA

(3) Only one driver output shorted at a time.

(4) If receiver inputs are unconnected, receiver output is a logic high.

(5) Only one driver output shorted at a time.

SWITCHING CHARACTERISTICS⁽¹⁾⁽²⁾⁽³⁾

$T_A = 25^{\circ}\text{C}$

Symbol	Parameter	Conditions	Min	Typ	Max	Units
DRIVER CHARACTERISTICS						
t _{PHL}	Propagation Delay High to Low	R _L = 3 kΩ, C _L = 50 pF (Figure 2, Figure 3)		60	350	ns
t _{PLH}	Propagation Delay Low to High			300	450	ns
t _r , t _f	Rise/Fall Time ⁽⁴⁾			40		ns
RECEIVER CHARACTERISTICS						
t _{PHL}	Propagation Delay High to Low	R _L = 1.5 kΩ, C _L = 15 pF (includes fixture plus probe), (Figure 4, Figure 5)		150	350	ns
t _{PLH}	Propagation Delay Low to High			240	350	ns
t _r	Rise Time			40	175	ns
t _f	Fall Time			40	100	ns

(1) All typicals are given for: $V_{CC} = +5V$, $V^{+} = +12V$, $V^{-} = -12V$, $T_A = +25^{\circ}\text{C}$.

(2) Generator characteristics for driver input: $f = 64 \text{ kHz}$ (128 kbps), $t_r = t_f < 10 \text{ ns}$, $V_{IH} = 3V$, $V_{IL} = 0V$, duty cycle = 50%.

(3) Generator characteristics for receiver input: $f = 64 \text{ kHz}$ (128 kbps), $t_r = t_f = 200 \text{ ns}$, $V_{IH} = 3V$, $V_{IL} = -3V$, duty cycle = 50%.

(4) Refer to typical curves. Driver output slew rate is measured from the +3V to the -3V level on the output waveform. Inputs not under test are connected to V_{CC} or GND. Slew rate is determined by load capacitance. To comply with a 30 V/ μs maximum slew rate, a minimum load capacitance of 390 pF is recommended.

PARAMETER MEASUREMENT INFORMATION

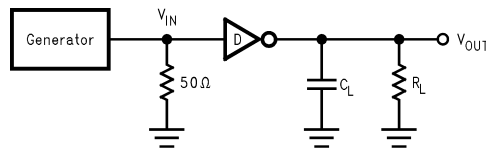


Figure 2. Driver Propagation Delay and Transition Time Test Circuit ⁽⁵⁾

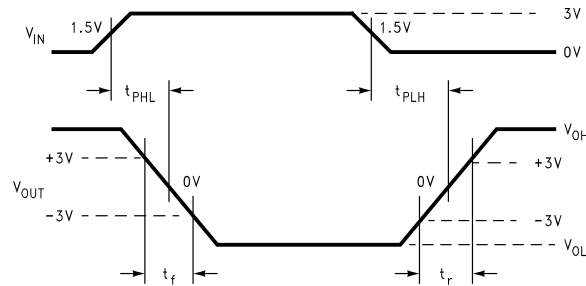


Figure 3. Driver Propagation Delay and Transition Time Waveforms Slew Rate (SR) = 6V/(t_r or t_f)

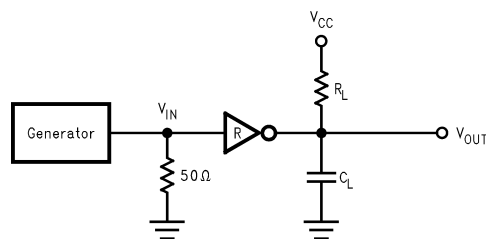


Figure 4. Receiver Propagation Delay and Transition Time Test Circuit ⁽⁶⁾

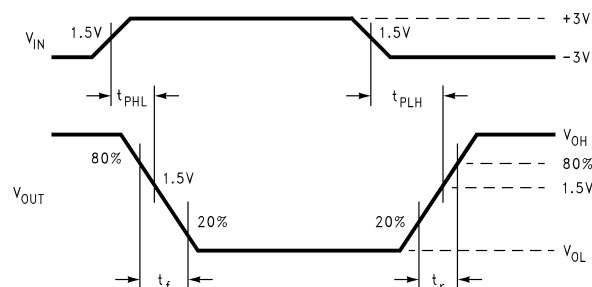


Figure 5. Receiver Propagation Delay and Transition Time Waveform

(5) Generator characteristics for driver input: $f = 64 \text{ kHz}$ (128 kbps), $t_r = t_f < 10 \text{ ns}$, $V_{IH} = 3\text{V}$, $V_{IL} = 0\text{V}$, duty cycle = 50%.

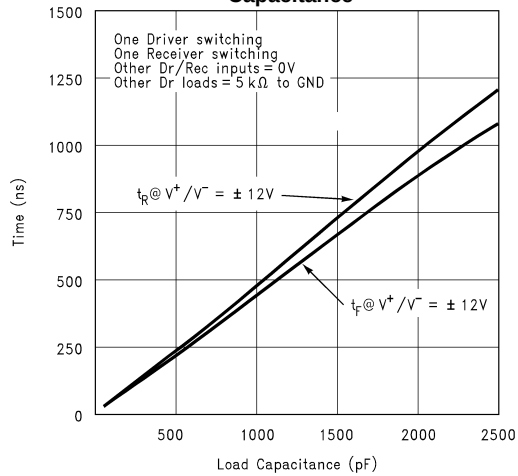
(6) Generator characteristics for receiver input: $f = 64 \text{ kHz}$ (128 kbps), $t_r = t_f = 200 \text{ ns}$, $V_{IH} = 3\text{V}$, $V_{IL} = -3\text{V}$, duty cycle = 50%.

PIN DESCRIPTIONS

Pin #	Pin Name	Description
2, 3, 4, 7, 9	D _{IN}	Driver Input Pins
12, 14, 17, 18, 19	D _{OUT}	Driver Output Pins, RS-232 Levels
13, 15, 16	R _{IN}	Receiver Input Pins, RS-232 Levels
5, 6, 8	R _{OUT}	Receiver Output Pins
10	GND	Ground
20	V ⁺	Positive Power Supply Pin ($+9.0 \leq V^+ \leq +13.2$)
11	V ⁻	Negative Power Supply Pin ($-9.0 \leq V^- \leq -13.2$)
1	V _{CC}	Positive Power Supply Pin (+5V $\pm$ 5%)

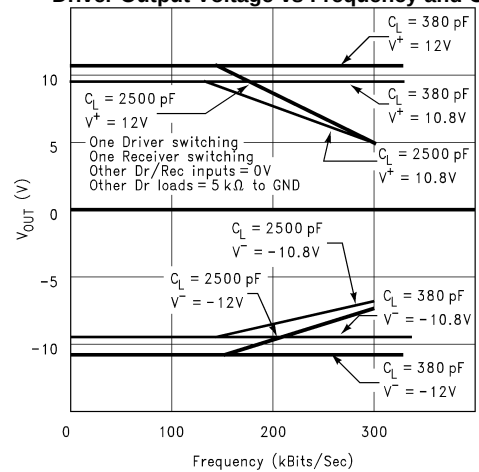
TYPICAL PERFORMANCE CHARACTERISTICS

Driver Output Slew Rate between +3V and -3V vs Load Capacitance



$V_{CC} = 5V$, $R_L = 5 k\Omega$, $T_A = 25^\circ C$, $f_{IN} = 64 kHz$ Square Wave
Figure 6.

Driver Output Voltage vs Frequency and C_L



$V_{CC} = 5V$, $R_L = 5 k\Omega$, $T_A = 25^\circ C$

Figure 7.

Driver Output Current vs Output Voltage

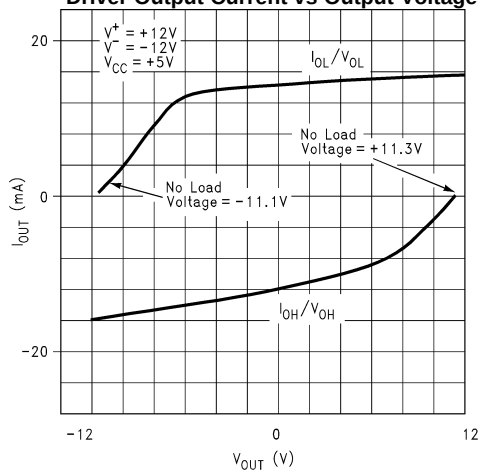


Figure 8.

APPLICATIONS INFORMATION

In a typical Data Terminal Equipment (DTE) to Data Circuit-Terminating Equipment (DCE) 9-pin de-facto interface implementation, 2 data lines and 6 control lines are required. The data lines are TXD and RXD. The control lines are RTS, DTR, DSR, DCD, CTS and RI.

The DS14196 is a 5 x 3 Driver/Receiver and offers a single chip solution for this DTE interface. As shown in Figure 9, this interface allows for direct flow-thru interconnect. For a more conservative design, the user may wish to insert ground traces between the signal lines to minimize cross talk.

FAILSAFE RECEIVER OUTPUTS

The DS14196 features failsafe receiver outputs. In failsafe mode, if the receiver input becomes zero or an open-circuit, the receiver output is pulled to a high level.

LapLink COMPATIBILITY

The DS14196 can easily provide 128 kbps data rate under maximum driver load conditions of $C_L = 2500$ pF and $R_L = 3$ k Ω , while power supplies are:

$$V_{CC} = +4.75V, V^+ = 10.8V, V^- = -10.8V \quad (1)$$

MOUSE DRIVING

A typical mouse can be powered from the drivers. Two driver outputs connected in parallel and set to V_{OH} can be used to supply power to the V^+ pin of the mouse. The third driver output is set to V_{OL} to sink the current from the V^- terminal. Refer to typical curves of V_{OUT}/I_{OUT} . Typical mouse specifications are:

$$10 \text{ mA at } +6V \quad 5 \text{ mA at } -6V \quad (2)$$

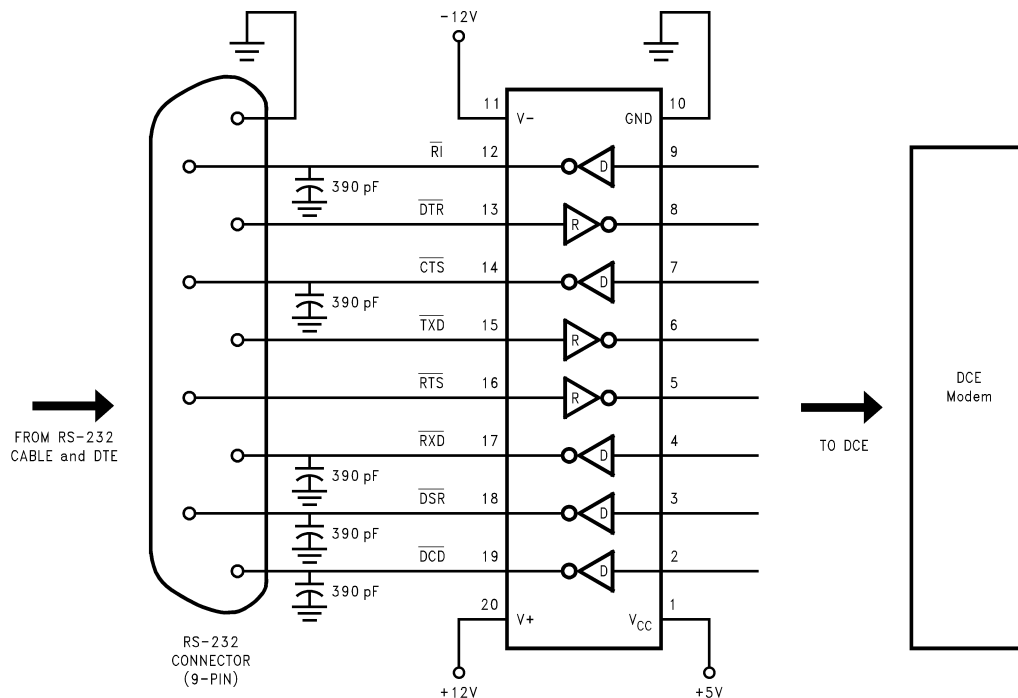


Figure 9. Typical DCE Application

REVISION HISTORY

Changes from Revision C (April 2013) to Revision D

Page

- Changed layout of National Data Sheet to TI format [7](#)

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