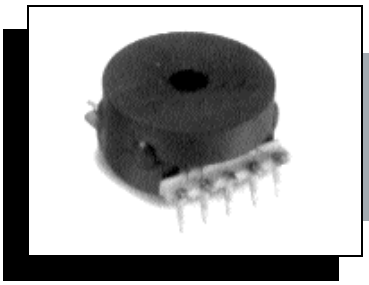

DATA COMMUNICATIONS TRANSFORMERS

ADSL SERIES

- Supports Industry Standard ADSL/ADSL G.Lite Transceivers
- Operating Temperature Range of -40°C to 85°C
- Conforms to ANSI, ITU and ETSI Standards Requirements

STANDARD SPECIFICATIONS

Part Number*	Apps	Turns Ratio** (Chip / Line)	OCL (Line)	RL@Midband (dB)	IL (dB)	Longitudinal Balance (dB)	THD (dB)	Pkg.	Cir.
ADSL-101[5]	CO/CP	1:1:1:1	5.0 mH	≥ 16.5 30 - 320 KHz	<1.0 30 - 320 KHz	≥ 50 .025 - 1.1 MHz	≤ -80	EP13	1
ADSL-102[2]	CPE	1:1:1:1	480 µH	≥ 16 .2 - 2 MHz	<1.1 .04 - 2 MHz	≥ 50 .025 - 1.1 MHz	≤ -70	EP13	1
ADSL-103[7]	COE	1:1:1:1	430 µH	≥ 16.5 .2 - 2 MHz	<1.1 .04 - 2 MHz	≥ 60 .025 - 1.1 MHz	≤ -80	EP13	1
ADSL-104	CO/CP	1:1:1:1	1.25 mH	≥ 15 .03 - 1.1 MHz	<1.0 .04 -2 MHz	≥ 50 .025 - 1.1 MHz	≤ -80	EP13	4
ADSL-105[8]	CPE	1:1:1:1	407 µH	≥ 16 .2 - 2 MHz	<1.5 .04 - 2 MHz	≥ 55 .025 - 1.1 MHz	≤ -80	EP13	1
ADSL-106[11]	COE	1:1.95	1.5 mH	≥ 16 .02 - 1.1 MHz	<1.0 .03 - 2 MHz	≥ 60 .025 - 1.1 MHz	≤ -80	EP13	5
ADSL-107[1]	COE	1:1:2:2	470 µH	≥ 16 .2 - 2 MHz	<1.1 .04 - 2 MHz	≥ 50 .025 - 1.1 MHz	≤ -70	EP13	1
ADSL-108[3]	COE	1CT:1.2	2.75 mH	≥ 16.5 30 - 320 KHz	<1.1 .03 - 1 MHz	≥ 50 .025 - 1.1 MHz	≤ -80	EP13	2
ADSL-109[4]	CPE	1.8:1	12.96 mH	≥ 16 .03 - 1.1 MHz	<1.0 .03 - 1 MHz	≥ 50 .025 - 1.1 MHz	≤ -80	EP13	2
ADSL-110[6]	COE	1.41:1	940 µH	≥ 16.5 .2 - 2 MHz	<1.0 .04 - 2 MHz	≥ 55 .025 - 1.1 MHz	≤ -80	23x11	3
ADSL-111[9]	CPE	1:1:1:1	410 µH	≥ 16 .2 - 2 MHz	<1.5 .04 - 2 MHz	≥ 55 .025 - 1.1 MHz	≤ -80	EP13	3
ADSL-112[10]	COE	1.41:1	475 µH	≥ 16 .2 - 1.1 MHz	<1.0 .03 - 2 MHz	≥ 60 .025 - 1.1 MHz	≤ -80	EP13	3
ADSL-113[12]	CPE	1:3.3	800 µH	≥ 15 .2 - 2 MHz	<1.2 .04 - 2 MHz	≥ 40 .025 - 1.1 MHz	≤ -80	EP13	4[12]
ADSL-114	COE	1:1:2:2	409 µH	≥ 16 .2 - 2 MHz	<1.1 .2 - 2 MHz	≥ 50 .02 - 1.1 MHz	≤ -78	EP13	4
ADSL-115	CPE	1:1:2:2	100 µH	≥ 16 .2 - 2 MHz	<1.1 .2 - 2 MHz	≥ 50 .02 - 1.1 MHz	≤ -90	EP13	4
ADSL-116	CO/CP	1:1:2:2	440 µH	≥ 16 .2 - 2 MHz	<1.1 .1 - 1.1 MHz	≥ 50 .03 - 1.1 MHz	≤ -90	EP13	4[12]
ADSL-117	CPE	1:1.8	450 µH	≥ 16 .2 - 2 MHz	<1.3 .03 - 2 MHz	≥ 60 .025 - 1.1 MHz	≤ -80	EP13	3
ADSL-118	COE	1:1.41	475 µH	≥ 16 .2 -1.1 MHz	<1.1 .1 - 1.1 MHz	≥ 60 .025 - 1.1 MHz	≤ -80	EP13	4
ADSL-119	CO/CP	1:1:1:1	5.0 mH	≥ 10 .035 -1.1 MHz	<1.0 @ 100 KHz	≥ 40 .03 - 1.1 MHz	≤ -80	EP13	1
ADSL-120	COE	1:2.125	5.0 mH	≥ 10 .035 -1.1 MHz	<1.0 @ 100 KHz	≥ 40 .03 - 1.1 MHz	≤ -80	EP13	6
ADSL-121	CO/CP	1:5.4	3.0 mH	≥ 15 40 -500 KHz	<0.5 @ 100 KHz	≥ 55 20 - 500 MHz	≤ -76	EP13	7

NOTES: See facing page

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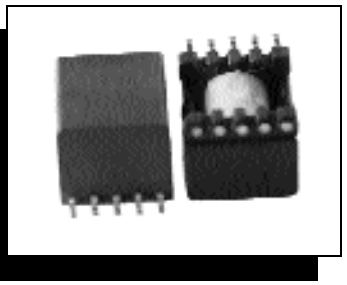
DATA COMMUNICATIONS TRANSFORMERS

ADSL SERIES



Test Conditions:

A. OCL: 100 mVRMS @ 10 KHz with appropriate IDC = 60 mA, Chip-Line
B. THD: 6 Vp-p @ 30 KHz with 100Ω
C. Longitudinal Balance: frequency range specified with 100Ω
D. DWV: 1,500 VRMS, greater voltage isolation available
E. THD: 7.07Vp-p @ 130 KHz with 100Ω for ADSL -119, -120
F. Parametric tests for ADSL 121 performed with line Z = 135Ω, Chip Z = 4.6 Ω

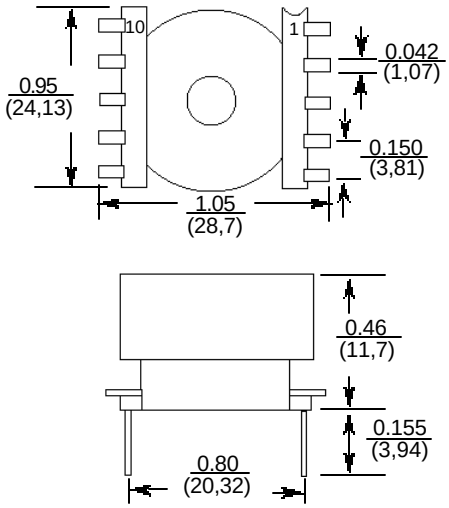
Notes:

* P/Ns with "S" as suffix are configured for surface mountable applications
** "CT" indicates a center-tapped winding, otherwise readings are intended to be across entire winding segment

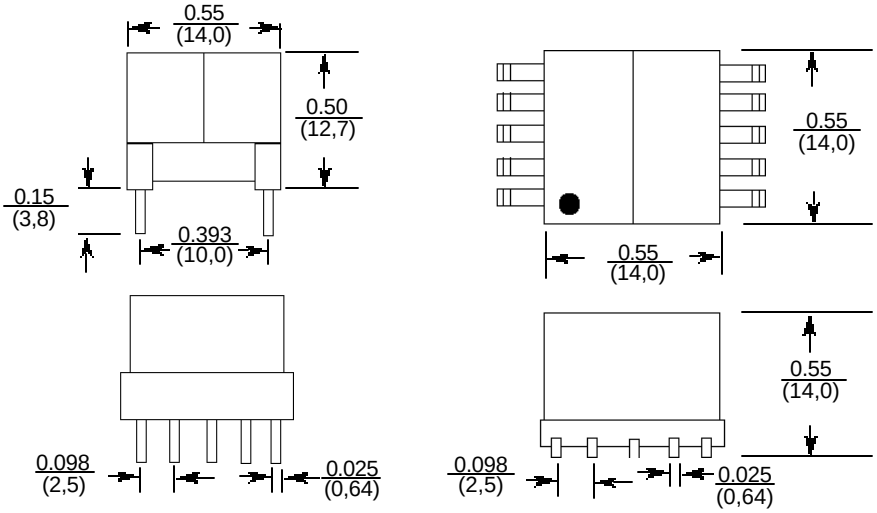
1. Supports Alcatel MTK 20150/134AFE / DCR < 0.5/0.8 Ω.
2. Supports Alcatel MTK 20140/134AFE / DCR < 1.0/1.0 Ω.
3. Supports Motorola MC145650 (MC03AX1456CO Line Driver) w/ DCR < 4.0/2.2, 2.2 Ω.
4. Supports Motorola MC145650 (MC03AX1456CO Line Driver) w/ DCR < 3.0/1.0, 1.0 Ω.; IDC = 0
5. Supports Analog Devices AD20msp910/918 / DCR < 4.5/4.5 Ω.
6. Supports Globespan 7M640 / DCR < 1.0/1.0 Ω. and DWV @ 3000 VRMS
7. Supports Globespan 7M640 / DCR < 2.4/1.5 Ω.
8. Supports Elantec EL-1501 / DCR < 1.5/1.5 Ω.
9. Supports Alcatel MTK 20131/134AFE / DCR < 1.5/1.5 Ω.
10. Supports Elantec EL-1501 / DCR < 1.0/1.0 Ω.
11. Supports T.I. TNETD3000C/P and BB1302 AFE
12. Supports connexant CN9001 circuit #4 (chip:line) pins reversed
13. Dimensions: inches / mm; see spec sheet for tolerance limits
14. Specifications subject to change without notice

PHYSICAL CHARACTERISTICS [13]

23x11



EP13



CIRCUITS