

10/100 BASE-T SINGLE PORT TRANSFORMER MODULES

With 2:1 Transmit Turns Ratios Compatible with MicroLinear, MITEL, and SEEQ Transceivers



- Patented Interlock Base design
- Meets all IEEE 802.3 and ANSI X3.263 standards including 350 μ H OCL with 8 mA bias
- Various pinout options available for optimal board layout
- IC grade transfer-molded package withstands 235°C IC reflow

Electrical Specifications @ 25°C — Operating Temperature 0°C to 70°C

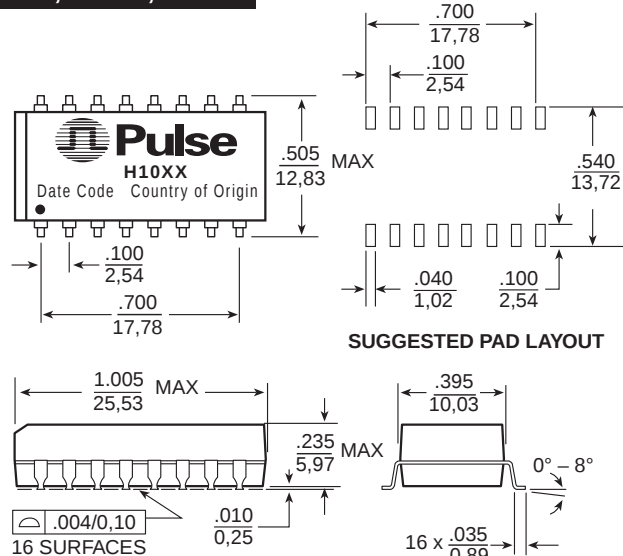
Part Number	Insertion Loss 0.1 to 100 MHz (dB TYP)	Rise Time (ns TYP) (10 - 90%)	Return Loss (dB TYP)		Differential to Common Mode Rejection (dB TYP)		Crosstalk (dB TYP)		Voltage Isolation (Vrms MIN)
			0.1-60 MHz	60-100 MHz	0.1-60 MHz	60-100 MHz	0.1-60 MHz	60-100 MHz	
H1038	-1.0	3.5	-16	-12	-35	-32	-40	-35	1500
H1090	-1.0	3.5	-16	-12	-35	-32	-40	-35	1500
H1094	-1.0	3.5	-16	-12	-35	-32	-40	-35	1500

NOTE: For Tape & Reel packaging add a "T" suffix to the part number (i.e.: H1038T).

U.S. Patent No. 5,015,981

Mechanical

H1038, H1090, H1094

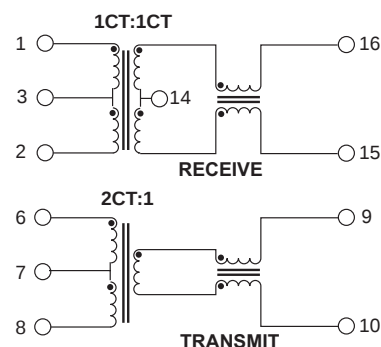


Dimensions: $\frac{\text{Inches}}{\text{mm}}$
Unless otherwise specified,
all tolerances are $\pm .010$
0,25

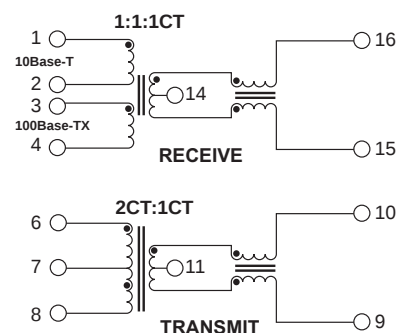
Weight 2.5 grams
Tube23/tube
Tape & Reel350/reel

Schematics

H1038



H1090



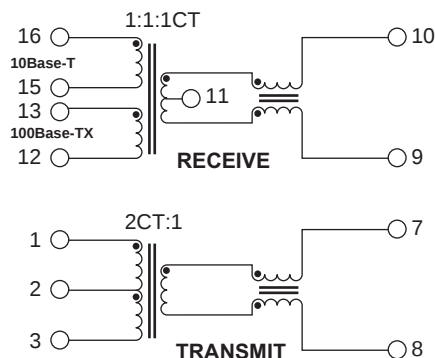
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Schematics

H1094



Application Notes

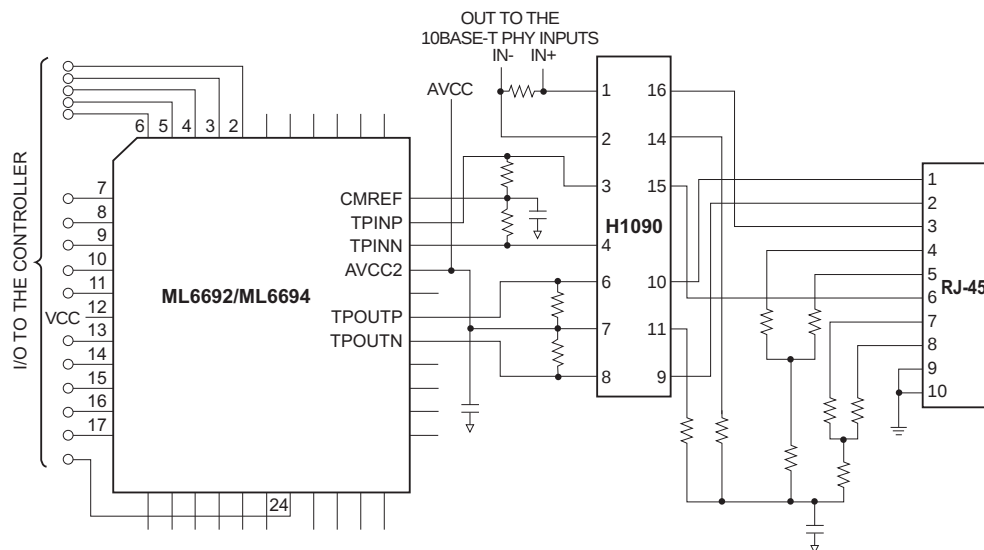
The magnetics modules have been specifically designed for implementation of 10/100 Mbps and 100Base-TX data links over data-grade unshielded twisted pair cable (UTP-5). The modules provide excellent EMI filtering and are targeted for adapter card and multiport applications. In designing these parts, Pulse has worked closely with members of the IEEE 802.3 and ANSI X3.263 committees to develop an analog interface that provides isolation and EMI filtering for MLT-3 and NRZ transceivers.

The H1090 and H1094 are designed to interface with the MicroLinear ML6692, ML6694, ML6697, or ML6698 in 10/100 Mbps applications. The H1038 is optimized for SEEQ or MITEL (formerly GEC Plessey) 10/100 Mbps applications.

The transformers used in the modules provide high voltage isolation, wide bandwidth, and fast rise time. They utilize stable ferrite materials to minimize the degradation of primary inductance with base-line wander. At least 350 μ H of inductance will be provided by the transformers in these modules when 0 - 8 mA is applied across the windings over a temperature range of 0° - 70°C. The High Speed LAN magnetics modules provide the designer with low cost analog components for simple and reliable designs. The parts are encased in IC-grade packaging, which shows superior performance under high temperature solder reflow conditions.

For information on TP-FDDI, Fast Ethernet, and ATM solutions, contact Pulse's HS LAN Marketing Group.

Typical Application Circuit



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