

< IGBT MODULES >

CM600DX-24S1

HIGH POWER SWITCHING USE INSULATED TYPE

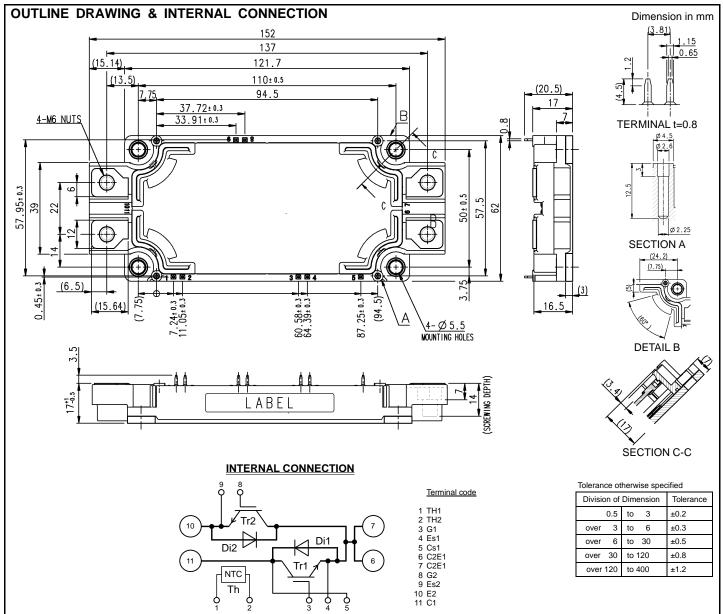


Dual switch (Half-Bridge)

- Flat base Type
- Copper base plate (non-plating)
- •Tin plating pin terminals
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.



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MAXIMUM RATINGS (T_j =25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
I _C	Collector ourrent	DC, T _C =94 °C (Note2, 4)	600	۸
I _{CRM}	Collector current	Pulse, Repetitive, V _{GE} =15 V (Note3)	1200	Α
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	3330	W
I _E (Note1)	Emitter current	DC (Note2)	600	^
I _{ERM} (Note1)		Pulse, Repetitive (Note3)	1200	Α

MODULE

Symbol	Item	Conditions	Rating	Unit
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T _{jmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature	(Note4)	125	
T _{jop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T_j =25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

0	16	0			Limits		11.2
Symbol	Item	Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =60 mA, V _{CE} =10 V		5.4	6.0	6.6	V
.,		I _C =600 A, V _{GE} =15 V,	T _j =25 °C	-	2.00	2.45	
V _{CEsat} (Terminal)		Refer to the figure of test circuit	T _j =125 °C	-	2.30	-	V
(Terminal)	Collector emitter esturation valters	(Note5)	T _j =150 °C	-	2.40	-	
.,	Collector-emitter saturation voltage	I _C =600 A,	T _j =25 °C	-	1.85	2.35	
V _{CEsat} (Chip)		V _{GE} =15 V,	T _j =125 °C	-	2.10	-	V
(Chip)		(Note5)	T _j =150 °C	-	2.15	-	
Cies	Input capacitance			-	-	50	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	10	nF
Cres	Reverse transfer capacitance			-	-	0.83	
Q _G	Gate charge	V _{CC} =600 V, I _C =600 A, V _{GE} =15 V		-	1050	-	nC
t _{d(on)}	Turn-on delay time	V _{CC} =600 V, I _C =600 A, V _{GE} =±15 V,		-	-	800	200
t _r	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time	T ₅		-	-	600	ns
t _f	Fall time	$R_G=0 \Omega$, Inductive load		-	-	300	
. (Note1)		 	T _j =25 °C	-	2.8	3.60	V
V _{EC} (Note1) (Terminal)			T _j =125 °C	-	2.4	-	
(Terminal)	Fasition collector valte as	(Note5)	T _j =150 °C	-	2.3	-	
(Note1)	Emitter-collector voltage	I _E =600 A,	T _j =25 °C	-	2.7	3.50	
V _{EC} (Note1) (Chip)		G-E short-circuited,	T _j =125 °C	-	2.3	-	V
(Chip)		(Note5)	T _j =150 °C	-	2.2	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =600 A, V _{GE} =±15 V,		-	-	300	ns
Q _{rr} (Note1)	Reverse recovery charge	R _G =0 Ω, Inductive load		-	16	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =600 A,		-	91.5	-	I
E _{off}	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=0 \Omega, T_{j}=150 \text{ °C},$		-	63.1	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	36.1	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note4)		-	-	0.4	mΩ
r _g	Internal gate resistance	Per switch		-	5.0	-	Ω

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ELECTRICAL CHARACTERISTICS (cont.; $T_j=25$ °C, unless otherwise specified) NTC THERMISTOR PART

Symbol	Itom	Conditions		Limits	Limits	Uni	Unit
	Item	Conditions	Min.	Тур. Мах.	Unit		
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ	
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%	
B _(25/50)	B-constant	Approximate by equation (Note6)	-	3375	-	K	
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW	

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Itom	Conditions	Limits			l lasis
	ltem	Conditions	Min.	Тур.	Max.	Unit
R _{th(j-c)Q}	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	45	K/kW
R _{th(j-c)D}		Junction to case, per Inverter DIODE (Note4)	-	-	72	r/KVV
R _{th(c-s)}	Contact thermal registeres	Case to heat sink, per 1 module,		15		K/kW
	Contact thermal resistance	Thermal grease applied (Note4, 7)	-	15	-	r/KVV

MECHANICAL CHARACTERISTICS

Symbol	ltom	Item Conditions		Limits			Unit
	Item	Conditions	Conditions		Тур.	Max.	Offic
M _t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N∙m
٦	Creepage distance	Terminal to terminal		17	-	-	mm
d _s		Terminal to base plate		18.5	-	-	
	Clearance	Terminal to terminal		10	-	-	
d _a	Clearance	Terminal to base plate		16.3	-	-	mm
m	mass	-		-	350	-	g
ес	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+100	μm

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (DIODE)

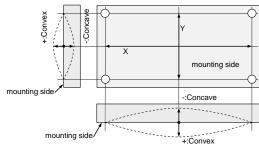
- 2. Junction temperature (T_j) should not increase beyond T_{jmax} rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed T_{jmax} rating.
- 4. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise.

$$6.\,\mathsf{B}_{\left(25/50\right)}=\!\mathsf{In}(\frac{\mathsf{R}_{25}}{\mathsf{R}_{50}})/\!(\frac{1}{\mathsf{T}_{25}}\!-\!\frac{1}{\mathsf{T}_{50}})\;,$$

 R_{25} : resistance at absolute temperature T_{25} [K]; T_{25} =25 [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ=0.9 W/(m·K).
- 8. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



9. Use the following screws when mounting the printed circuit board (PCB) on the stand offs. $^{\circ}$ ϕ 2.6×10 or ϕ 2.6×12 B1 tapping screw"

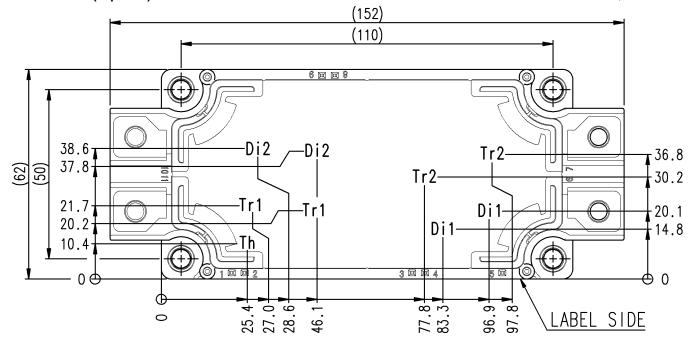
The length of the screw depends on thickness (t1.6~t2.0) of the PCB.

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions		Limits		Unit
	item	Conditions	Min.	Тур.	Max.	Utill
V _{CC}	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	0	-	6.8	Ω

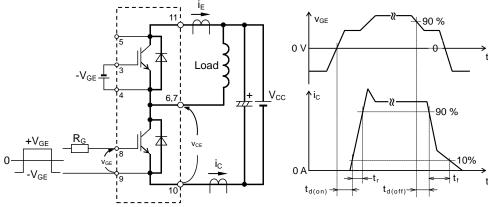
CHIP LOCATION (Top view)

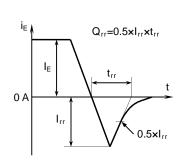
Dimension in mm, tolerance: ±1 mm



Tr1/Tr2: IGBT, Di1/Di2: DIODE, Th: NTC thermistor

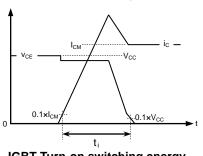
TEST CIRCUIT AND WAVEFORMS

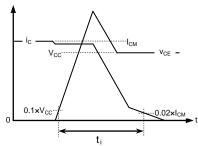


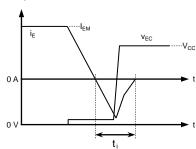


Switching characteristics test circuit and waveforms

trr, Qrr characteristics test waveform







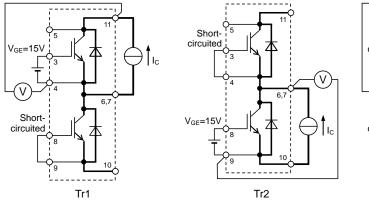
IGBT Turn-on switching energy

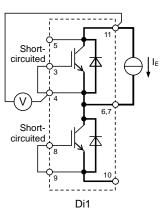
IGBT Turn-off switching energy

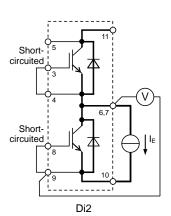
DIODE Reverse recovery energy

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT







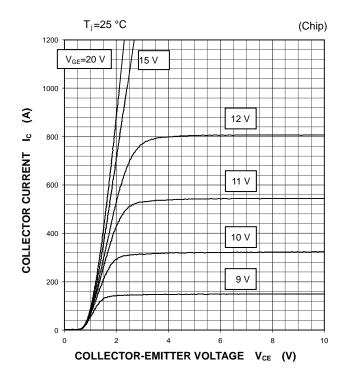
V_{CEsat} characteristics test circuit

V_{EC} characteristics test circuit

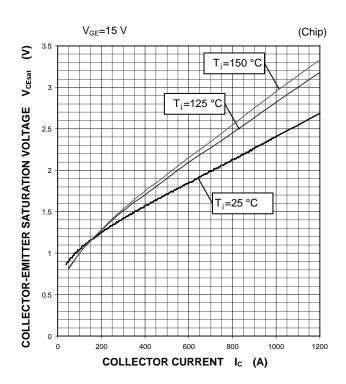
PERFORMANCE CURVES

INVERTER PART

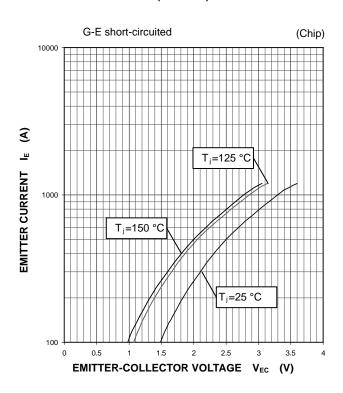
OUTPUT CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

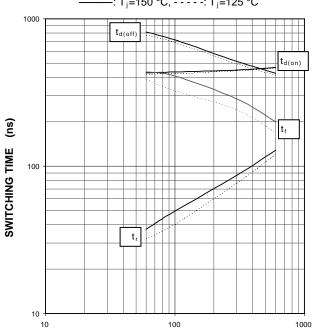


PERFORMANCE CURVES

INVERTER PART

HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

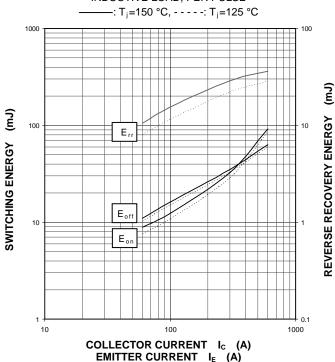
 V_{CC} =600 V, V_{GE} =±15 V, R_G =0 Ω , INDUCTIVE LOAD ——: T_j =150 °C, - - - - : T_j =125 °C



COLLECTOR CURRENT Ic (A)

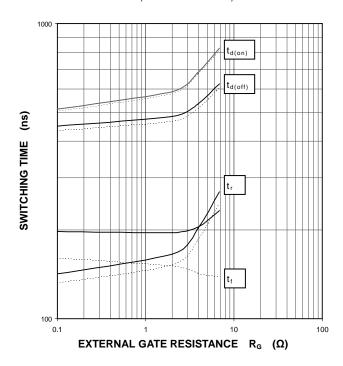
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, R_{G} =0 Ω , INDUCTIVE LOAD, PER PULSE



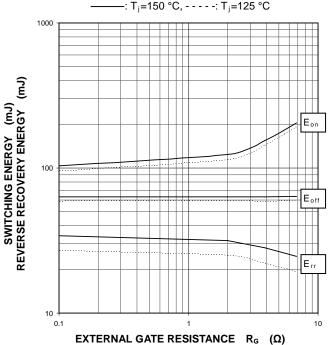
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, I_{C} =600 A, INDUCTIVE LOAD ———: T_i =150 °C, ----: T_i =125 °C



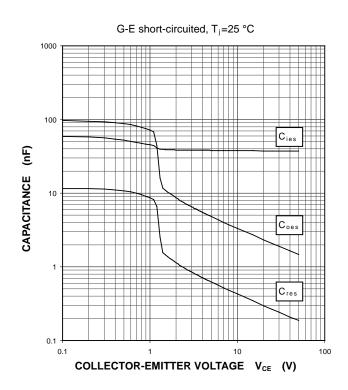
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=600 V, V_{GE}=±15 V, I_C/I_E=600 A, INDUCTIVE LOAD, PER PULSE

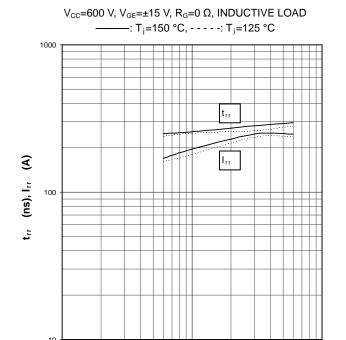


PERFORMANCE CURVES INVERTER PART

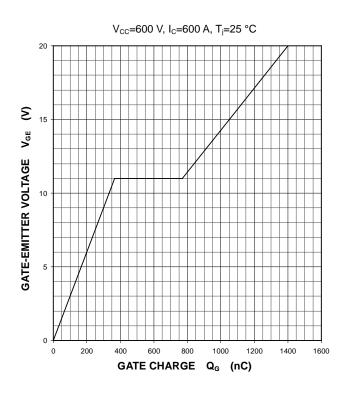
CAPACITANCE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



GATE CHARGE CHARACTERISTICS (TYPICAL)

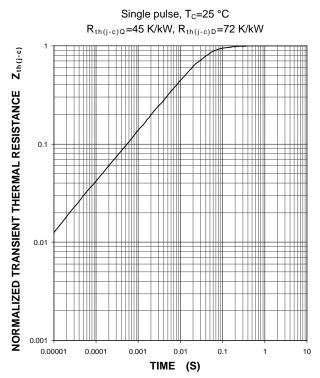


TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

EMITTER CURRENT IE (A)

1000

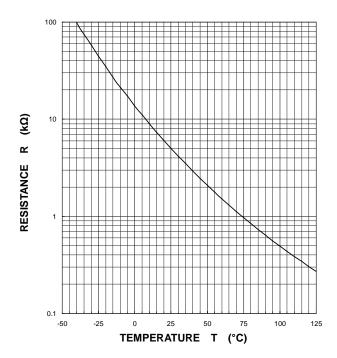
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PERFORMANCE CURVES

NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



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