

# **HD74LS30**

# 8-input Positive NAND Gate

REJ03D0404-0200 Rev.2.00 Feb.18.2005

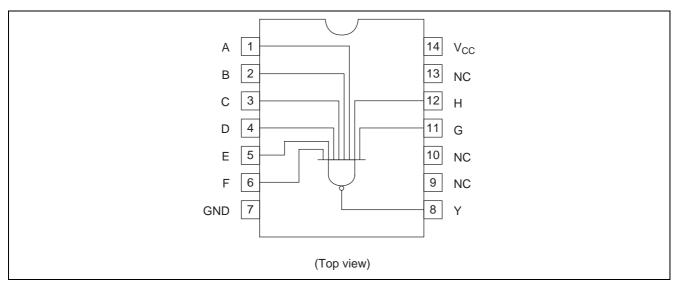
#### **Features**

• Ordering Information

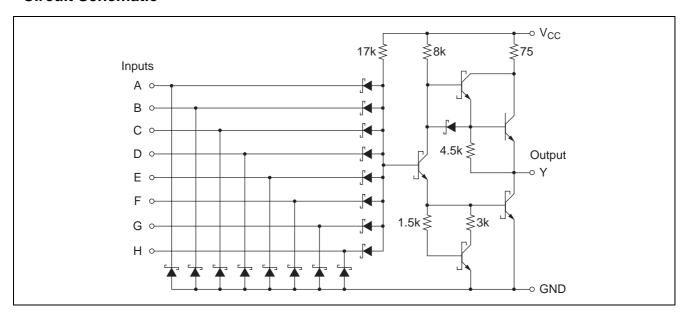
Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS30P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74LS30FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)
HD74LS30RPEL	SOP-14 pin (JEDEC)	PRSP0014DE-A (FP-14DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

# **Pin Arrangement**



### **Circuit Schematic**



## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage	Vcc	7	V
Input voltage	V <sub>IN</sub>	7	V
Power dissipation	P <sub>T</sub>	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

# **Recommended Operating Conditions**

ltem	Symbol	Min	Тур	Max	Unit
Supply voltage	Vcc	4.75	5.00	5.25	V
Output current	I <sub>OH</sub>	_	_	-400	μΑ
Output current	I <sub>OL</sub>	_	_	8	mA
Operating temperature	Topr	-20	25	75	°C

### **Electrical Characteristics**

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$ 

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	V <sub>IH</sub>	2.0	_	_	V	
Input voltage	V <sub>IL</sub>	_	_	0.8	V	
	V <sub>OH</sub>	2.7	_	_	V	$V_{CC} = 4.75 \text{ V}, V_{IL} = 0.8 \text{ V}, I_{OH} = -400 \mu\text{A}$
Output voltage	V <sub>OL</sub>	_	_	0.5	V	$I_{OL} = 8 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V}$
		_	_	0.4		$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.73 \text{ V}, \text{ VIH} = 2 \text{ V}$
	I <sub>IH</sub>	_	_	20	μΑ	$V_{CC} = 5.25 \text{ V}, V_{I} = 2.7 \text{ V}$
Input current	I <sub>IL</sub>	_	_	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_I = 0.4 \text{ V}$
	II	_	_	0.1	mA	$V_{CC} = 5.25 \text{ V}, V_I = 7 \text{ V}$
Short-circuit output current	I <sub>OS</sub>	-20	_	-100	mA	V <sub>CC</sub> = 5.25 V
Supply current	I <sub>CCH</sub>	_	0.35	0.5	mA	V <sub>CC</sub> = 5.25 V
	I <sub>CCL</sub>	_	0.6	1.1	mA	V <sub>CC</sub> = 5.25 V
Input clamp voltage	$V_{IK}$	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

Note:  $^*V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C}$ 

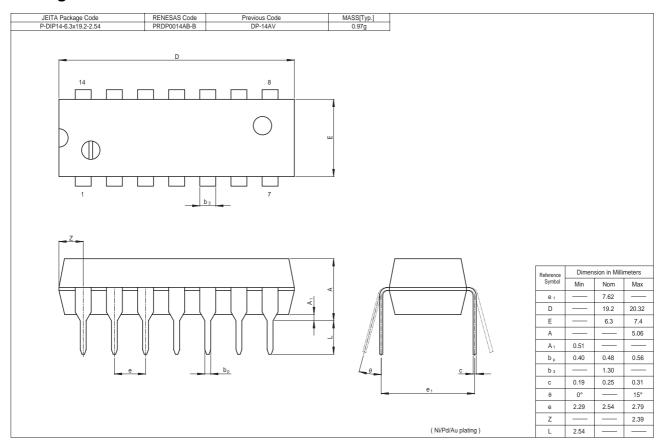
# **Switching Characteristics**

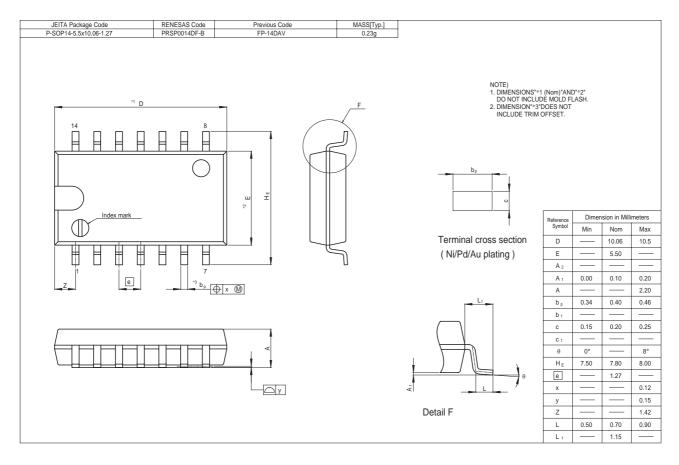
 $(V_{CC} = 5 \text{ V}, \text{Ta} = 25^{\circ}\text{C})$ 

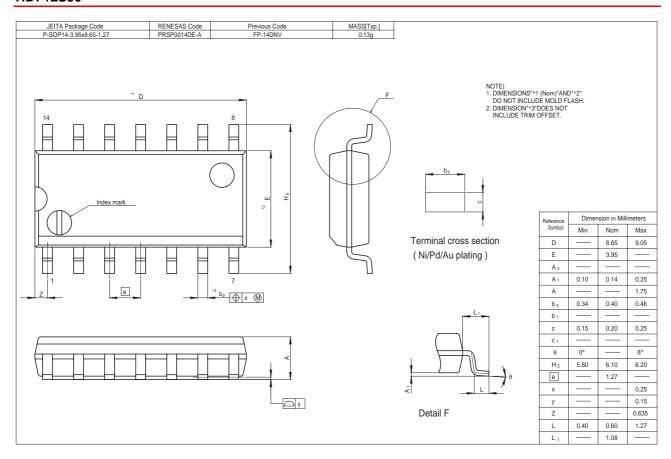
Item	Symbol	min.	typ.	max.	Unit	Condition
Propagation delay time	t <sub>PLH</sub>	_	8	15	ns	$C_1 = 15 \text{ pF}, R_1 = 2 \text{ k}\Omega$
	t <sub>PHL</sub>	_	13	20	ns	$G_L = 15  \text{pr},  \text{RL} = 2  \text{Ksz}$

Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".

### **Package Dimensions**







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