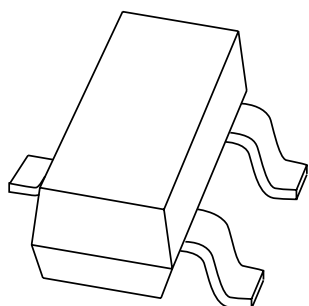


DATA SHEET



BFS19

NPN medium frequency transistor

Product specification
Supersedes data of 1997 Jul 02

1999 Apr 15

NPN medium frequency transistor

BFS19

FEATURES

- Low current (max. 30 mA)
- Low voltage (max. 20 V).

APPLICATIONS

- Medium frequency applications in thick and thin-film circuits.

DESCRIPTION

NPN medium frequency transistor in a SOT23 plastic package.

MARKING

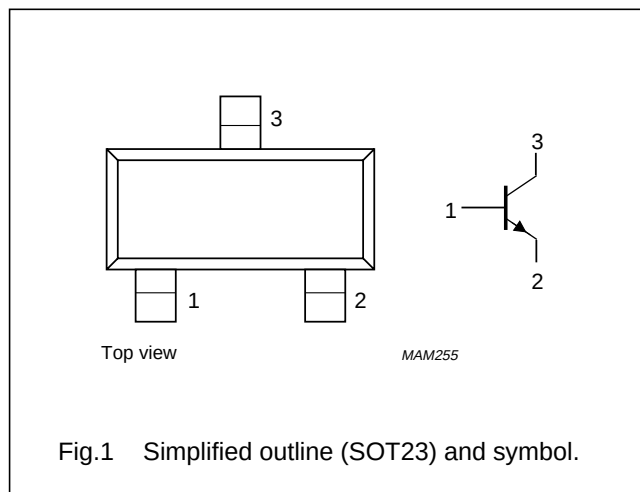
TYPE NUMBER	MARKING CODE ⁽¹⁾
BFS19	F2*

Note

- * = p : Made in Hong Kong.
* = t : Made in Malaysia.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	30	V
V_{CEO}	collector-emitter voltage	open base	–	20	V
V_{EBO}	emitter-base voltage	open collector	–	5	V
I_C	collector current (DC)		–	30	mA
I_{CM}	peak collector current		–	30	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$; note 1	–	250	mW
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	150	°C
T_{amb}	operating ambient temperature		–65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

NPN medium frequency transistor

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

$T_j = 25\text{ °C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{CBO}	collector cut-off current	$I_E = 0; V_{CB} = 20\text{ V}$	–	–	100	nA
		$I_E = 0; V_{CB} = 20\text{ V}; T_j = 100\text{ °C}$	–	–	10	μA
I_{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = 5\text{ V}$	–	–	100	nA
h_{FE}	DC current gain	$I_C = 1\text{ mA}; V_{CE} = 10\text{ V}$	65	–	225	
V_{BE}	base-emitter voltage	$I_C = 1\text{ mA}; V_{CE} = 10\text{ V}$	650	–	740	mV
C_c	collector capacitance	$I_E = 0; V_{CB} = 10\text{ V}; f = 1\text{ MHz}$	–	1	–	pF
C_{re}	feedback capacitance	$I_C = 0\text{ mA}; V_{CB} = 10\text{ V}; f = 1\text{ MHz}$	–	0.85	–	pF
f_T	transition frequency	$I_C = 1\text{ mA}; V_{CE} = 10\text{ V}; f = 100\text{ MHz}$	–	260	–	MHz

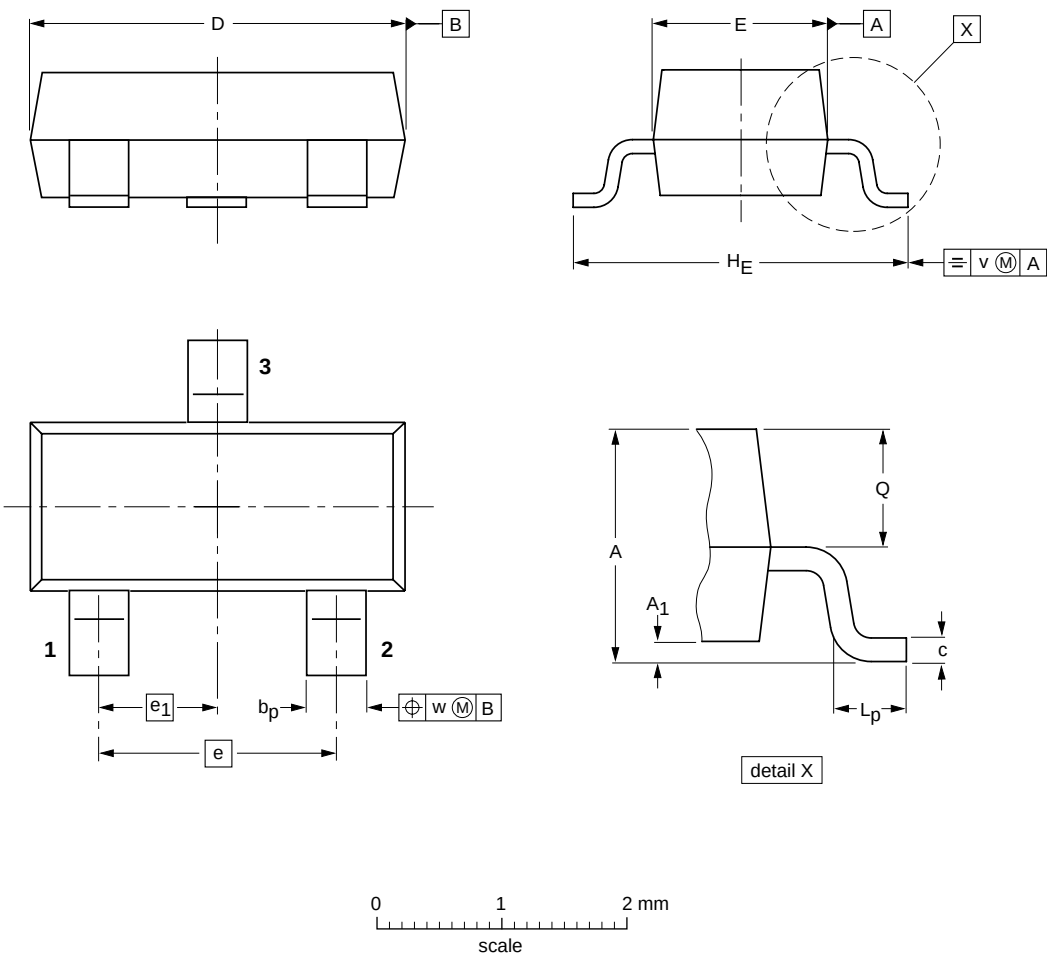
NPN medium frequency transistor

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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁ max.	b _p	c	D	E	e	e ₁	H _E	L _p	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT23						97-02-28

NPN medium frequency transistor

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DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

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NOTES

NPN medium frequency transistor

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NOTES

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Printed in The Netherlands

115002/00/03/pp8

Date of release: 1999 Apr 15

Document order number: 9397 750 05673

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