

Product Summary (@T_A = +25°C)

V _{RRM} (V)	I _o (A)	V _F (V)	I _R (μA)
1000	0.8	1.1	5

Description and Applications

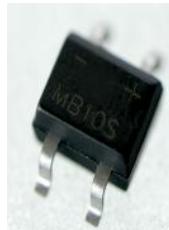
Suitable for AC to DC bridge full wave rectification for SMPS, LED lighting, adapter, battery charger, home appliances, office equipment, and telecommunication applications.

Features and Benefits

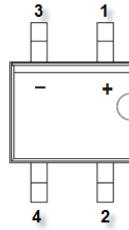
- Glass Passivated Die Construction
- Miniature Package Saves Space on PC Boards
- Low Leakage Current
- Ideal for SMT Manufacturing
- Low Forward Voltage Drop
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

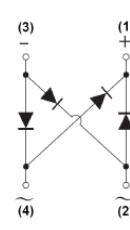
- Case: MBS
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Lead Free Plating (Matte Tin Finish). Solderable per MIL-STD-202, Method 208 (E3)
- Polarity: As Marked on Body
- Weight: 0.11 grams (Approximate)



Top View



Pin Diagram



Internal Schematic

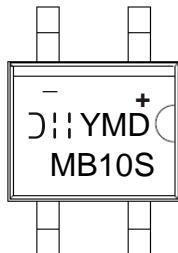
Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
MB10S-13	Commercial	MBS	3,000/Tape & Reel

Notes:

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



MB10S= Product Type Marking Code

D11= Manufacturers' Code Marking

YMD = Date Code Marking

Y = Last Digit of Year (ex: 7 = 2017)

M = See Month/Code Table Below

D = Day 1~9 = 1~9; Day 10~31= A~V

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	1,000	V
RMS Reverse Voltage	$V_R(\text{RMS})$	700	V
Average Rectified Output Current (Note 5)@ $T_A = +40^\circ\text{C}$ (Note 6)@ $T_A = +40^\circ\text{C}$	I_O	0.5 0.8	A
Non-Repetitive Peak Forward Surge Current, 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I_{FSM}	30	A
I^2t Rating for Fusing (1ms < t < 8.3ms)	I^2t	3.74	A^2s

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance, Junction to Ambient (Note 6) (Per Element)	$R_{\theta JA}$	101	$^\circ\text{C}/\text{W}$
Typical Thermal Resistance, Junction to Lead (Per Element)	$R_{\theta JL}$	42	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	$V_{(BR)R}$	1,000	—	—	V	$I_R = 5\mu\text{A}$
Forward Voltage (Per Element)	V_F	—	0.93	1.1	V	$I_F = 0.8\text{A}, T_A = +25^\circ\text{C}$
Leakage Current (Note 7) (Per Element)	I_R	— —	0.2 21	5 500	μA	$V_R = 1,000\text{V}, T_A = +25^\circ\text{C}$ $V_R = 1,000\text{V}, T_A = +125^\circ\text{C}$
Total Capacitance (Per Element)	C_T	—	8	—	pF	$V_R = 4\text{V}, f = 1.0\text{MHz}$

Notes: 5. Device mounted on FR-4 substrate, 1" * 1", 2oz, single-sided, PC boards with 0.1" * 0.15" copper pad.
 6. Device mounted on FR-4 substrate, 0.4" * 0.5", 2oz, single-sided, PC boards with 0.2" * 0.25" copper pad.
 7. Short duration pulse test used to minimize self-heating effect.

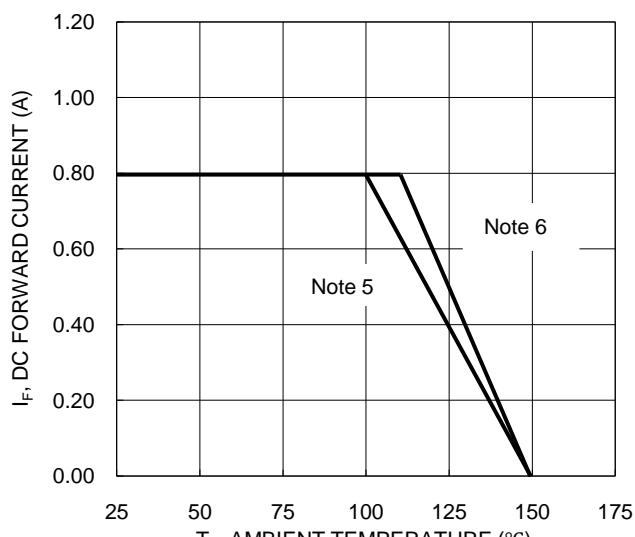


Figure 1. DC Forward Current Derating

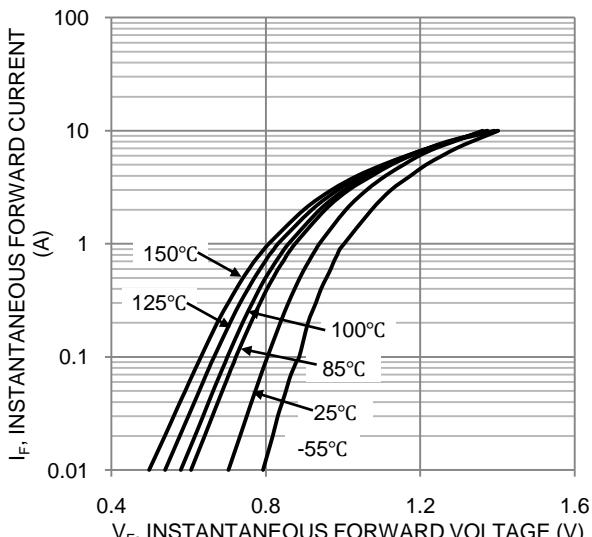


Figure 2. Typical Forward Characteristics (Per Leg)

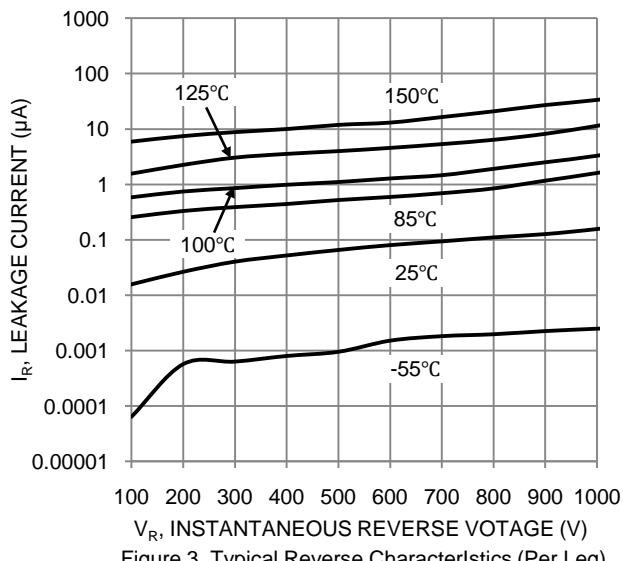


Figure 3. Typical Reverse Characteristics (Per Leg)

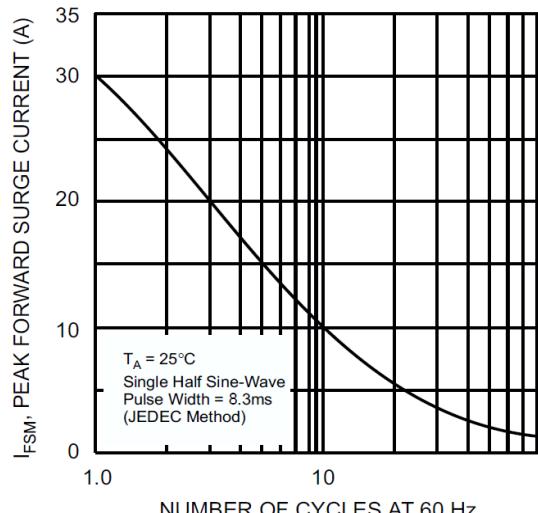


Figure 4. Maximum Peak Forward Surge Current (Per Leg)

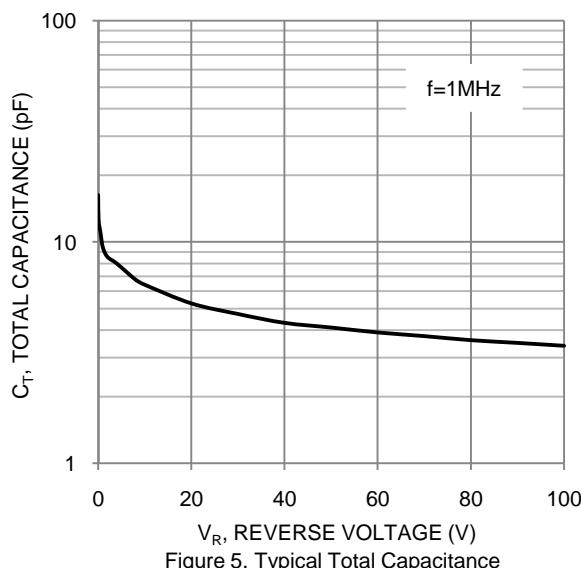
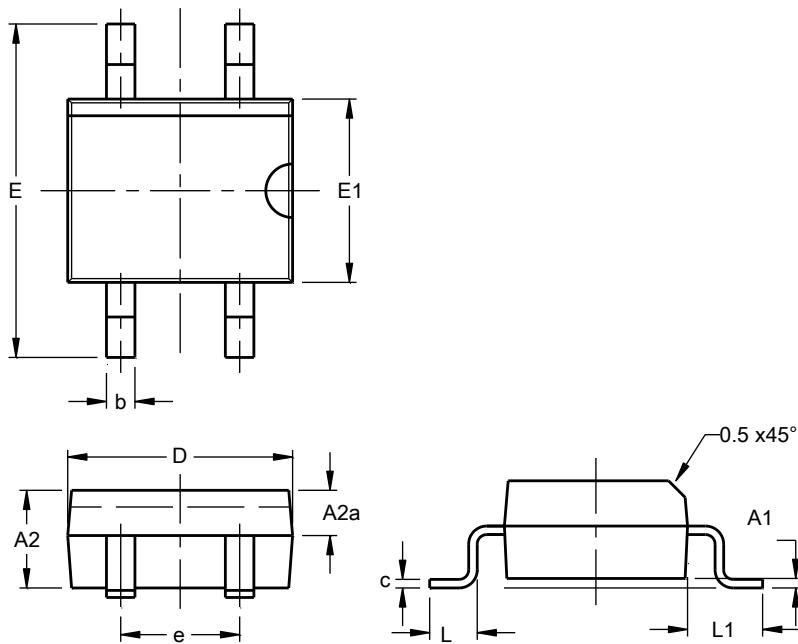


Figure 5. Typical Total Capacitance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

MBS



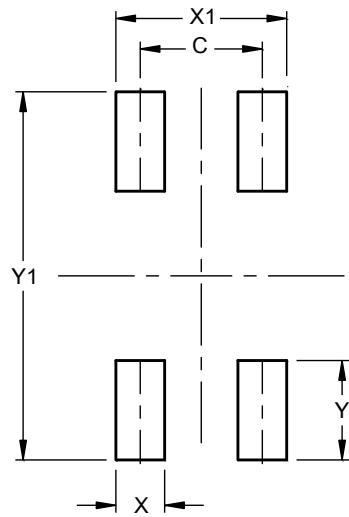
MBS			
Dim	Min	Max	Typ
A1	--	0.20	--
A2	2.30	2.70	--
A2a	0.90	1.30	--
b	0.50	0.70	--
c	0.15	0.25	--
D	4.50	4.95	--
E	--	7.00	--
E1	3.60	4.10	--
e	2.30	2.70	--
L	0.60	1.10	--
L1	--	1.70	--

All Dimensions in mm

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

MBS



Dimensions	Value (in mm)
C	2.50
X	1.00
X1	3.50
Y	2.15
Y1	7.50

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