

Description

The ZXTR2105F monolithically integrates a transistor, zener diode and resistor to function as a linear regulator. The device regulates with a 5V nominal output at 15mA. It is designed for use in high-voltage applications where standard linear regulators cannot be used. This function is fully integrated into a SOT23 package, minimizing PCB area and reducing the number of components when compared with a multi-chip discrete solution.

Applications

Supply voltage regulation for:

- 12V to 5V Rails
- 24V to 5V Rails
- Other Customized Input Rails

Features

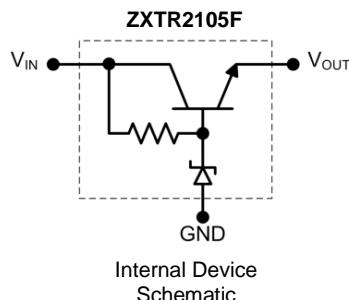
- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage = 7V to 60V (For regulated output Voltage)
- Output Voltage = 5V \pm 5%
- Fully Integrated into a SOT23 Package
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **An Automotive-Compliant Part is Available Under Separate Datasheet ([ZXTR2105FQ](#))**

Mechanical Data

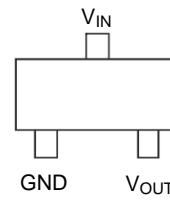
- Case: SOT23
- Case Material: Molded Plastic "Green" Molding Compound; UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208 ^(e3)
- Weight: 0.008 grams (Approximate)



Top View



Internal Device Schematic



Top View
Pin-Out

Pin Name	Pin Function
V _{IN}	Input Supply
GND	Power Ground
V _{OUT}	Voltage Output

Ordering Information (Note 5)

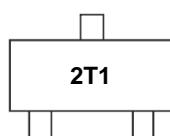
Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ZXTR2105F-7	AEC-Q101	2T1	7	8	3,000

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information

SOT23



2T1 = Product Type Marking Code

Absolute Maximum Ratings (Voltage relative to GND, @ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Voltage	V_{IN}	-0.3 to 60	V
Continuous Input and Output Current	I_{IN}, I_{OUT}	320	mA
Peak Pulsed Input and Output Current	I_{IM}, I_{OM}	2	A
Maximum Voltage Applied to V_{OUT}	$V_{OUT(MAX)}$	Smaller of $V_{IN}+5\text{V}$ or 10V	V

Maximum Current at $V_{IN} = 12\text{V}$ (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Continuous Output Current	I_{OUT}	89	mA
Pulsed Output Current	I_{OM}	2,000	mA
		890	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation	P_D	625	mW
		500	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	°C/W
		250	
Thermal Resistance, Junction to Lead	$R_{\theta JL}$	197	°C
Thermal Resistance, Junction to Case	$R_{\theta JC}$	17	
Maximum Operating Junction and Storage Temperature Range	T_J, T_{STG}	-65 to +150	°C

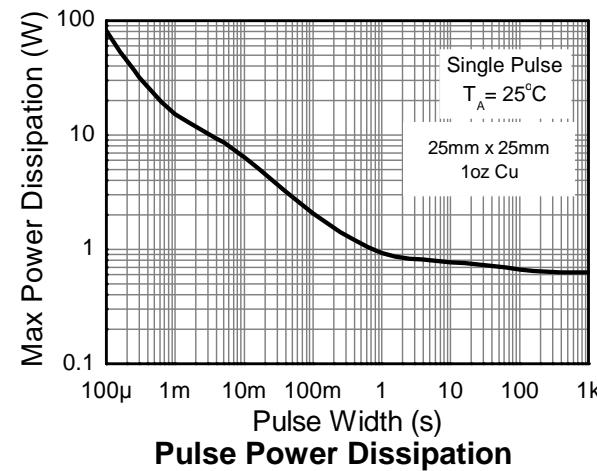
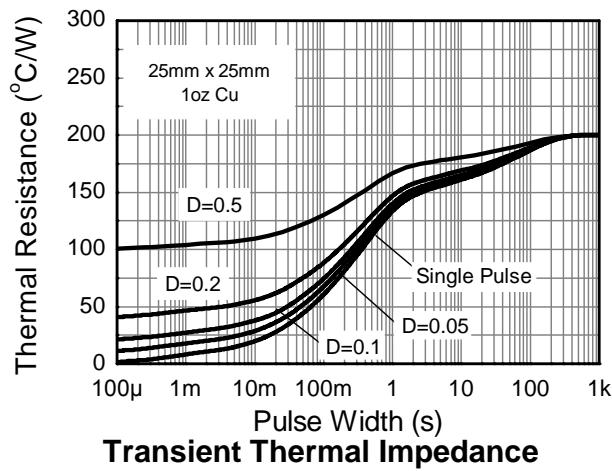
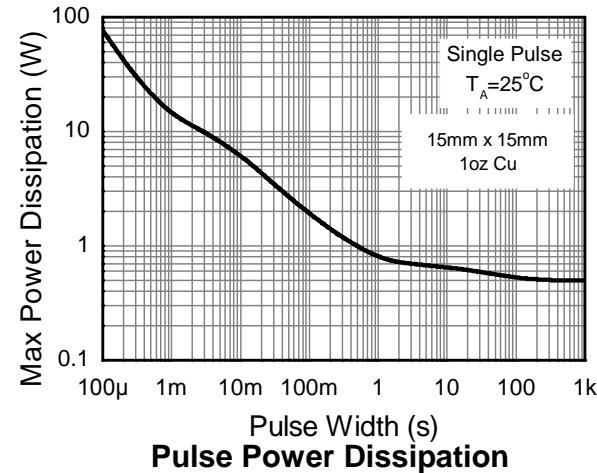
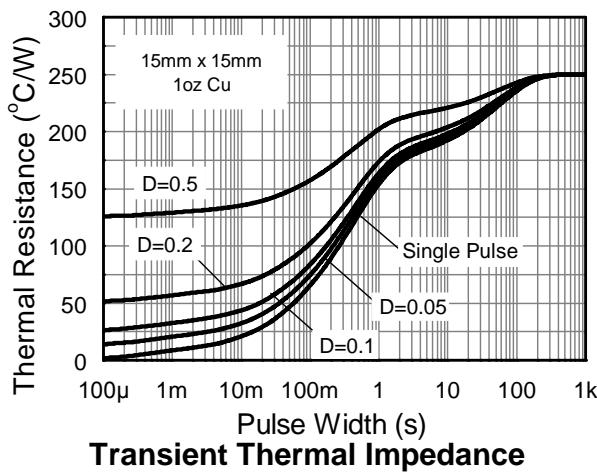
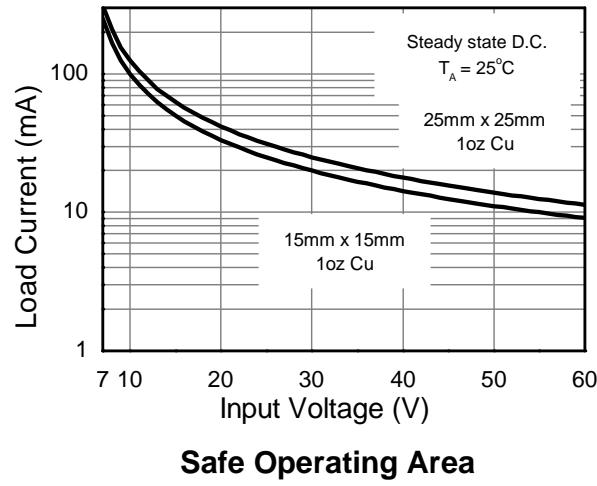
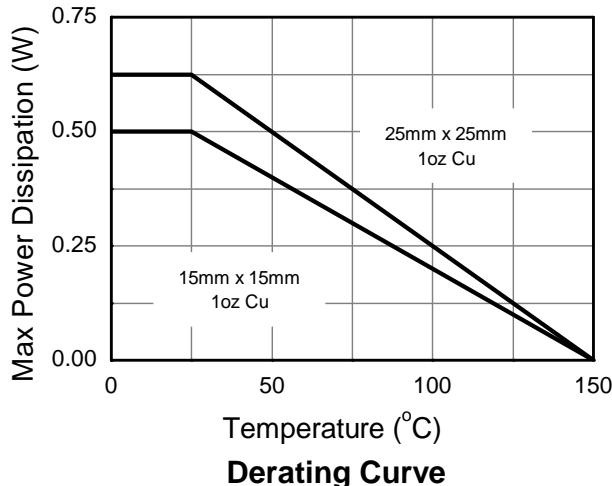
ESD Ratings (Note 11)

Characteristics	Symbols	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Machine Model	ESD MM	400	V	C

Notes:

- 5. For a device mounted with the V_{IN} lead on 25mm x 25mm 1oz copper that is on a single-sided 1.6mm FR-4 PCB; device is measured under still air conditions whilst operating in steady-state.
- 6. Same as Note 5, except mounted on 15mm x 15mm 1oz copper.
- 7. Same as Note 5, whilst operating at $V_{IN}=12\text{V}$. Refer to Safe Operating Area for other Input Voltages.
- 8. Same as Note 5, except measured with a single pulse width = 100μs and $V_{IN}=12\text{V}$.
- 9. Same as Note 5, except measured with a single pulse width = 10ms and $V_{IN}=12\text{V}$.
- 10. $R_{\theta JL}$ = Thermal resistance from junction to solder-point (at the end of the V_{IN} lead). $R_{\theta JC}$ = Thermal resistance from junction to the top of case.
- 11. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

Thermal Characteristics and Derating Information



Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Output Voltage (Note 12)	V_{OUT}	4.75	5.0	5.25	V	$V_{\text{IN}} = 12\text{V}$, $I_{\text{OUT}} = 15\text{mA}$
Line Regulation (Notes 12 & 13)	ΔV_{OUT}	—	33	220	mV	$V_{\text{IN}} = 10\text{V}$ to 15V , $I_{\text{OUT}} = 15\text{mA}$
		—	400	700		$V_{\text{IN}} = 7\text{V}$ to 60V , $I_{\text{OUT}} = 15\text{mA}$
		—	145	400		$V_{\text{IN}} = 10\text{V}$ to 60V , $I_{\text{OUT}} = 15\text{mA}$
Temperature Coefficient	$\Delta V_{\text{OUT}}/\Delta T$	—	3.52	—	mV/°C	$T_J = -40^\circ\text{C}$ to $+150^\circ\text{C}$ $V_{\text{IN}} = 12\text{V}$, $I_{\text{OUT}} = 15\text{mA}$
Load Regulation (Notes 12 & 14)	ΔV_{OUT}	—	-20 -166	-130 -300	mV	$I_{\text{OUT}} = 10\text{mA}$ to 20mA , $V_{\text{IN}} = 12\text{V}$ $I_{\text{OUT}} = 0.1\text{mA}$ to 50mA , $V_{\text{IN}} = 12\text{V}$
Minimum Value of Input Voltage Required to Maintain Line Regulation	$V_{\text{IN(MIN)}}$	7	—	—	V	—
Quiescent Current	I_Q	—	450 4,000	800 6,700	μA	$V_{\text{IN}} = 12\text{V}$, $I_{\text{OUT}} = 10\text{μA}$ $V_{\text{IN}} = 60\text{V}$, $I_{\text{OUT}} = 10\text{μA}$
Power Supply Rejection Ratio	$\Delta V_{\text{IN}}/\Delta V_{\text{OUT}}$	—	46	—	dB	$C_{\text{OUT}} = 100\text{nF}$, $I_{\text{OUT}} = 15\text{mA}$, $V_{\text{OUT}} = 5\text{V}$, $V_{\text{IN}} = 7\text{V}$ to 60V , $f = 100\text{Hz}$

Notes: 12. Measured Under Pulsed Conditions; Pulse Width $\leq 300\text{μs}$. Duty cycle $\leq 2\%$.

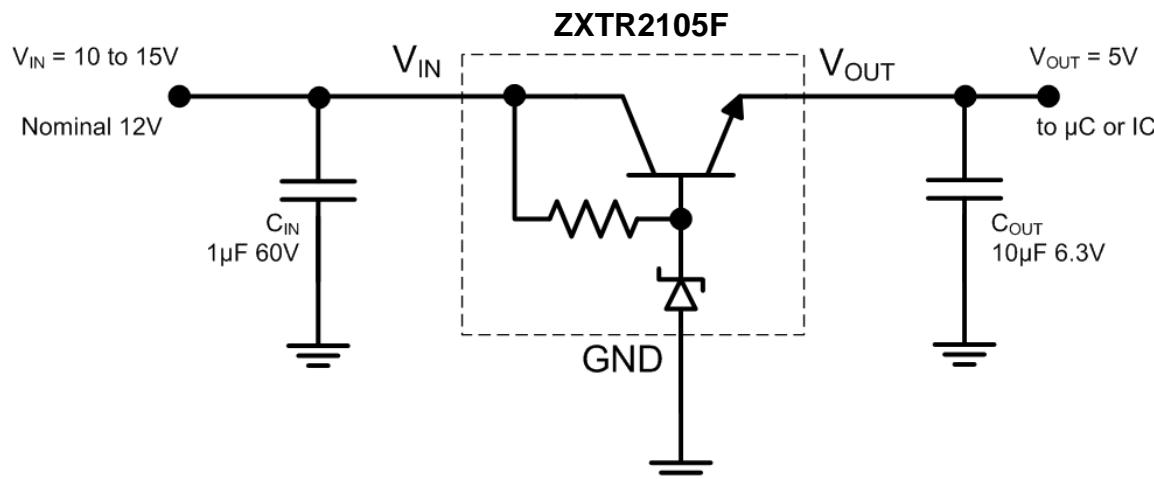
13. Line Regulation $\Delta V_{\text{OUT}} = V_{\text{OUT}}(@V_{\text{IN}} = 15\text{V}) - V_{\text{OUT}}(@V_{\text{IN}} = 10\text{V})$

$\Delta V_{\text{OUT}} = V_{\text{OUT}}(@V_{\text{IN}} = 60\text{V}) - V_{\text{OUT}}(@V_{\text{IN}} = 7\text{V})$

$\Delta V_{\text{OUT}} = V_{\text{OUT}}(@V_{\text{IN}} = 60\text{V}) - V_{\text{OUT}}(@V_{\text{IN}} = 10\text{V})$

14. Load Regulation $\Delta V_{\text{OUT}} = V_{\text{OUT}}(@I_{\text{OUT}} = 20\text{mA}) - V_{\text{OUT}}(@I_{\text{OUT}} = 10\text{mA})$

$\Delta V_{\text{OUT}} = V_{\text{OUT}}(@I_{\text{OUT}} = 50\text{mA}) - V_{\text{OUT}}(@I_{\text{OUT}} = 0.1\text{mA})$

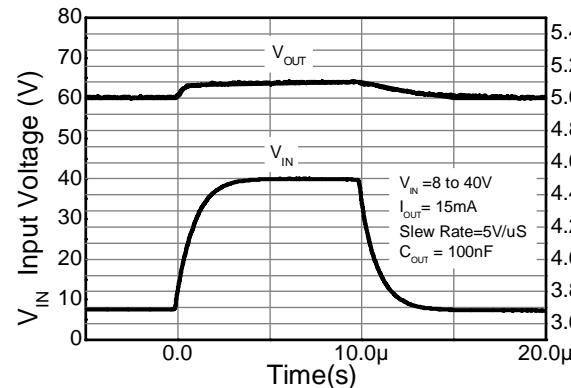
Typical Application Circuit


Example of a 5V regulated supply from a nominal 12V for powering a Controller IC.

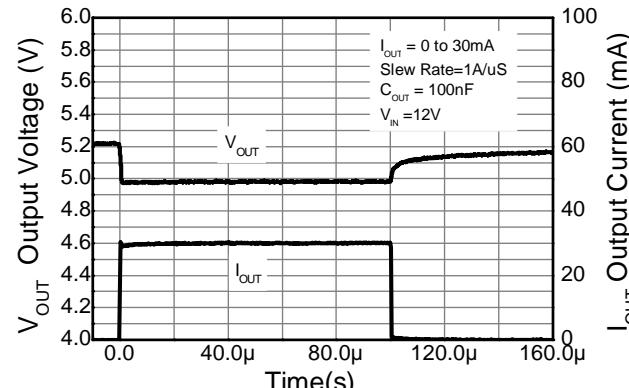
Pin Functions

Pin Name	Pin Function	Notes
V_{IN}	Input Supply	Input voltage can vary from -0.3V to 60V with respect to GND; for V_{OUT} regulated then $7\text{V} \leq V_{\text{IN}} \leq 60\text{V}$. It is recommended to connect a $1\mu\text{F}$ capacitor to GND.
GND	Power Ground	This pin should be tied to the system ground.
V_{OUT}	Voltage Output	Outputs a regulated 5V when $7\text{V} \leq V_{\text{IN}} \leq 60\text{V}$. When $V_{\text{IN}} < 7\text{V}$, then V_{OUT} maximum = $V_{\text{IN}} - 1\text{V}$. The pin can be pulled high to a maximum of +10V with respect to GND, or +5V with respect to V_{IN} , whichever is lower. It is recommended to connect a $10\mu\text{F}$ capacitor to GND and a minimum of $10\mu\text{A}$ to be drawn from V_{OUT} to maintain regulation.

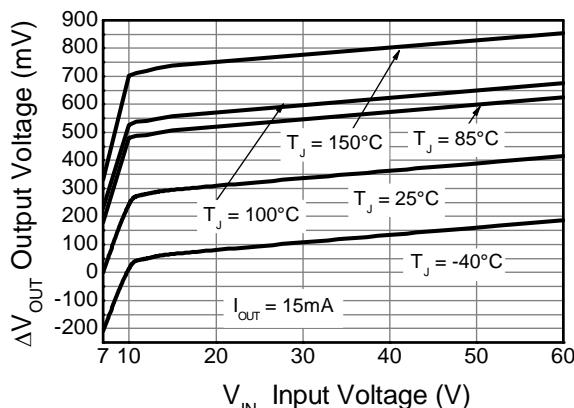
Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)



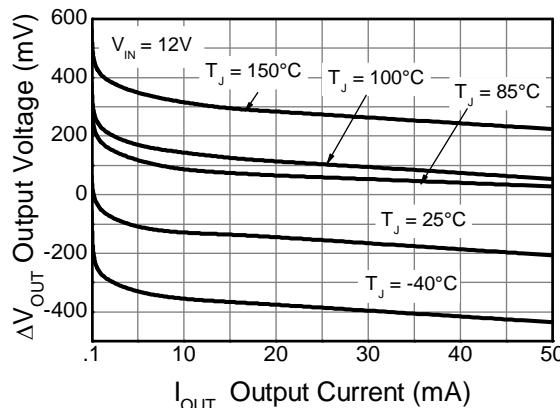
Line transient response



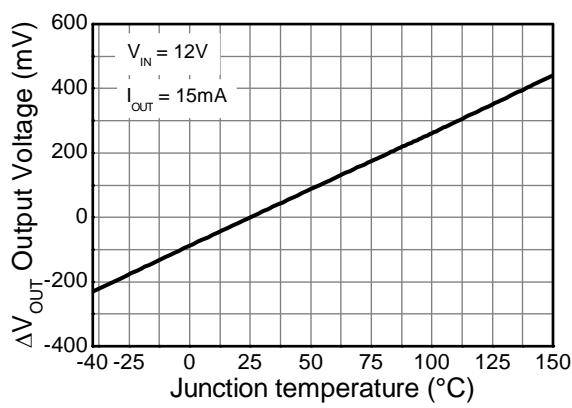
Load transient response



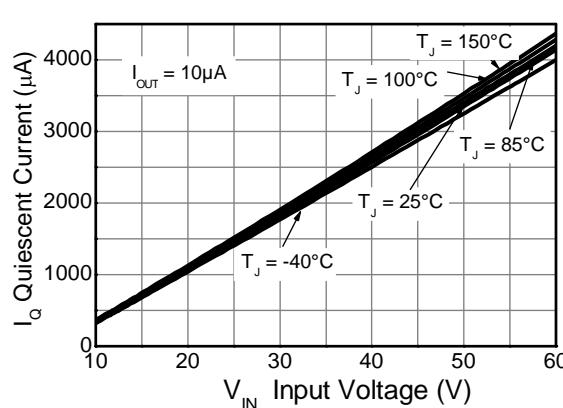
Line Regulation (Note 15)



Load Regulation (Note 16)



Temperature Coefficient (Note 17)



Quiescent Current

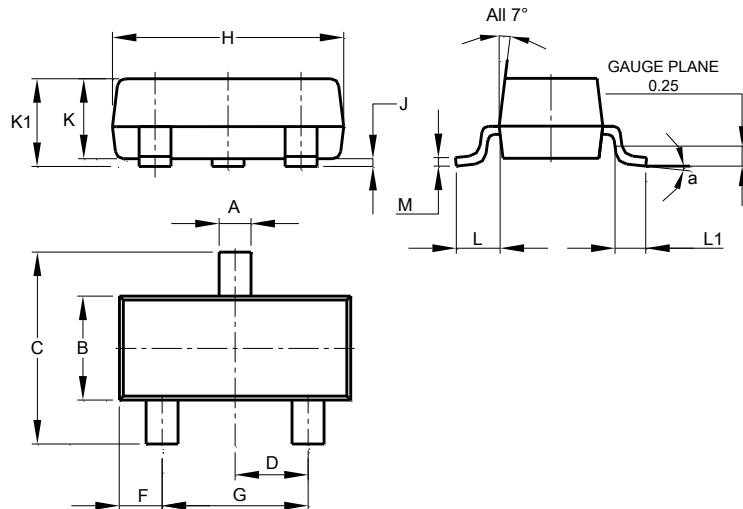
Notes:

15. Line Regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@ V_{IN} = 7\text{V}, I_{OUT} = 15\text{mA}, T_J = +25^\circ\text{C})$.
16. Load Regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@ V_{IN} = 12\text{V}, I_{OUT} = 0.1\text{mA}, T_J = +25^\circ\text{C})$.
17. Temperature Coefficient $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@ V_{IN} = 12\text{V}, I_{OUT} = 15\text{mA}, T_J = +25^\circ\text{C})$.

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23



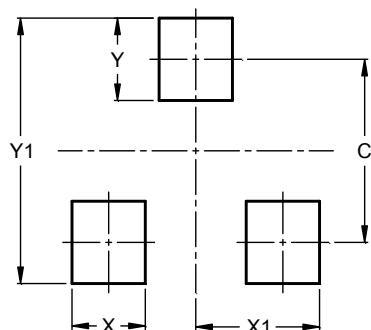
SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--

All Dimensions in mm

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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