TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WZ34FU, TC7WZ34FK

Triple Non-Inverter

Features

• High output current : ±24 mA (min) at V_{CC} = 3 V

• Super high speed operation : t_{pd} = 2.4 ns (typ.)

at V_{CC} = 5 V, 50 pF

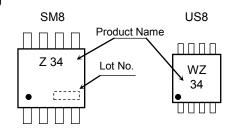
• Operating voltage range : V_{CC} = 1.65 to 5.5 V

5.5-V tolerant inputs

• 5.5-V power down protection outputs

• Matches the performance of TC74LCX series when operated at 3.3 V $\rm V_{CC}$

Marking



TC7WZ34FU SSOP8-P-0.65 TC7WZ34FK (SM8) SSOP8-P-0.50A (US8)

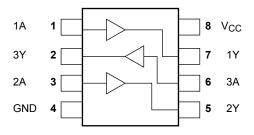
Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC} -0.5 to 6		V	
DC input voltage	V _{IN}	−0.5 to 6	V	
DC output voltage	\/	-0.5 to 6 (Note 1)	V	
DC output voltage	V _{OUT}	-0.5 to V _{CC} +0.5 (Note 2)	V	
Input diode current	I _{IK}	-20	mA	
Output diode current	lok	-20 (Note 3)	mA	
DC output current	lout	±50	mA	
DC V _{CC} /ground current	Icc	±50	mA	
Power dissipation	P _D	300 (SM8) 200 (US8)	mW	
Storage temperature	T _{stg}	−65 to 150	°C	
Lead temperature (10s)	TL	260	°C	

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0V$

Note 3: Vout < GND

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings.

Start of commercial production

2000-08



IEC Logic Symbol

Truth Table



Α	Υ
L	L
Н	Н

Operating Ranges

Characteristics	Symbol	Rating	Unit		
Cupply voltage	V	1.65 to 5.5	V		
Supply voltage	V _{CC}	1.5 to 5.5 (Note 4)			
Input voltage	V _{IN}	0 to 5.5	V		
Output voltage	V _{OUT}	0 to 5.5 (Note 5)	V		
		0 to V _{CC} (Note 6)			
Operating temperature	T _{opr}	−40 to 85	°C		
	dt/dv	0 to 20 (V_{CC} = 1.80 V ± 0.15 V, 2.5 V ± 0.2 V)	ns/V		
Input rise and fall time		0 to 10 ($V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$)			
		0 to 5 (V _{CC} = 5.0 V ± 0.5 V)			

Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or low state



Electrical Characteristics

DC Characteristics

Characteristics Symbol Tes		Condition		Ta = 25°C			Ta = -40 to 85°C		l lait		
		rest	Test Condition		Min	Тур.	Max	Min	Max	Unit	
High		\/	_		1.65 to 1.95	V _{CC} × 0.75	ı	ı	V _{CC} × 0.75	ı	- V
Input voltage Low level	V _{IH}	2.3 to 5.5			V _{CC} × 0.7	_	_	V _{CC} × 0.7	_		
	V _{IL}	_		1.65 to 1.95	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25		
				2.3 to 5.5	-	_	V _{CC} × 0.3	_	V _{CC} × 0.3		
					1.65	1.55	1.65	1	1.55	1	
				I _{OH} = -100 μA	2.3	2.2	2.3	-	2.2	-	
				ΙΟΗ = – 100 μΑ	3.0	2.9	3.0	_	2.9	_	
					4.5	4.4	4.5	_	4.4	-	
	High level	V _{OH}	$V_{IN} = V_{IH}$	I _{OH} = -4 mA	1.65	1.29	1.52	_	1.29	_	
lever				I _{OH} = -8 mA	2.3	1.9	2.14	_	1.9	-	
				I _{OH} = -16 mA	3.0	2.4	2.75	_	2.4	_	
				I _{OH} = -24 mA	3.0	2.3	2.62	_	2.3	_	
			I _{OH} = -32 mA	4.5	3.8	4.13	_	3.8	_	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Output voltage		1/01	$V_{IN} = V_{IL}$	I _{OL} = 100 μA	1.65	-	0	0.1	_	0.1	V
					2.3	_	0	0.1	_	0.1	
					3.0	_	0	0.1	_	0.1	
					4.5	-	0	0.1	_	0.1	
Low	Low level			I _{OL} = 4 mA	1.65	_	0.08	0.24	_	0.24	
				I _{OL} = 8 mA	2.3	1	0.1	0.3	_	0.3	
				I _{OL} = 16 mA	3.0	1	0.16	0.4	_	0.4	
				I _{OL} = 24 mA	3.0	-	0.24	0.55	_	0.55	
				I _{OL} = 32 mA	4.5		0.25	0.55	_	0.55	
Input leakage cur	rent	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5		-	±1	_	±10	μА
Power off leakage current	•	l _{OFF}	V _{IN} or V _{OU}	_{JT} = 5.5 V	0.0	_	ı	1	_	10	μА
Quiescent supply current I _{CC} V _{IN}		V _{IN} = 5.5 V	V _{IN} = 5.5 V or GND		-	_	1	_	10	μА	

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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Ob ana stanistica	O. made al	Total Consulting		Ta = 25°C Ta =			Ta = -40	a = -40 to 85°C	
Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pLH t _{pHL}	C_L = 15 pF, R_L = 1 M Ω	1.8 ± 0.15	2.0	4.4	9.5	2.0	10.0	- ns
			2.5 ± 0.2	1.0	3.0	5.2	1.0	5.8	
			3.3 ± 0.3	0.8	2.3	3.6	0.8	4.0	
			5.0 ± 0.5	0.5	1.8	2.9	0.5	3.2	
		$C_L = 50 \text{ pF},$ $R_L = 500 \Omega$	3.3 ± 0.3	1.2	3.0	4.6	1.2	5.1	
			5.0 ± 0.5	0.8	2.4	3.8	0.8	4.2	
Input capacitance	C _{IN}	_	0 to 5.5	_	3.0	_	_	_	pF
Power dissipation capacitance	C _{PD}	(Note 7)	3.3	_	24	_	_	_	٠,٢
		(Note 7)	5.5	_	34	_	_	_	pF

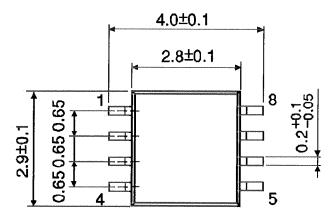
Note 7: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

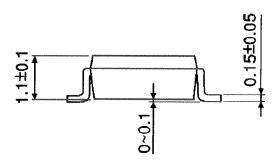
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$

Package Dimensions

SSOP8-P-0.65 Unit: mm



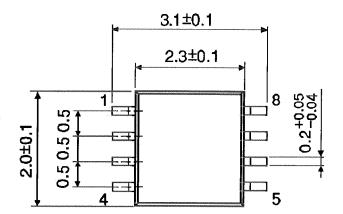


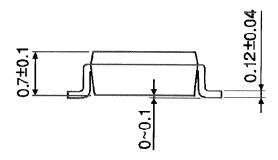
Weight: 0.02 g (typ.)

Unit: mm

Package Dimensions

SSOP8-P-0.50A





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Weight: 0.01 g (typ.)

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