



TYNx40 Series

STANDARD

40A SCRs

Table 1: Main Features

Symbol	Value	Unit
$I_{T(RMS)}$	40	A
V_{DRM}/V_{RRM}	600 to 1000	V
I_{GT}	35	mA

DESCRIPTION

The **TYNx40** series is suitable for applications where in-rush current conditions are critical, such as overvoltage crowbar protection circuits in power supplies, in-rush current limiting circuits, solid state relays (in back to back configuration), welding equipment, high power motor control circuits.

Using clip assembly technology, they provide a superior performance in high surge current capabilities.

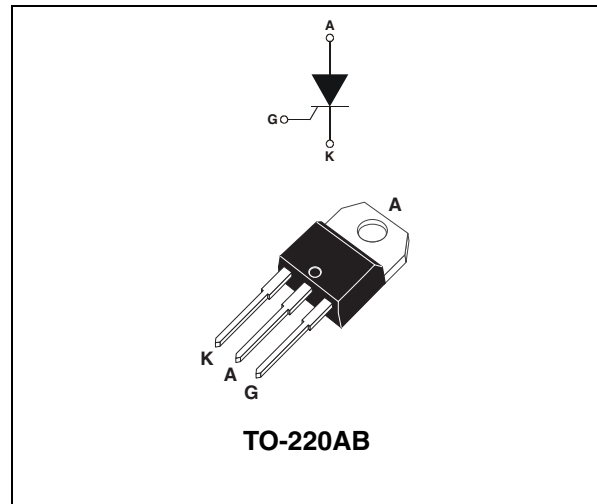


Table 2: Order Codes

Part Numbers	Marking
TYN640RG	TYN640
TYN840RG	TYN840
TYN1040RG	TYN1040

Table 3: Absolute Ratings (limiting values)

Symbol	Parameter		Value	Unit
$I_{T(RMS)}$	RMS on-state current (180° conduction angle)	$T_c = 95^\circ\text{C}$	40	A
$I_{T(AV)}$	Average on-state current (180° conduction angle)	$T_c = 95^\circ\text{C}$	25	A
I_{TSM}	Non repetitive surge peak on-state current	$t_p = 8.3 \text{ ms}$	480	A
		$t_p = 10 \text{ ms}$	460	
I^2t	I^2t Value for fusing	$t_p = 10 \text{ ms}$	1060	A^2s
di/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, $t_r \leq 100 \text{ ns}$	$F = 60 \text{ Hz}$	50	$\text{A}/\mu\text{s}$
I_{GM}	Peak gate current	$t_p = 20 \mu\text{s}$	4	A
$P_{G(AV)}$	Average gate power dissipation	$T_j = 125^\circ\text{C}$	1	W
T_{stg} T_j	Storage junction temperature range Operating junction temperature range		- 40 to + 150 - 40 to + 125	$^\circ\text{C}$
V_{RGM}	Maximum peak reverse gate voltage		5	V

TYNx40 Series

Tables 4: Electrical Characteristics ($T_j = 25^\circ\text{C}$, unless otherwise specified)

Symbol	Test Conditions			Value	Unit
I _{GT}	V _D = 12 V R _L = 33 Ω		MIN.	3.5	mA
			MAX.	35	
V _{GT}			MAX.	1.3	V
V _{GD}	V _D = V _{DRM} R _L = 3.3 kΩ	T _j = 125°C	MIN.	0.2	V
I _H	I _T = 500 mA Gate open		MAX.	75	mA
I _L	I _G = 1.2 x I _{GT}		MAX.	150	mA
dV/dt	V _D = 67 % V _{DRM} Gate open	T _j = 125°C	MIN.	1000	V/μs
V _{TM}	I _{TM} = 80 A tp = 380 μs	T _j = 25°C	MAX.	1.6	V
V _{t0}	Threshold voltage	T _j = 125°C	MAX.	0.85	V
R _d	Dynamic resistance	T _j = 125°C	MAX.	10	mΩ
I _{DRM} I _{RRM}	V _{DRM} = V _{RRM}	T _j = 25°C	MAX.	5	μA
		T _j = 125°C		4	mA

Table 5: Thermal resistance

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	Junction to case (DC)	0.8	$^\circ\text{C/W}$
$R_{th(j-a)}$	Junction to ambient (DC)	60	$^\circ\text{C/W}$

Figure 1: Maximum average power dissipation versus average on-state current

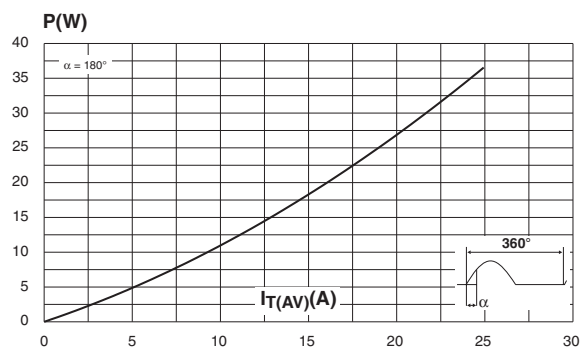


Figure 2: Average and D.C. on-state current versus case temperature

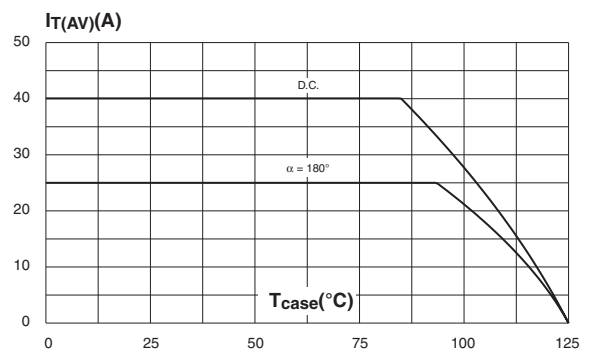


Figure 3: Relative variation of thermal impedance versus pulse duration

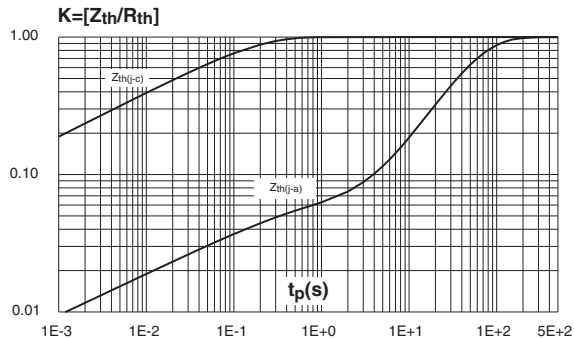


Figure 4: Relative variation of gate trigger current, holding current and latching current versus junction temperature

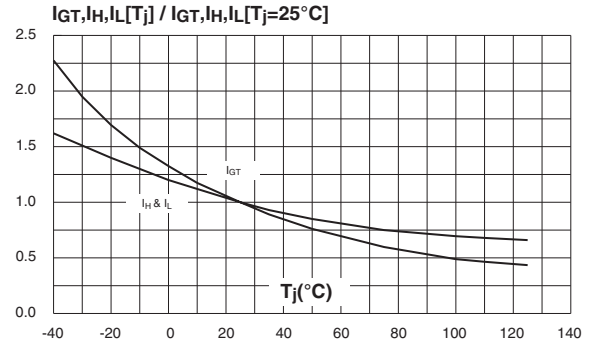


Figure 5: Surge peak on-state current versus number of cycles

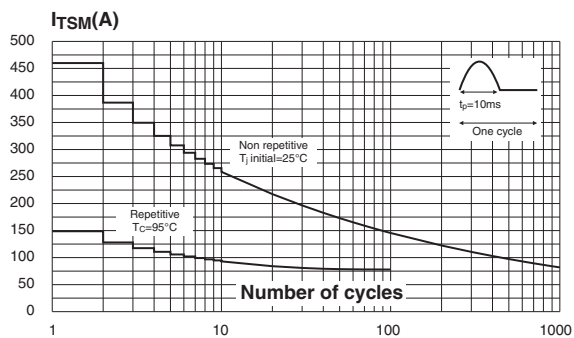


Figure 6: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp < 10 ms, and corresponding values of I²t

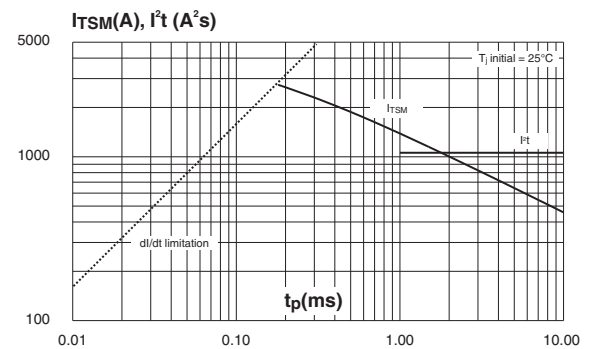
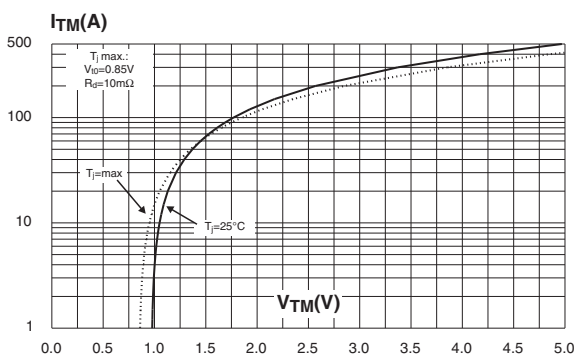


Figure 7: On-state characteristics (maximum values)



TYNx40 Series

Figure 8: Ordering Information Scheme

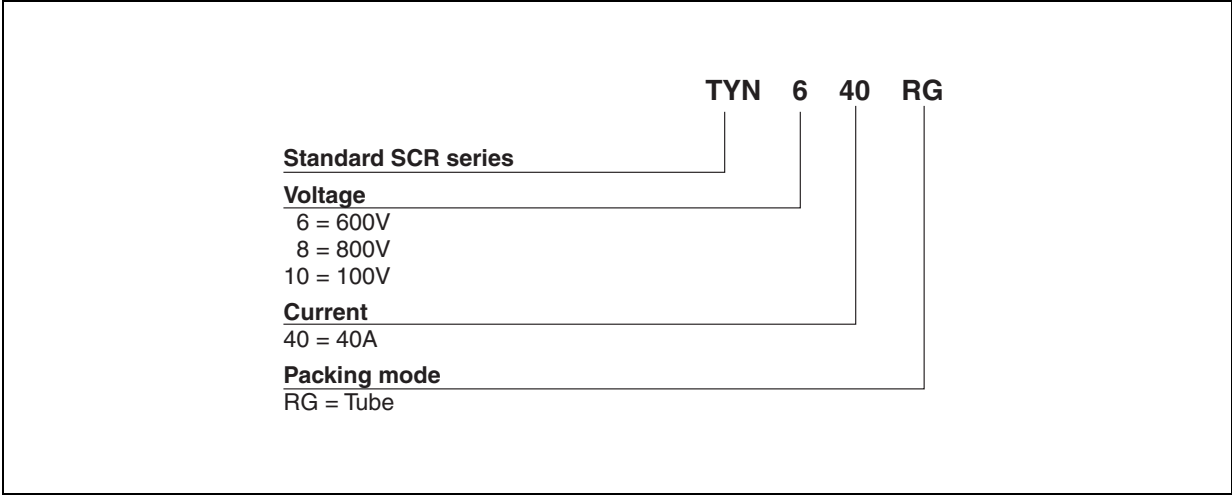
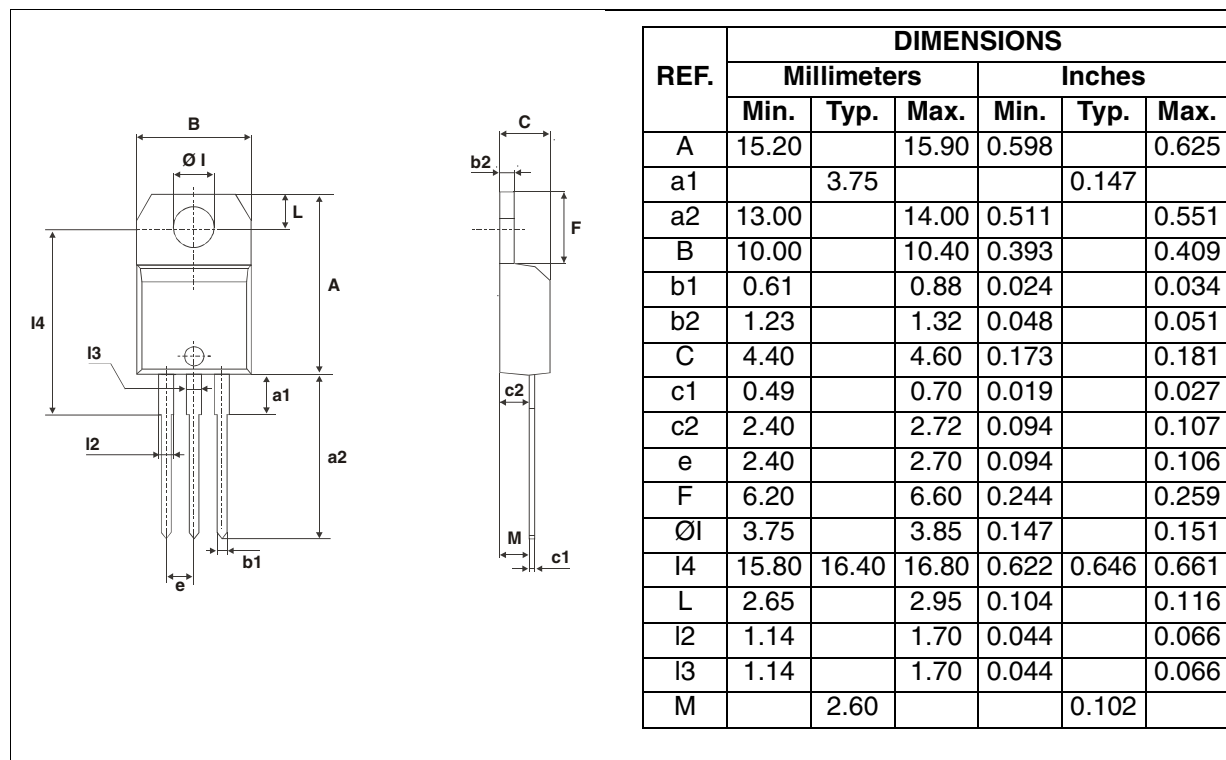


Table 6: Product Selector

Part Numbers	Voltage (xxx)			Sensitivity	Package
	600 V	800 V	1000 V		
TYNx40	X	X	X	35 mA	TO-220AB

Figure 9: TO-220AB Package Mechanical Data



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

Table 7: Ordering Information

Ordering type	Marking	Package	Weight	Base qty	Delivery mode
TYN640RG	TYN640	TO-220AB	2.3 g	50	Tube
TYN840RG	TYN840				
TYN1040RG	TYN1040				

Table 8: Revision History

Date	Revision	Description of Changes
Apr-2002	4A	Last update.
13-Feb-2006	5	TO-220AB delivery mode changed from bulk to tube. ECOPACK statement added.

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

The ST logo is a registered trademark of STMicroelectronics.
All other names are the property of their respective owners

© 2006 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan -
Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America
www.st.com