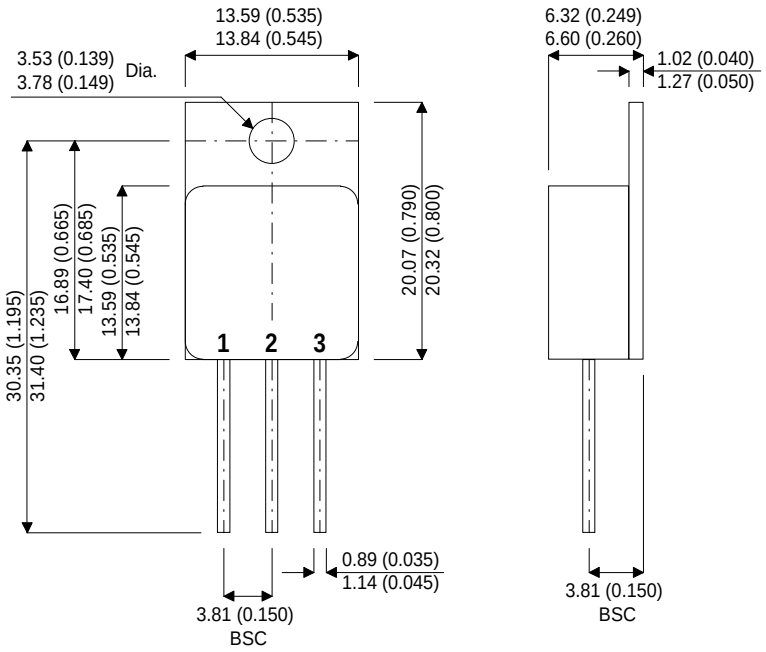
2N7224
IRFM150

MECHANICAL DATA
Dimensions in mm (inches)



TO–254AA – Package

Pin 1 – Drain Pin 2 – Source Pin 3 – Gate

N–CHANNEL POWER MOSFET

V_{DSS}	100V
$I_{D(cont)}$	34A
$R_{DS(on)}$	0.070Ω

FEATURES

- **REPETITIVE AVALANCHE RATING**
- **ISOLATED AND HERMETICALLY SEALED**
- **ALTERNATIVE TO TO-3 PACKAGE**
- **SIMPLE DRIVE REQUIREMENTS**
- **EASE OF PARALLELING**

ABSOLUTE MAXIMUM RATINGS (T_{case} = 25°C unless otherwise stated)

V_{GS}	Gate – Source Voltage		±20V
I_D	Continuous Drain Current	(V_{GS} = 10V , T_{case} = 25°C)	34A
I_D	Continuous Drain Current	(V_{GS} = 10V , T_{case} = 100°C)	21A
I_{DM}	Pulsed Drain Current [1]		136A
P_D	Power Dissipation @ T_{case} = 25°C		150W
	Linear Derating Factor		1.2W/°C
E_{AS}	Single Pulse Avalanche Energy [2]		150mJ
dv/dt	Peak Diode Recovery [3]		5.5V/ns
T_J , T_{stg}	Operating and Storage Temperature Range		–55 to 150°C
$R_{\theta JC}$	Thermal Resistance Junction to Case		0.83°C/W
$R_{\theta JCS}$	Thermal Resistance Case to Sink (Typical)		0.21°C/W
$R_{\theta JCA}$	Thermal Resistance Junction-to-Ambient		48°C/W

Notes

1) Pulse Test: Pulse Width ≤ 300μs, δ ≤ 2%

2) @ V_{DD} = 25V , L ≥ 200μH , R_G = 25Ω , Peak I_L = 34A , Starting T_J = 25°C

3) @ I_{SD} ≤ 34A , di/dt ≤ 70A/μs , V_{DD} ≤ BV_{DSS} , T_J ≤ 150°C , SUGGESTED R_G = 2.35Ω

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SEME LAB

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ELECTRICAL CHARACTERISTICS (T_{amb} = 25°C unless otherwise stated)

	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
	STATIC ELECTRICAL RATINGS					
BV_{DSS}	Drain – Source Breakdown Voltage	V_{GS} = 0 I_D = 1mA	100			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Temperature Coefficient of Breakdown Voltage	Reference to 25°C I_D = 1mA		0.13		V/°C
$R_{DS(on)}$	Static Drain – Source On–State Resistance	V_{GS} = 10V I_D = 21A			0.070	Ω
		V_{GS} = 10V I_D = 34A			0.081	
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS} I_D = 250µA	2		4	V
g_{fs}	Forward Transconductance	V_{DS} ≥ 15V I_{DS} = 21A	9			S(℧)
I_{DSS}	Zero Gate Voltage Drain Current	V_{GS} = 0 V_{DS} = 0.8BV_{DSS}			25	µA
		T_J = 125°C			250	
I_{GSS}	Forward Gate – Source Leakage	V_{GS} = 20V			100	nA
I_{GSS}	Reverse Gate – Source Leakage	V_{GS} = –20V			–100	
	DYNAMIC CHARACTERISTICS					
C_{iss}	Input Capacitance	V_{GS} = 0		3700		pF
C_{oss}	Output Capacitance	V_{DS} = 25V		1100		
C_{rss}	Reverse Transfer Capacitance	f = 1MHz		200		
Q_g	Total Gate Charge	V_{GS} = 10V I_D = 34A V_{DS} = 0.5BV_{DS}	50		125	nC
Q_{gs}	Gate – Source Charge	I_D =34A V_{DS} = 0.5BV_{DS}	8		22	nC
Q_{gd}	Gate – Drain (“Miller”) Charge		15		65	
$t_{d(on)}$	Turn–On Delay Time	V_{DD} = 50V I_D = 34A R_G = 2.35Ω			35	ns
t_r	Rise Time				190	
$t_{d(off)}$	Turn–Off Delay Time				170	
t_f	Fall Time				130	
	SOURCE – DRAIN DIODE CHARACTERISTICS					
I_S	Continuous Source Current				34	A
I_{SM}	Pulse Source Current [2]				136	
V_{SD}	Diode Forward Voltage	I_S = 34A T_J = 25°C V_{GS} = 0			1.8	V
t_{rr}	Reverse Recovery Time	I_F = 34A T_J = 25°C			500	ns
Q_{rr}	Reverse Recovery Charge	d_i / d_t ≤ 100A/µs V_{DD} ≤ 50V			2.9	µC
t_{on}	Forward Turn–On Time			Negligible		
	PACKAGE CHARACTERISTICS					
L_D	Internal Drain Inductance (from centre of drain pad to die)			8.7		nH
L_S	Internal Source Inductance (from centre of source pad to end of source bond wire)			8.7		

Notes

1) Pulse Test: Pulse Width ≤ 300µs, δ ≤ 2%

2) Repetitive Rating – Pulse width limited by maximum junction temperature.

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