

Product Summary

BV_{DSS}	Max R_{DS(on)}	Max I_D T_A = +25°C (Note 6)
100V	230mΩ @ V _{GS} = 10V	1.9A
	300mΩ @ V _{GS} = 4.5V	1.68A

Description and Applications

This MOSFET utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes it ideal for high-efficiency, low voltage, power management applications.

- DC - DC Converters
- Power Management Functions
- Disconnect Switches
- Motor Control

Features and Benefits

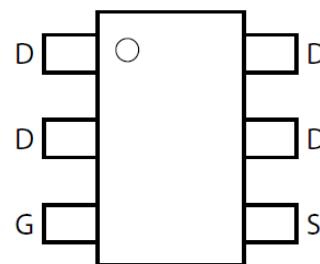
- Low On-Resistance
- Fast Switching Speed
- Low Threshold
- Low Gate Drive
- SOT26 Package
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

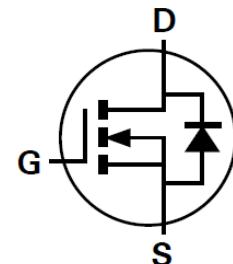
- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 **(e3)**
- Weight: 0.015 grams (Approximate)



Top View



Pinout Top-view



Device Symbol

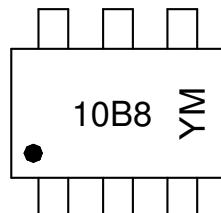
Ordering Information (Note 4)

Part Number	Reel Size (inch)	Tape Width (mm)	Quantity Per Reel
ZXMN10B08E6TA	7	8	3,000
ZXMN10B08E6TC	13	8	10,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com>.

Marking Information

SOT26



10B8 = Product Type Marking Code
 YM = Date Code Marking
 Y or \bar{Y} = Year (ex: C = 2015)
 M or \bar{M} = Month (ex: 9 = September)

Date Code Key

Year	2015	2016	2017	2018	2019	2020	2021	2022
Code	C	D	E	F	G	H	I	J

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

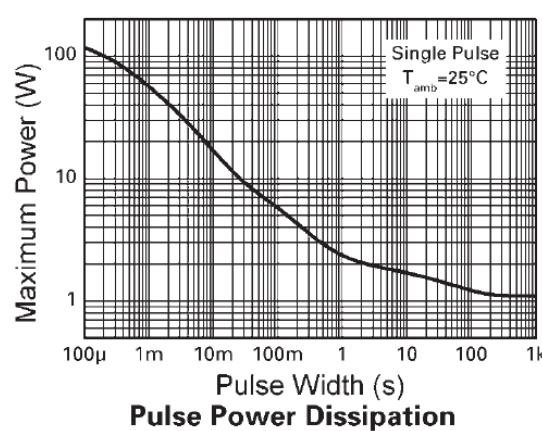
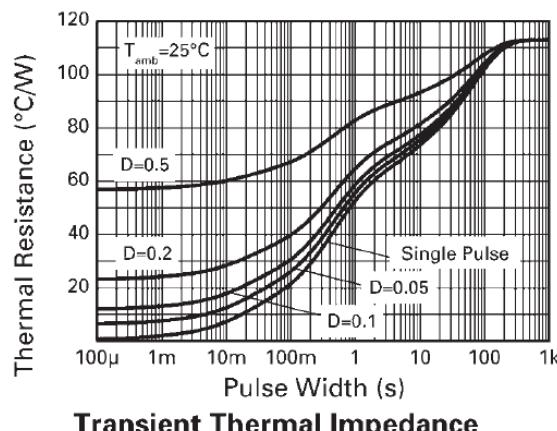
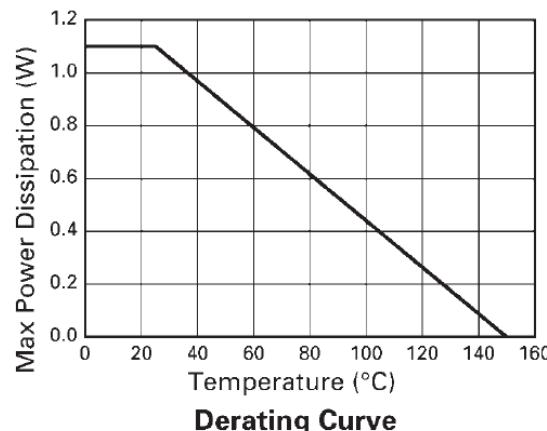
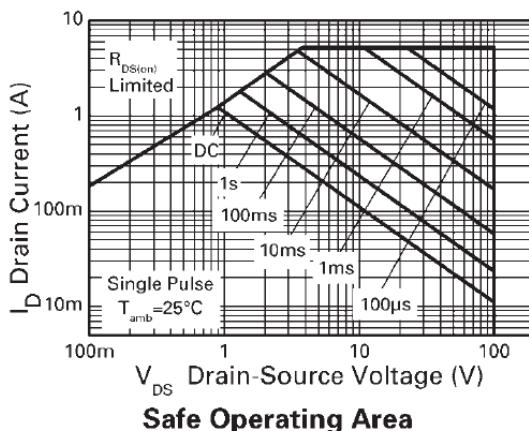
Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	100	V
Gate-Source Voltage			V_{GSS}	± 20	V
Continuous Drain Current	$V_{GS} = 10\text{V}$	(Note 6)	I_D	1.9	A
		$T_A = +70^\circ\text{C}$ (Note 6)		1.5	
		(Note 5)		1.6	
Pulsed Drain Current	(Note 7)		I_{DM}	9	A
Continuous Source Current (Body Diode)	(Note 6)		I_S	2.5	A
Pulsed Source Current (Body Diode)	(Note 7)		I_{SM}	9	A

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 5)	P_D	1.1	W
Linear Derating Factor	(Note 5)	P_D	8.8	
Power Dissipation	(Note 6)	P_D	1.7	W
Linear Derating Factor	(Note 6)	P_D	13.6	
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{\theta JA}$	113	°C/W
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\theta JA}$	73	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

- Notes:
5. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
 6. For a device surface mounted on FR4 PCB measured at $t \leq 5$ secs.
 7. Repetitive rating 25mm x 25mm FR4 PCB, $D = 0.02$, pulse width 300 μs - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.

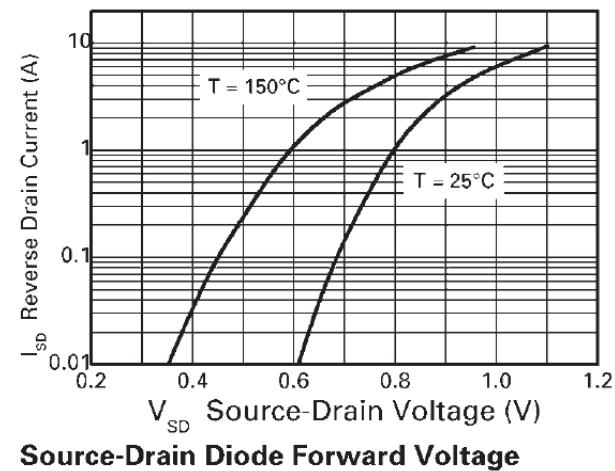
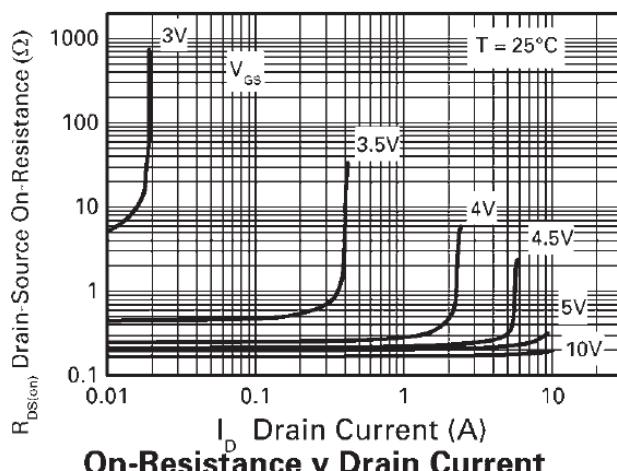
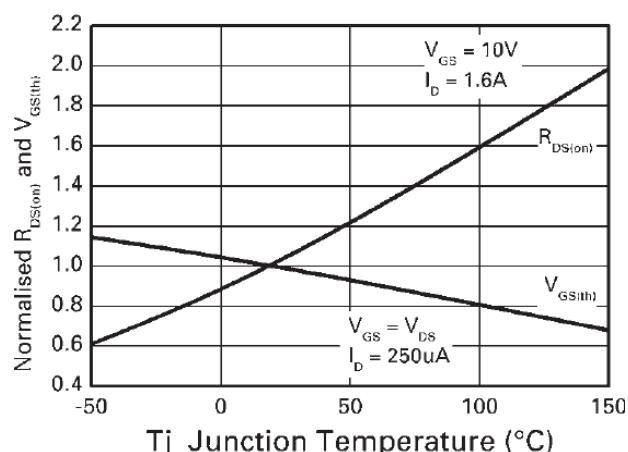
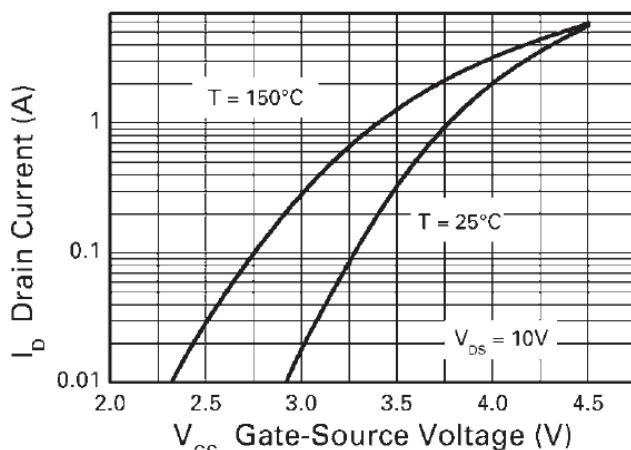
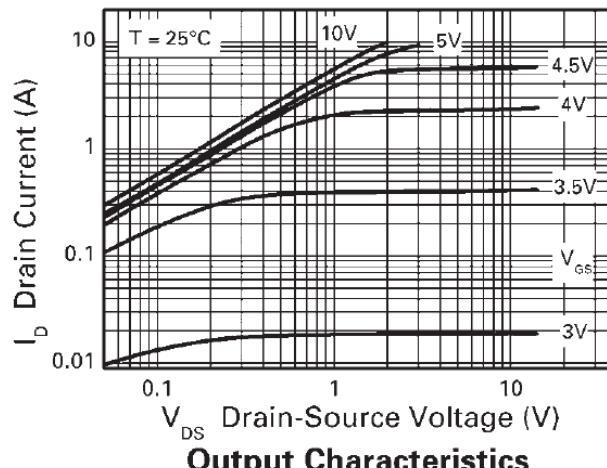
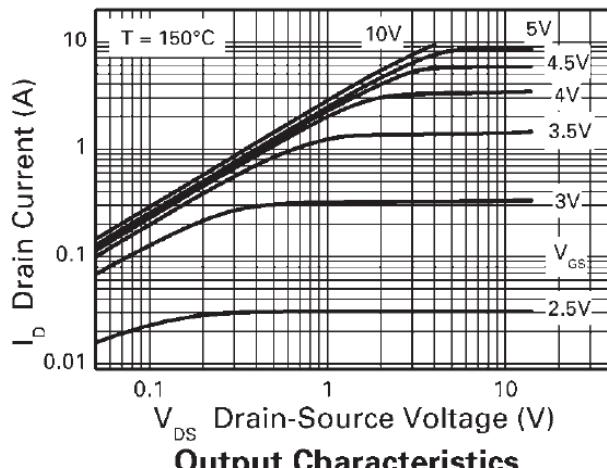
Thermal Characteristics


Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

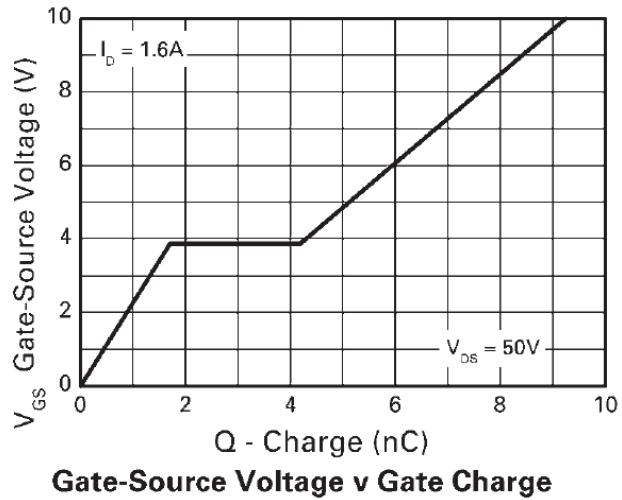
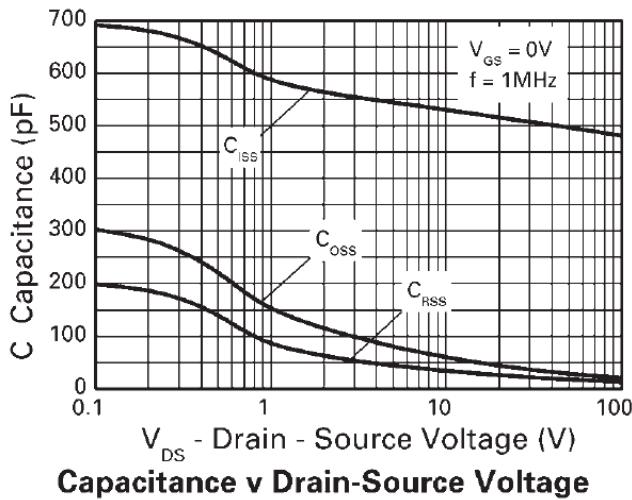
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	100	—	—	V	$I_D = 250\mu\text{A}$, $V_{\text{GS}} = 0\text{V}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	0.5	μA	$V_{\text{DS}} = 100\text{V}$, $V_{\text{GS}} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	100	nA	$V_{\text{GS}} = \pm 20\text{V}$, $V_{\text{DS}} = 0\text{V}$
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	1.0	—	3.0	V	$I_D = 250\mu\text{A}$, $V_{\text{DS}} = V_{\text{GS}}$
Static Drain-Source On-Resistance (Note 8)	$R_{\text{DS}(\text{ON})}$	—	—	0.23	Ω	$V_{\text{GS}} = 10\text{V}$, $I_D = 1.6\text{A}$
				0.30		$V_{\text{GS}} = 4.5\text{V}$, $I_D = 1.4\text{A}$
				0.50		$V_{\text{GS}} = 4.3\text{V}$, $I_D = 1.1\text{A}$
Forward Transconductance (Notes 8 & 10)	g_{fs}	—	4.8	—	S	$V_{\text{DS}} = 15\text{V}$, $I_D = 1.6\text{A}$
Diode Forward Voltage (Note 8)	V_{SD}	—	0.85	0.95	V	$T_J = +25^\circ\text{C}$, $I_S = 2.0\text{A}$, $V_{\text{GS}} = 0\text{V}$
DYNAMIC CHARACTERISTICS (Note 10)						
Input Capacitance	C_{iss}	—	497	—	pF	$V_{\text{DS}} = 50\text{V}$, $V_{\text{GS}} = 0\text{V}$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	29	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	18	—	pF	
Gate Charge (Note 9)	Q_g	—	5.0	—	nC	$V_{\text{DS}} = 50\text{V}$, $V_{\text{GS}} = 5\text{V}$, $I_D = 1.6\text{A}$
Total Gate Charge (Note 9)	Q_g	—	9.2	—	nC	$V_{\text{DS}} = 50\text{V}$, $V_{\text{GS}} = 10\text{V}$, $I_D = 1.6\text{A}$
Gate-Source Charge (Note 9)	Q_{gs}	—	1.7	—	nC	
Gate-Drain Charge (Note 9)	Q_{gd}	—	2.5	—	nC	
Turn-On Delay Time (Note 9)	$t_{\text{d}(\text{on})}$	—	2.9	—	ns	$V_{\text{DD}} = 50\text{V}$, $I_D = 1.0\text{A}$, $R_G \geq 6.0\Omega$, $V_{\text{GS}} = 10\text{V}$
Turn-On Rise Time (Note 9)	t_r	—	2.1	—	ns	
Turn-Off Delay Time (Note 9)	$t_{\text{d}(\text{off})}$	—	12.1	—	ns	
Turn-Off Fall Time (Note 9)	t_f	—	5.0	—	ns	
Reverse Recovery Time	t_{rr}	—	32	—	ns	$T_J = +25^\circ\text{C}$, $I_F = 1.7\text{A}$, $di/dt = 100\text{A}/\mu\text{s}$
Reverse Recovery Charge	Q_{rr}	—	40	—	nC	

- Notes:
- 8. Measured under pulsed conditions. Width $\leq 300\mu\text{s}$. Duty cycle $\leq 2\%$.
 - 9. Switching characteristics are independent of operating junction temperature.
 - 10. For design aid only, not subject to production testing.

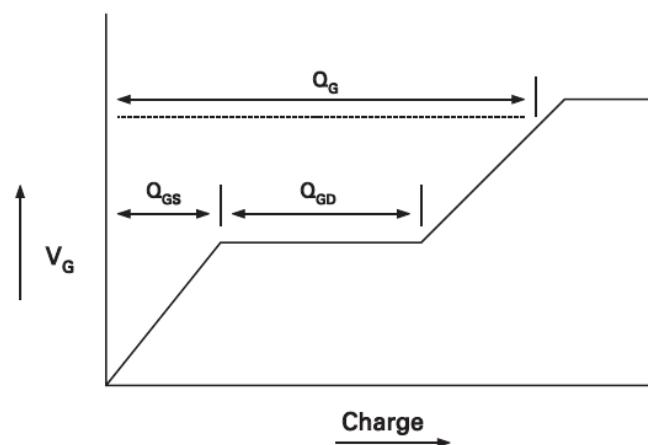
Typical Characteristics



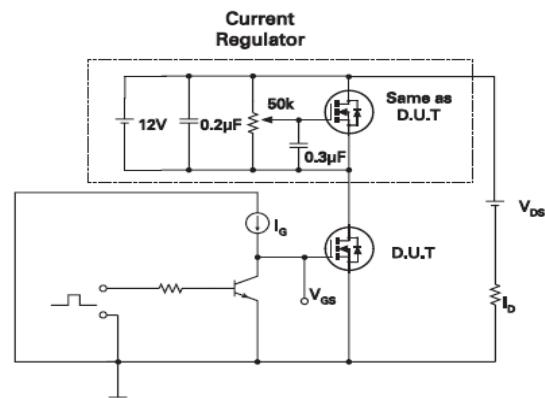
Typical Characteristics (continued)



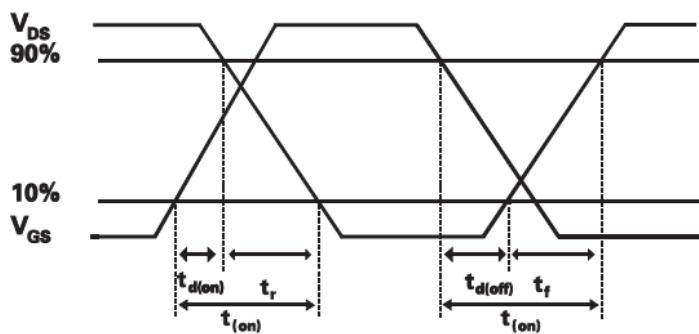
Test Circuits



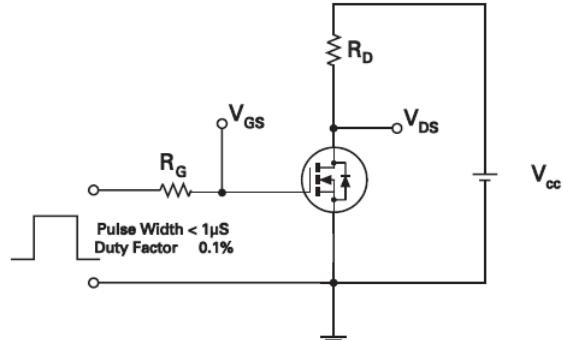
Basic Gate Charge Waveform



Gate Charge Test Circuit



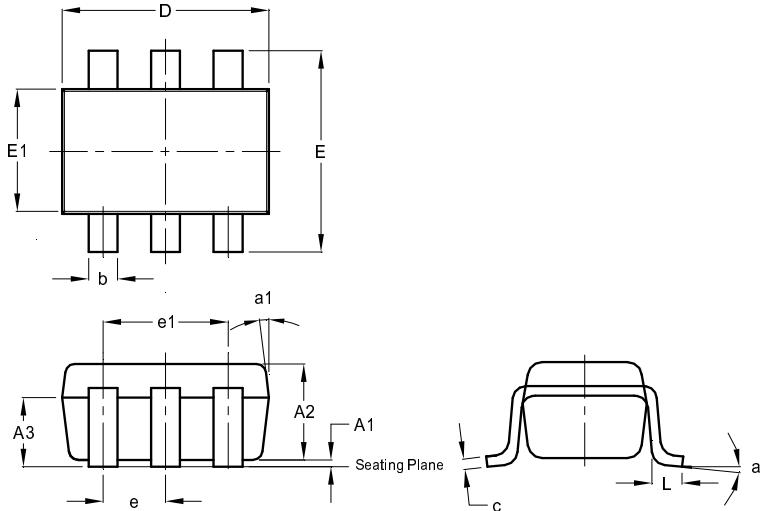
Switching Time Waveforms



Switching Time Test Circuit

Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.

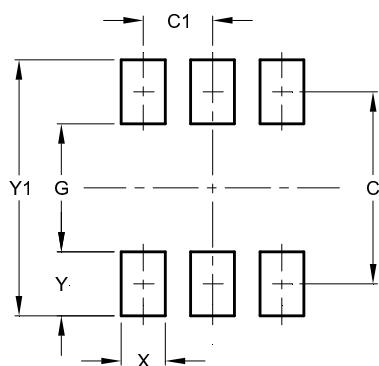


SOT26			
Dim	Min	Max	Typ
A1	0.013	0.10	0.05
A2	1.00	1.30	1.10
A3	0.70	0.80	0.75
b	0.35	0.50	0.38
c	0.10	0.20	0.15
D	2.90	3.10	3.00
e	-	-	0.95
e1	-	-	1.90
E	2.70	3.00	2.80
E1	1.50	1.70	1.60
L	0.35	0.55	0.40
a	-	-	8°
a1	-	-	7°

All Dimensions in mm

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	2.40
C1	0.95
G	1.60
X	0.55
Y	0.80
Y1	3.20

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