OCTAL BUS TRANSCEIVER AND REGIST WITH 3-STATE OUTPUTS

SCAS302 - JANUARY 1993 - REVISED MARCH 1994

13 B8

 EPIC™ (Enhanced-Performance Implanted CMOS) Submicron Process 	DB, DW, OR PW PACKAGE (TOP VIEW)				
 Typical V_{OLP} (Output Ground Bounce) 0.8 V at V_{CC} = 3.3 V, T_A = 25°C 	CLKAB 1 24 V _{CC} SAB 2 23 CLKBA				
 Typical V_{OHV} (Output V_{OH} Undershoot) 2 V at V_{CC} = 3.3 V, T_A = 25°C 	DIR [3 22] SBA A1 [4 21] OE				
 Package Options Include Plastic Small-Outline (DW), Shrink Small-Outline 	A2 5 20 B1 A3 6 19 B2				
(DB), and Thin Shrink Small-Outline (PW) Packages	A4 [] 7 18] B3 A5 [] 8 17 [] B4				
description	A6 9 16 B5 A7 10 15 B6				
This octal bus transceiver and register is designed	A8 🖸 11 14 🗓 B7				

The SN74LVC646 consists of bus transceiver circuits, D-type flip-flops, and control circuitry arranged for multiplexed transmission of data directly from the input bus or from the internal registers. Data on the A or B bus is clocked into the registers on the low-to-high transition of the appropriate clock (CLKAB or CLKBA) input. Figure 1 illustrates the four fundamental bus-management functions that can be performed with the SN74LVC646.

Output-enable (OE) and direction-control (DIR) inputs are provided to control the transceiver functions. In the transceiver mode, data present at the high-impedance port may be stored in either register or in both.

The select-control (SAB and SBA) inputs can multiplex stored and real-time (transparent mode) data. The direction control (DIR) determines which bus receives data when \overline{OE} is low. In the isolation mode (\overline{OE} high), A data may be stored in one register and/or B data may be stored in the other register.

When an output function is disabled, the input function is still enabled and may be used to store and transmit data. Only one of the two buses, A or B, may be driven at a time.

The SN74LVC646 is characterized for operation from -40°C to 85°C.

FUNCTION TABLE

INPUTS					DATA	A I/Os	000000000000000000000000000000000000000		
OE	DIR	CLKAB	CLKBA	SAB	SBA	A1 THRU A8	B1 THRU B8	OPERATION OR FUNCTION	
Х	Х	1	Х	Х	Х	Input	Unspecified [†]	Store A, B unspecified†	
Х	Х	Х	1	Х	Х	Unspecified†	Input	Store B, A unspecified†	
Н	Х	1	1	Х	Х	Input	Input	Store A and B data	
Н	Х	H or L	H or L	X	X	Input disabled	Input disabled	Isolation, hold storage	
L	L	Х	Х	Х	L	Output	Input	Real-time B data to A bus	
L	L	Х	H or L	Х	Н	Output	Input	Stored B data to A bus	
L	Н	X	Х	L	Х	Input	Output	Real-time A data to B bus	
L	Н	H or L	X	Н	Х	Input	Output	Stored A data to B bus	

The data output functions may be enabled or disabled by various signals at the OE and DIR inputs. Data input functions are always enabled; i.e., data at the bus pins will be stored on every low-to-high transition of the clock inputs.

EPIC is a trademark of Texas Instruments Incorporated

for 2.7-V to 3.6-V V_{CC} operation.

PRODUCT PREVIEW Information concerns products in the formative or deelign phase of development. Characteristic data and other specifications are design goals. Taxas instruments reserves the right to change or discontinue these products without notice.

8961723 0100531 906



Copyright @ 1994, Texas Instruments Incorporated



7-123

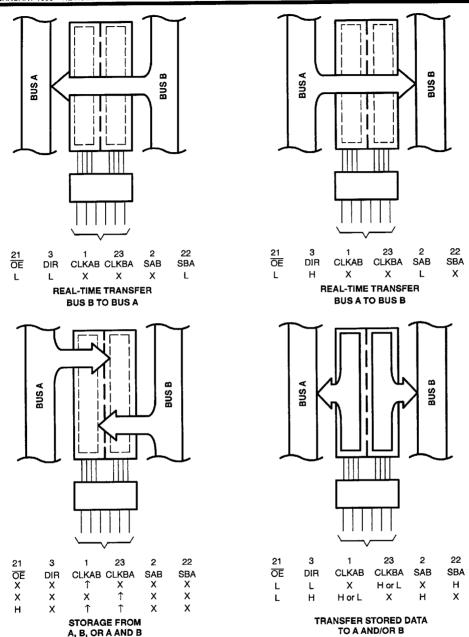


Figure 1. Bus-Management Functions

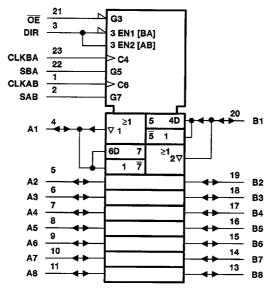
8961723 0100532 842



7-124

POST OFFICE BOX 655303 ● DALLAS, TEXAS 76265

logic symbol†



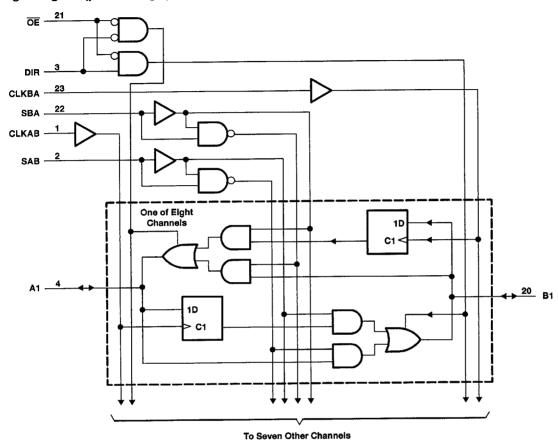
[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

8961723 0100533 789



7-125

logic diagram (positive logic)



8961723 0100534 615



7-126

PRODUCT PREVIEW

POST OFFICE BOX 655303 ● DALLAS, TEXAS 75265

SN74LVC646 OCTAL BUS TRANSCEIVER AND REGISTER WITH 3-STATE OUTPUTS

SCAS302 - JANUARY 1993 - REVISED MARCH 1994

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, V _{CC} –0.5	5 V to 4.6 V
Input voltage range, V _I (except I/O ports)	5 V to 4.6 V
Input voltage range, V _I (I/O ports) (see Note 1)	/cc + 0.5 V
Output voltage range, VO (see Note 1)	CC + 0.5 V
Input clamp current, $I_{ K }(V_{ } < 0)$	50 mA
Output clamp current, I _{OK} (V _O < 0 or V _O > V _{CC})	. ±50 mA
Continuous output current, I _O (V _O = 0 to V _{CC})	. ±50 mA
Continuous current through V _{CC} or GND	±100 mA
Maximum power dissipation at T _A = 55°C (in still air): DB package	0.65 W
DW package	
PW package	
Storage temperature range –65°	

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: This value is limited to 4.6 V maximum.

recommended operating conditions (see Note 2)

			MIN	MAX	UNIT	
Vcc	Supply voltage		2.7	3.6	٧	
VIH	High-level input voltage	V _{CC} = 2.7 V to 3.6 V	2		٧	
V_{IL}	Low-level input voltage	V _{CC} = 2.7 V to 3.6 V		0.8	٧	
٧	Input voltage		0	Vcc	V	
٧o	Output voltage		0	Vcc	٧	
ЮН	High-level output current	V _{CC} = 2.7 V		-12	A	
		V _{CC} = 3 V		-24	mA	
lOL	Low-level output current	V _{CC} = 2.7 V		12	4	
		V _{CC} = 3 V		24	mA	
Δt/Δν	Input transition rise or fall rate		0	10	ns/V	
TA	Operating free-air temperature		-40	85	°C	

NOTE 2: Unused or floating pins (input or I/O) must be held high or low.





SN74LVC646 OCTAL BUS TRANSCEIVER AND REGISTER WITH 3-STATE OUTPUTS

SCAS302 - JANUARY 1993 - REVISED MARCH 1994

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER				T _A = -40°0	UNUT	
		TEST CONDITIONS	v _{cc} †	MIN	MAX	UNIT
		I _{OH} = -100 μA	MIN to MAX	V _{CC} -0.2		
Voн			2.7 V	2.2		٧
		I _{OH} = - 12 mA	3 V	2.4		
		I _{OH} = -24 mA	3 V	2		
VOL		I _{OL} = 100 μA	MIN to MAX		0.2	
		I _{OL} = 12 mA	2.7 V		0.4	٧
) L		I _{OL} = 24 mA	3 V		0.55	
lı .		V _I = V _{CC} or GND	3.6 V		±5	μA
loz‡		VO = VCC or GND	3.6 V		±10	μΑ
loc		V _I = V _{CC} or GND, I _O = 0	3.6 V		20	μA
ΔICC		V _{CC} = 3 V to 3.6 V, One input at V _{CC} - 0.6 V, Other inputs at V _{CC} or GND			500	μА
Ci	Control inputs	V _I = V _{CC} or GND	3.3 V			pF
Cio	A or B ports	VO = VCC or GND	3.3 V			pF

[†] For conditions shown as MIN or MAX, use the appropriate values under recommended operating conditions.

8961723 0100536 498



7-128

POST OFFICE BOX 655303 ● DALLAS, TEXAS 75265

[‡] For I/O ports, the parameter IOZ includes the input leakage current.