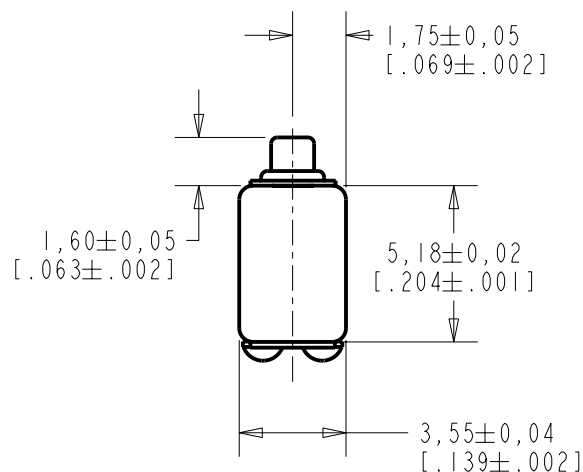
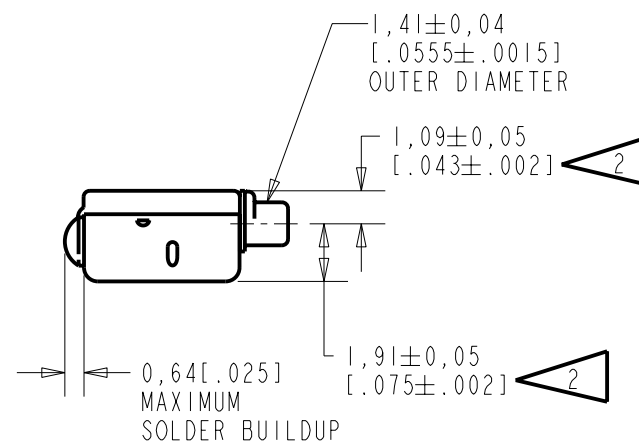
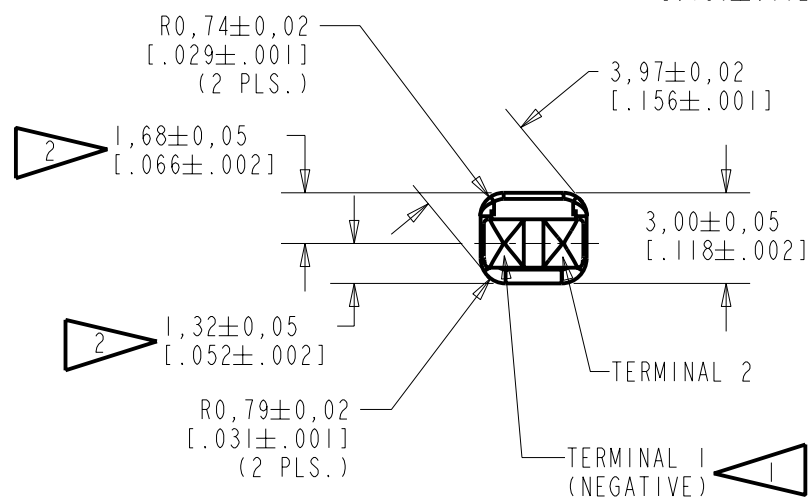


SHT 1.1



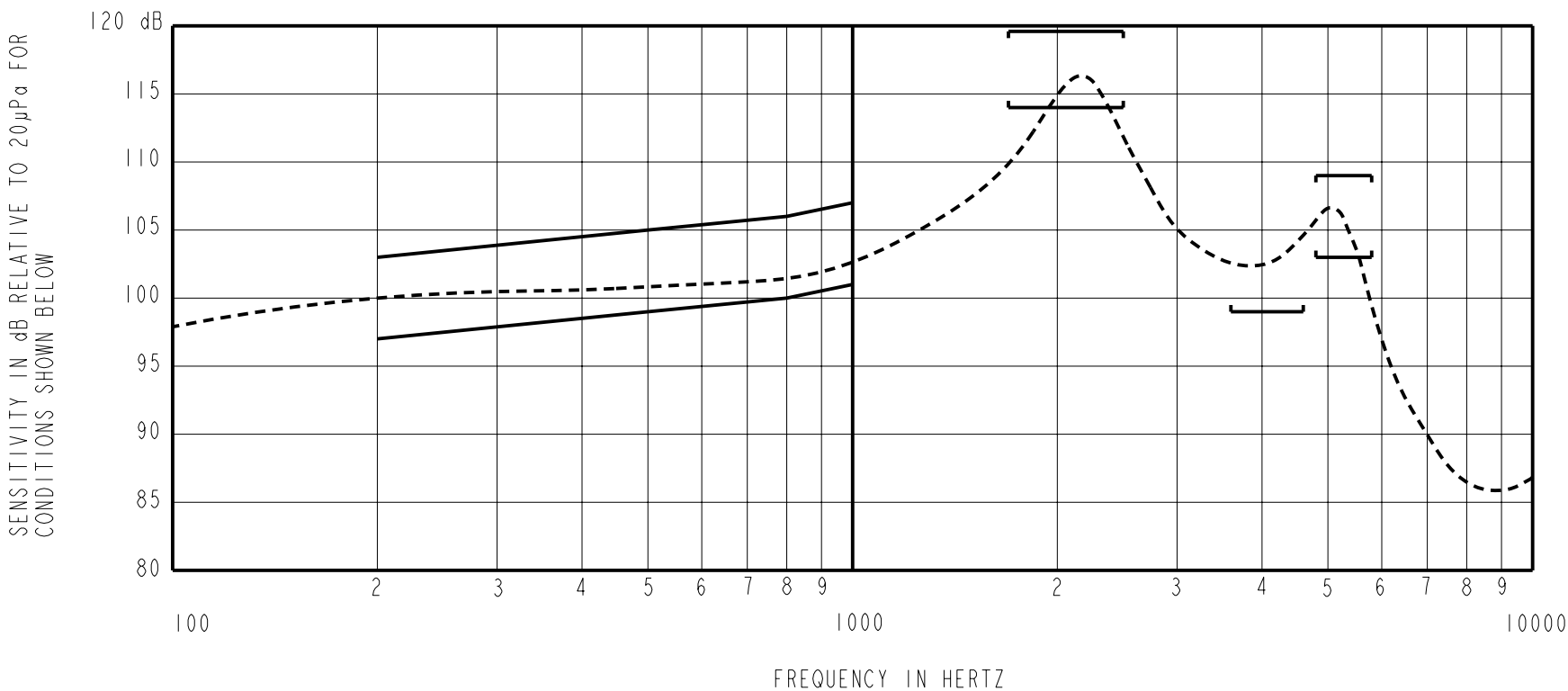
1 A POSITIVE GOING VOLTAGE AT TERMINAL 2, RELATIVE TO TERMINAL 1, CAUSES A DECREASE IN PRESSURE AT THE SOUND OUTLET.

2 LOCATED FROM TWO SURFACES FOR CUSTOMER CONVENIENCE. ONLY APPLICABLE FROM ONE SURFACE, NOT TO BE USED TOGETHER.



KNOWLES ELECTRONICS
ITASCA, ILLINOIS U.S.A.

Revision	C.O. #	Implementation Date	RELEASE LEVEL	REVISION
			Released	A
A	C10103264	11-3-05		
SCALE: 4:1				
DO NOT SCALE DRAWING			DR. BY	DATE
TITLE: RECEIVER OUTLINE DRAWING			CRG	11-3-05
			CK. BY	DATE
			GJP	11-8-05
			APP. BY	DATE
			GJP	11-8-05
			SHT 1.1	



NOTES:

1. MEASUREMENTS MADE USING 10mm (.394") X 1mm (.039") ID TUBE CONNECTED TO A SIMULATED ANSI S3.3-1960 TYPE HA-3 COUPLER. (T3420 AND B & K DB0138).
2.

FREQUENCY	MIN.	MAX.
200	97.0	103.0
500	99.0	105.0
800	100.0	106.0
1000	101.0	107.0
1700-2500	114.0	120.0
3600-4600	99.0	---
4800-5800	103.0	109.0
3. RESPONSE, IMPEDANCE, AND DISTORTION MEASUREMENTS MADE USING THE ELECTRICAL TEST CONDITIONS SHOWN BELOW.
4. ELECTRICAL SOURCE IMPEDANCE MUST BE GREATER THAN 20 TIMES 1KHz IMPEDANCE FOR TEST CONDITIONS SHOWN BELOW.
5. INDIVIDUAL SPECIFICATIONS.

PORT LOCATION	IMPEDANCE OHMS ±15%		DCR @20°C OHMS ±10%	DISTORTION		ELECTRICAL TEST CONDITIONS	
	1KHz	500Hz		MAX. %	FREQ Hz	AC mA RMS	DC mA
12C	550	425	354	10	700	1.02	0.00

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