Unit in mm

11-7A8

TOSHIBA Photocoupler Photo Relay

# TLP599G

**Telecommunication Data Acquisition** Measurement Instrumentation

The TOSHIBA TLP599G consists of a gallium arsenide infrared emitting diode optically coupled to a photo-MOS FET in a six lead plastic DIP package (DIP6).

The TLP599G is a bi-directional switch which can replace mechanical relays in many applications.

- Peak off-state voltage: 400V (min.)
- On-state current: 120mA (max.) (A connection)
- On-state resistance: 30Q (max.) (A connection)
- Insulation thickness: 0.4mm (max.)
- Isolation voltage: 2500Vrms (min.)

UL recognized: UL1577, file no. E67349									
Trigger LED current (Ta = 25°C)									
Classification	(m		Marking Of						
(Note 1)	ان النظام النظا	120mA Max.	Classification						
(IFT2)		2	T2						
(11 12)									
Standard	_	5	T2, blank						

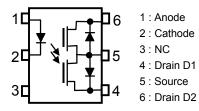
(Note 1): Application type name for certification test, please use standard product type name, i.e.

TLP599G (IFT2):TLP599G

# 7.12 ± 0.25 7.62 ± 0.25

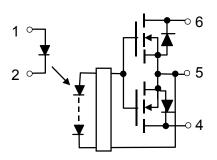
#### Pin Configuration (top view)

11-7A8



#### **Schematic**

TOSHIBA



#### **Maximum Ratings (Ta = 25°C)**

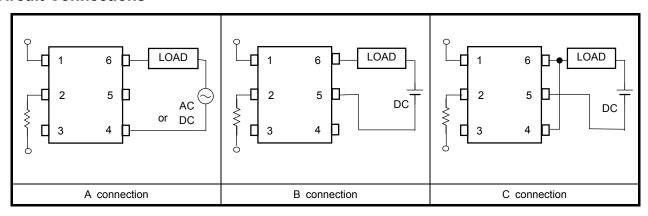
	Characteristic		Symbol	Rating	Unit
	Forward current	l <sub>F</sub>	50	mA	
Forward current derating (Ta ≥ 25°C)			ΔI <sub>F</sub> / °C	-0.5	mA / °C
ГE	Peak forward current (100 µs pulse, 100 p	ops)	I <sub>FP</sub>	1	Α
	Reverse voltage		V <sub>R</sub>	5	V
	Forward current derating (Ta ≥ 25°C)  Peak forward current (100 µs pulse, 100 pps)		Tj	125	°C
	Off-state output terminal voltage	V <sub>OFF</sub>	400	V	
		A connection		120	
		B connection	lon	150	mA
ctor		C connection		200	
Dete		A connection		-1.2	mA / °C
	On–state current derating (Ta ≥ 25°C)	B connection	ΔI <sub>ON</sub> / °C	-1.5	
		C connection		-2.0	
	Junction temperature	·	Tj	125	°C
Stora	Storage temperature range			-55~125	°C
Oper	Operating temperature range		T <sub>opr</sub>	<b>−40~85</b>	°C
Lead	soldering temperature (10 s)		T <sub>sol</sub>	260	°C
Isolat	ion voltage (AC, 1 min., R.H.≤ 60%)	(Note 2)	BVS	2500	Vrms

(Note 2): Device considered a two–terminal device: Pins 1, 2 and 3 shorted together, and pins 4, 5 and 6 shorted together.

## **Recommended Operating Conditions**

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	$V_{DD}$	_	_	320	V
Forward current	lF	7.5	15	25	mA
On-state current	I <sub>ON</sub>	_	_	120	mA
Operating temperature	T <sub>opr</sub>	-20	_	80	°C

#### **Circuit Connections**



## Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I <sub>R</sub>	$V_R = 5V$	_	_	10	μΑ
	Capacitance	C <sub>T</sub>	V = 0, f = 1 MHz		30		pF
Detector	Off-state current	l <sub>OFF</sub>	V <sub>OFF</sub> = 400 V			1	μΑ
Dete	Capacitance	C <sub>OFF</sub>	V = 0, f = 1 MHz				pF

## **Coupled Electrical Characteristics (Ta = 25°C)**

Char	racteristic	Symbol	Test Condition	Min.	Typ/	Max.	Unit
Trigger LED curr	ent	I <sub>FT</sub>	I <sub>ON</sub> = 120 mA	_	1	5	mA
	A connection		I <sub>ON</sub> = 120 mA, I <sub>F</sub> = 10 mA	_	20	30	
On–state Resistance	B connection	R <sub>ON</sub>	I <sub>ON</sub> = 150 mA, I <sub>F</sub> = 10 mA	_	12	20	Ω
rtoolotarioo	C connection		$I_{ON} = 200 \text{ mA}, I_F = 10 \text{ mA}$	-	6	10	

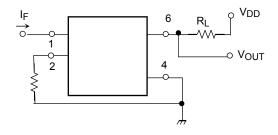
## Isolation Characteristics (Ta = 25°C)

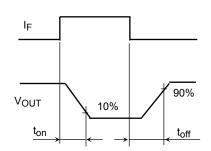
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Capacitance input to output	CS	V <sub>S</sub> = 0, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H.≤ 60%	5 × 10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage		AC, 1 minute	2500	_	_	Vrms
	$BV_S$	AC, 1 second (in oil)	_	5000	_	VIIIIS
		DC, 1 minute (in oil)	_	5000	_	V <sub>dc</sub>

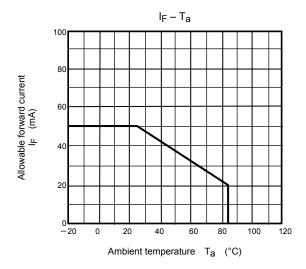
## **Switching Characteristics (Ta = 25°C)**

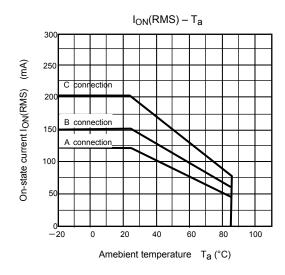
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Turn-on time	t <sub>on</sub>	$R_L = 200\Omega$	_	_	2	ms
Turn-off time	t <sub>off</sub>	$V_{DD} = 20 \text{ V, I}_{F} = 10 \text{ mA}$	_	_	2	1115

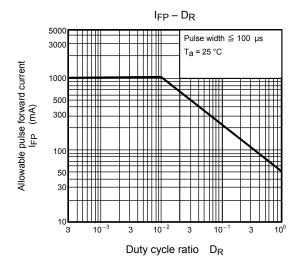
## **Switching Time Test Circuit**

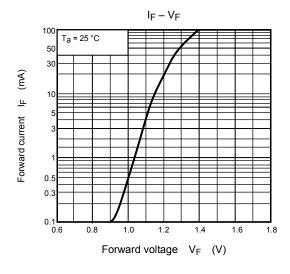


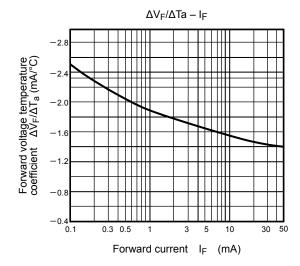


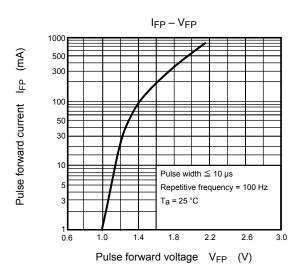




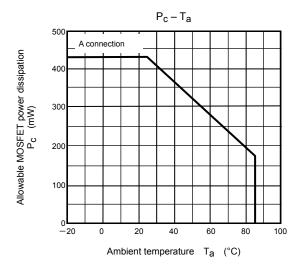


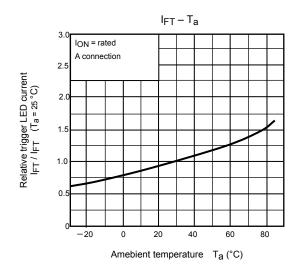


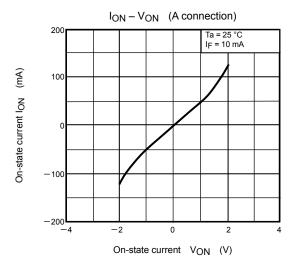


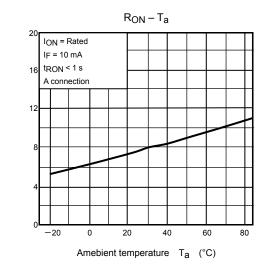


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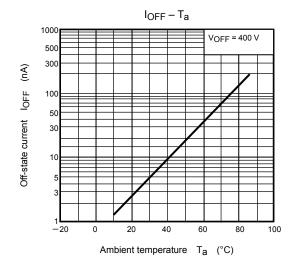


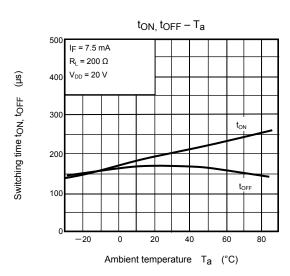


<u>Q</u>

On-state resistance RoN(RMS)

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20070701-EN GENERAL

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