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FDP2710

N-Channel PowerTrench® MOSFET

250 V, 50 A, 42.5 mΩ

Features

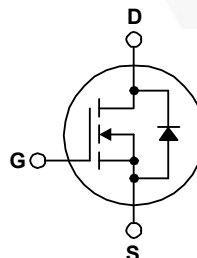
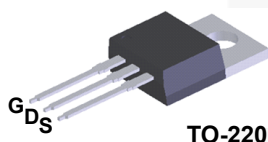
- $R_{DS(on)} = 36.3 \text{ m}\Omega$ (Typ.) @ $V_{GS} = 10 \text{ V}$, $I_D = 25 \text{ A}$
- Fast Switching Speed
- Low Gate Charge
- High Performance Trench technology for Extremely Low $R_{DS(on)}$
- High Power and Current Handling Capability
- RoHS Compliant

General Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench® process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Applications

- Consumer Appliances
- Synchronous Rectification



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	FDP2710	Unit
V_{DS}	Drain-Source Voltage	250	V
V_{GS}	Gate-Source voltage	± 30	V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$) - Continuous ($T_C = 100^\circ\text{C}$)	50 31.3	A A
I_{DM}	Drain Current - Pulsed (Note 1)	See Figure 9	A
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	145	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	4.5	V/ns
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$) - Derate above 25°C	260 2.1	W W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	FDP2710	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.48	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	$^\circ\text{C/W}$

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDP2710	FDP2710	TO-220	Tube	N/A	50 units

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA, T _J = 25°C	250	--	--	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	--	0.25	--	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 250V, V _{GS} = 0V V _{DS} = 250V, V _{GS} = 0V, T _C = 125°C	-- --	-- --	10 500	μA μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30V, V _{DS} = 0V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30V, V _{DS} = 0V	--	--	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	3.0	4.0	5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10V, I _D = 25A	--	36.3	42.5	mΩ
g _{FS}	Forward Transconductance	V _{DS} = 10V, I _D = 25A	--	63	--	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	--	5470	7280	pF
C _{oss}	Output Capacitance		--	426	570	pF
C _{rss}	Reverse Transfer Capacitance		--	97	146	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 125V, I _D = 50A V _{GS} = 10V, R _{GEN} = 25Ω (Note 4)	--	80	170	ns
t _r	Turn-On Rise Time		--	252	515	ns
t _{d(off)}	Turn-Off Delay Time		--	112	235	ns
t _f	Turn-Off Fall Time		--	154	320	ns
Q _g	Total Gate Charge	V _{DS} = 125V, I _D = 50A V _{GS} = 10V (Note 4)	--	78	101	nC
Q _{gs}	Gate-Source Charge		--	34	--	nC
Q _{gd}	Gate-Drain Charge		--	18	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	50	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	150	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0V, I _S = 50A	--	--	1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _S = 50A	--	163	--	ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt =100A/μs	--	1.3	--	μC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $L = 1\text{mH}$, $I_{AS} = 17A$, $V_{DD} = 50V$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq 50A$, $di/dt \leq 100A/\mu s$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$
4. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

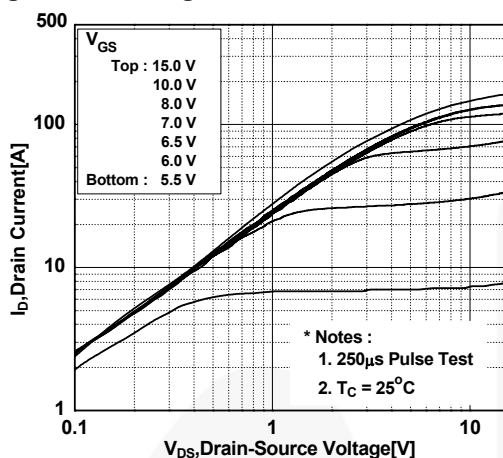


Figure 2. Transfer Characteristics

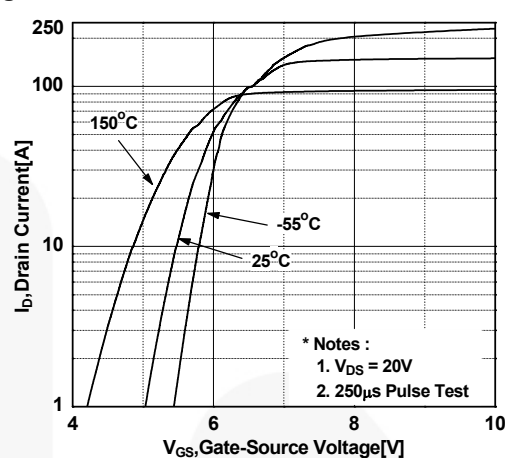


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

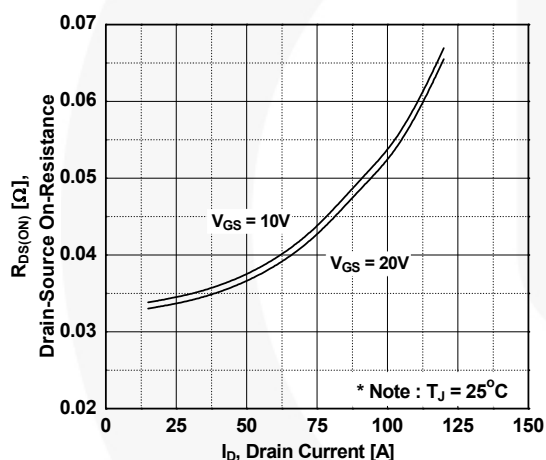


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

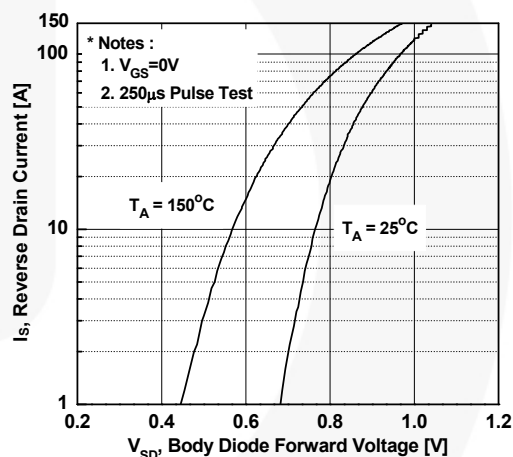


Figure 5. Capacitance Characteristics

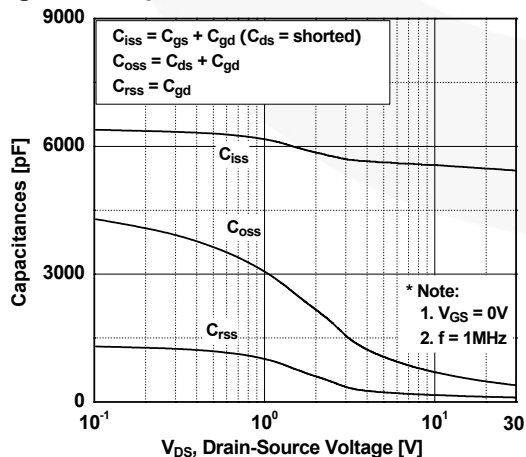
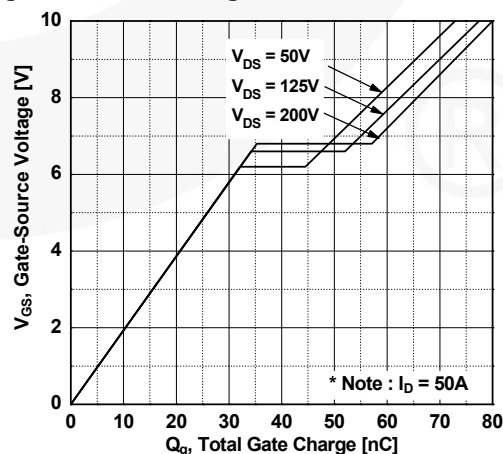


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

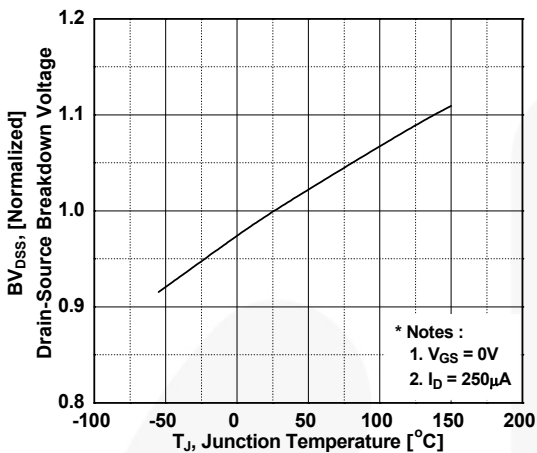


Figure 8. On-Resistance Variation vs. Temperature

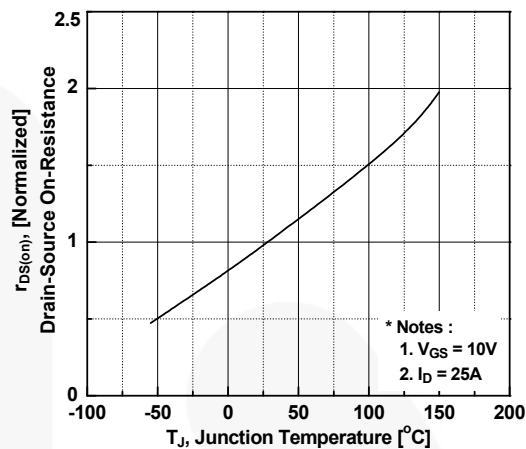


Figure 9. Maximum Safe Operating Area

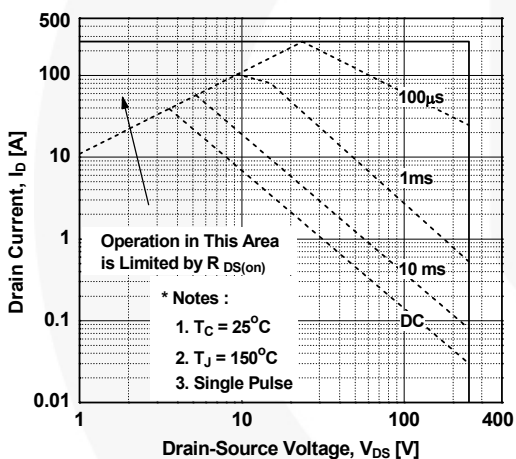


Figure 10. Maximum Drain Current vs. Case Temperature

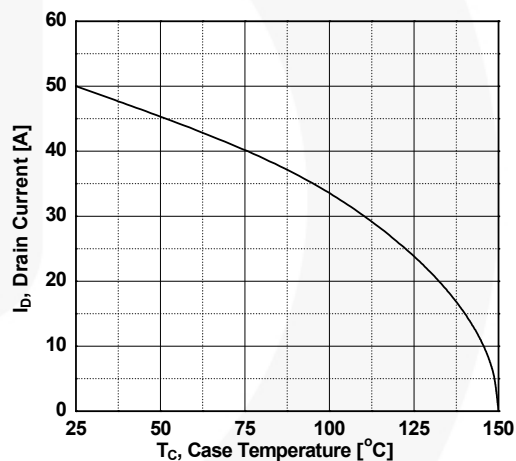


Figure 11. Transient Thermal Response Curve

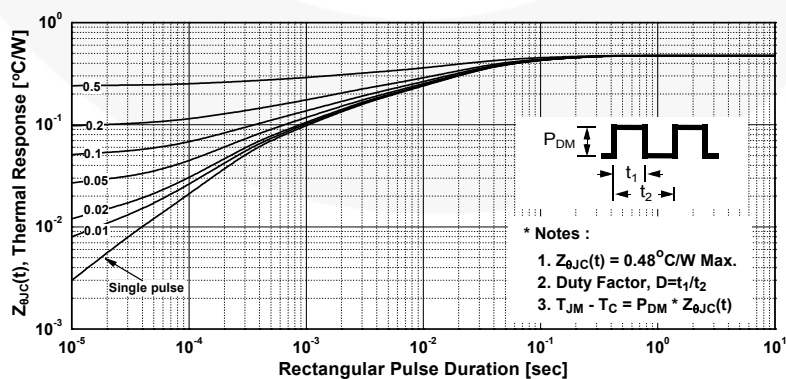


Figure 12. Gate Charge Test Circuit & Waveform

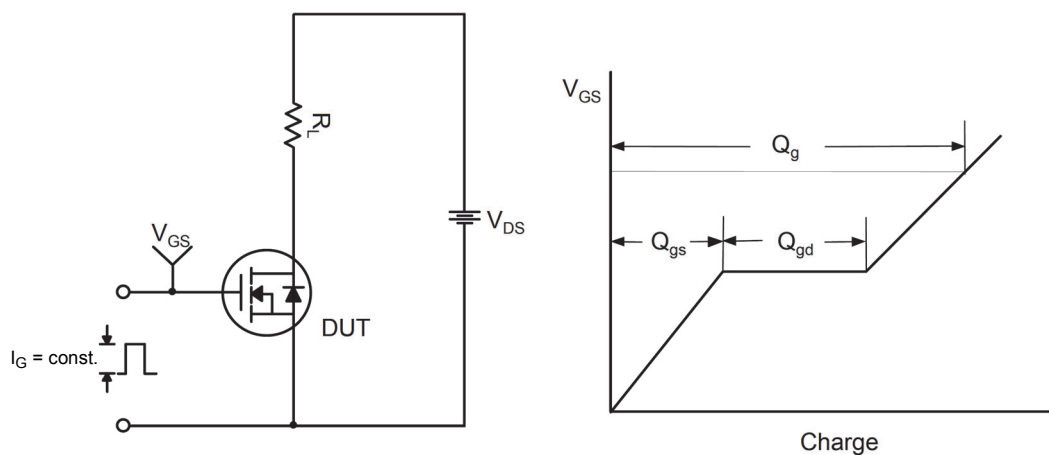


Figure 13. Resistive Switching Test Circuit & Waveforms

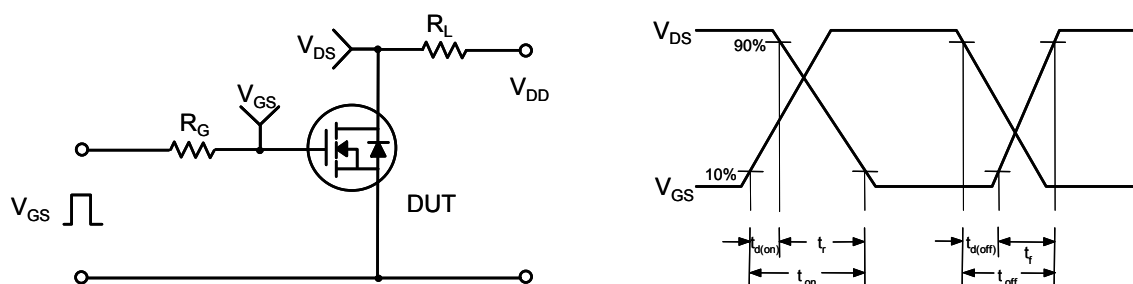


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

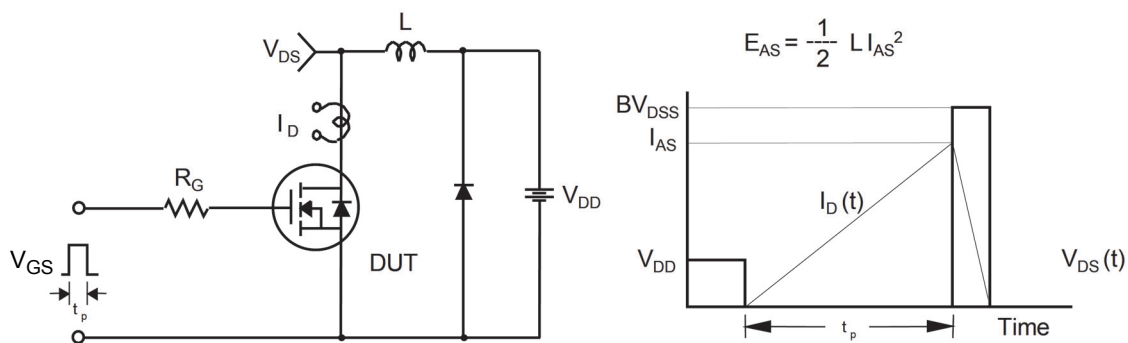
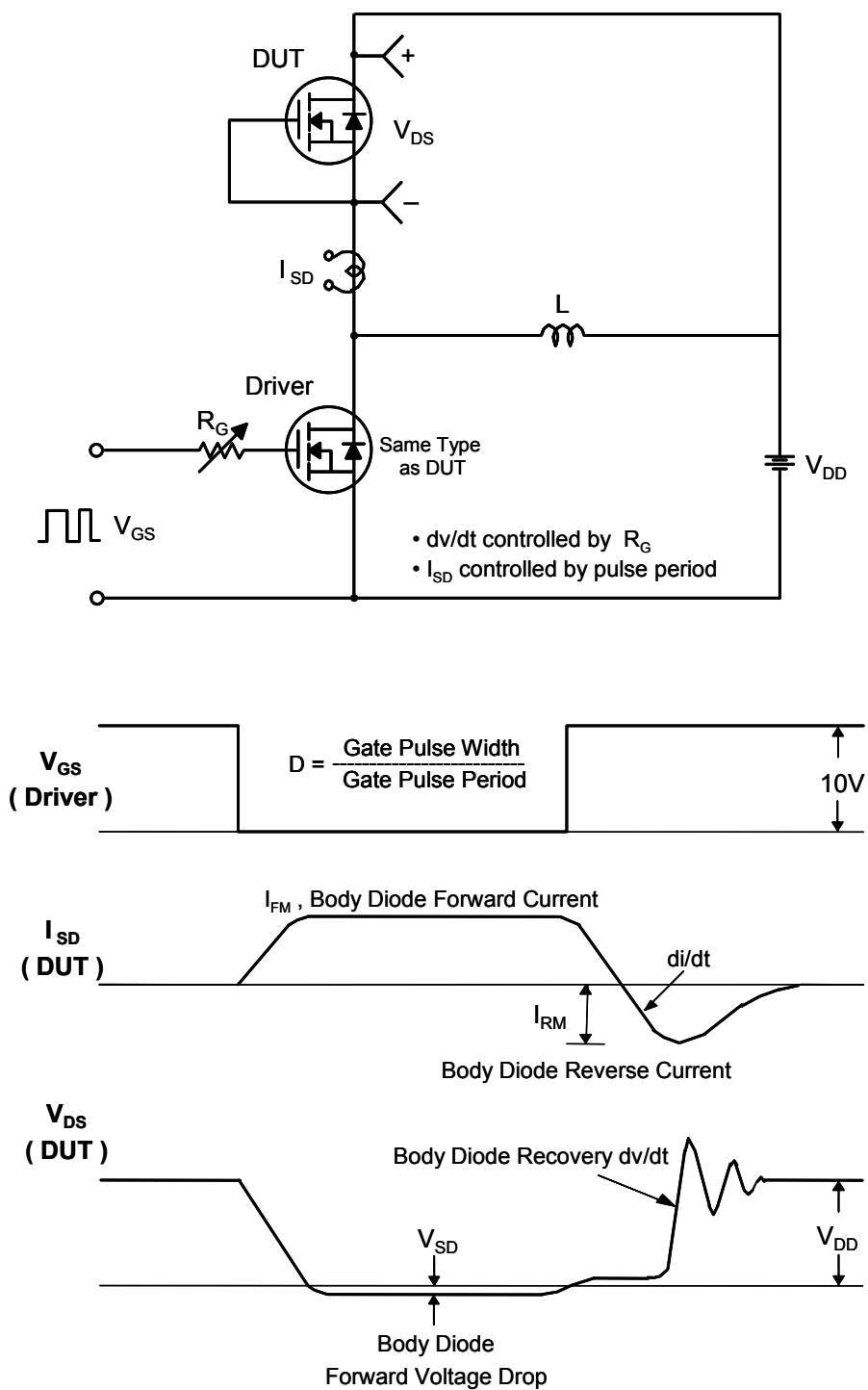


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms



Mechanical Dimensions

TO-220 3L

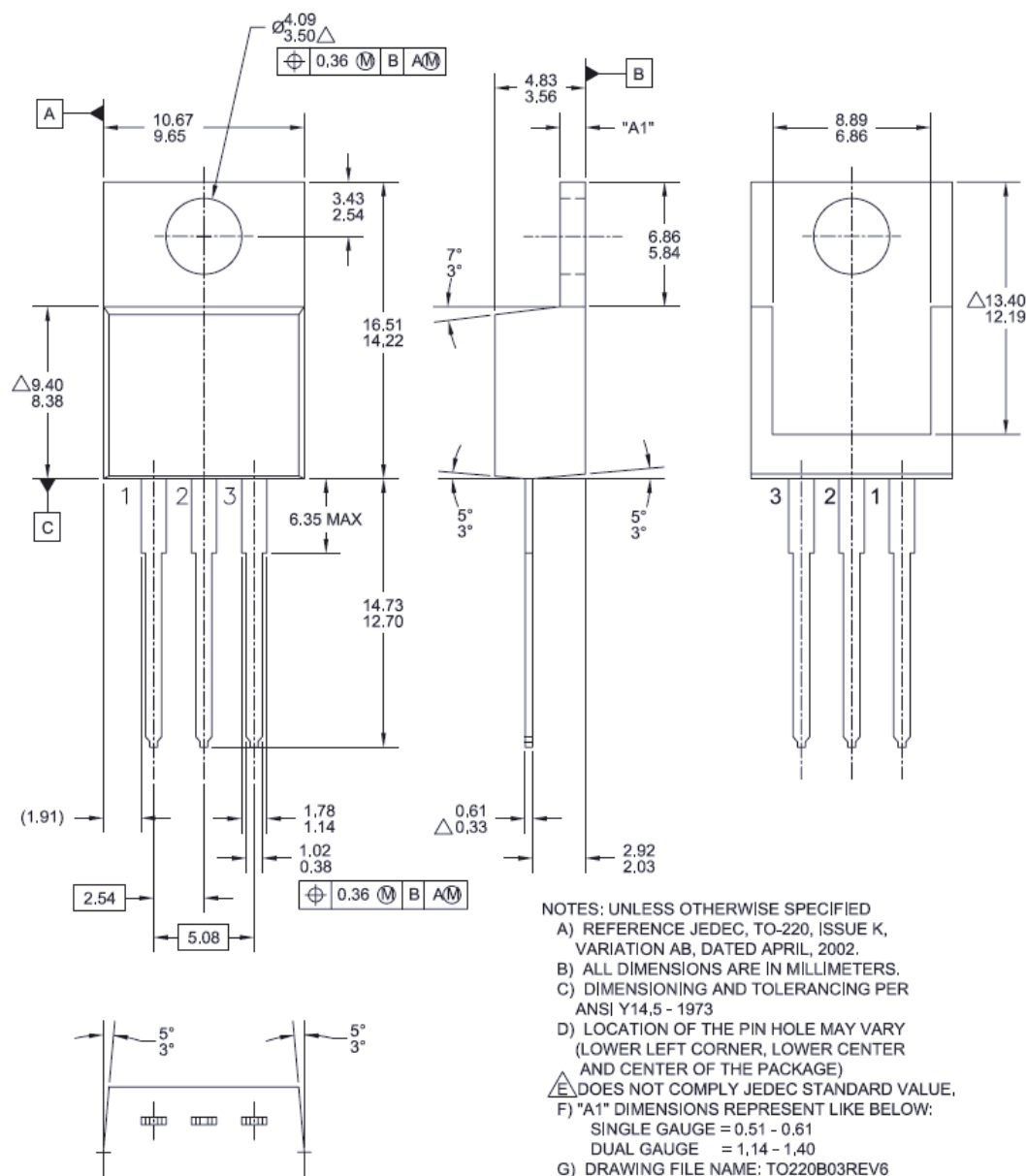


Figure 16. TO-220, Molded, 3Lead, Jedec Variation AB

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Dimension in Millimeters



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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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