

# LMV431/LMV431A

## Low-Voltage (1.24V) Adjustable Precision Shunt Regulators

### General Description

The LMV431 and LMV431A are precision 1.24V shunt regulators capable of adjustment to 30V. Negative feedback from the cathode to the adjust pin controls the cathode voltage, much like a non-inverting op amp configuration (Refer to Symbol and Functional diagrams). A two resistor voltage divider terminated at the adjust pin controls the gain of a 1.24V band-gap reference. Shorting the cathode to the adjust pin (voltage follower) provides a cathode voltage of a 1.24V.

The LMV431 and LMV431A have respective initial tolerances of 1.5% and 1%. Both grades are available in commercial and Industrial temperature ranges.

The LMV431 and LMV431A functionally lends themselves to several applications that require zener diode type performance at low voltages. Applications include a 3V to 2.7V low drop-out regulator, an error amplifier in a 3V off-line switching regulator and even as a voltage detector. The part is typically stable with capacitive loads greater than 10nF and less than 50 pF.

The LMV431 and LMV431A provide performance at a competitive price.

### Features

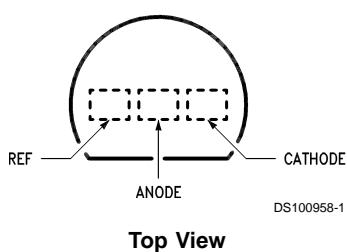
- Low Voltage Operation/Wide Adjust Range (1.24V/30V)
- 1% Initial Tolerance (LMV431A)
- Temperature Compensated for Industrial Temperature Range (39 PPM/°C for the LMV431A)
- Low Operation Current (55µA)
- Low Output Impedance (0.25Ω)
- Fast Turn-On Response
- Low Cost

### Applications

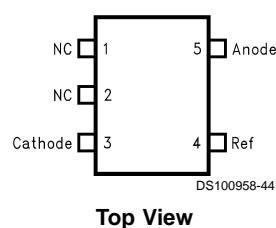
- Shunt Regulator
- Series Regulator
- Current Source or Sink
- Voltage Monitor
- Error Amplifier
- 3V Off-Line Switching Regulator
- Low Dropout N-Channel Series Regulator

### Connection Diagrams

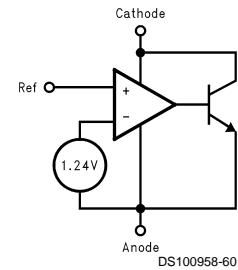
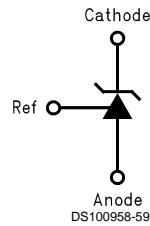
TO92: Plastic Package



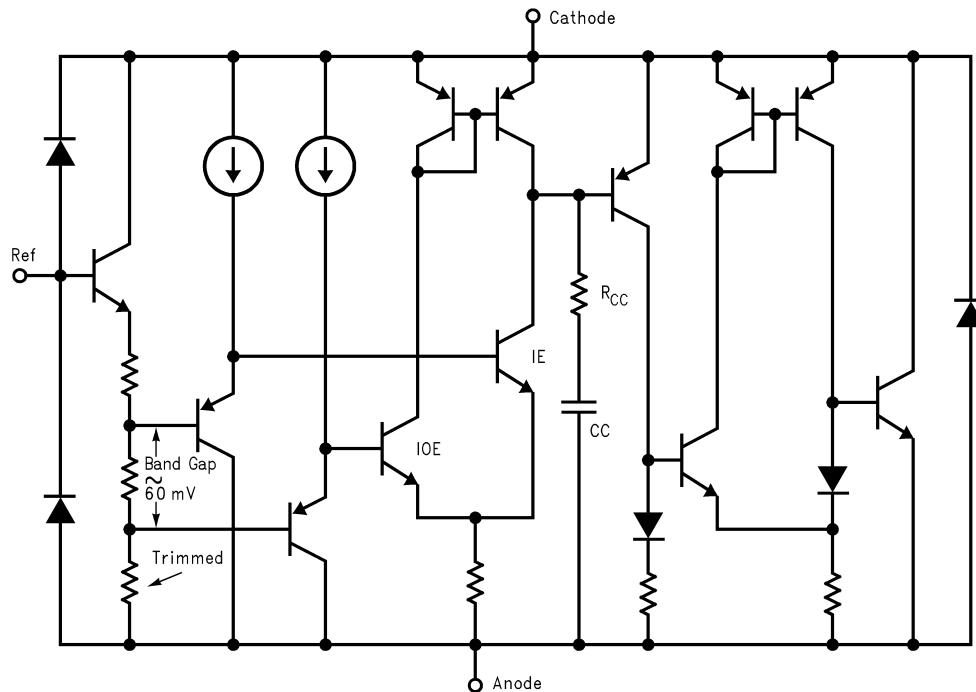
SOT23-5



## Symbol and Functional Diagrams



## Simplified Schematic



## Ordering Information

Package	Temperature Range	Voltage Tolerance	Part Number	Package Marking	Drawing Number
TO92	Industrial Range -40°C to +85°C	1%	LMV431AIZ	LMV431AIZ	Z03A
		1.5%	LMV431IZ	LMV431IZ	
	Commercial Range 0°C to +70°C	1%	LMV431ACZ	LMV431ACZ	
		1.5%	LMV431CZ	LMV431CZ	
SOT23-5	Industrial Range -40°C to +85°C	1%	LMV431AIM5	N08A	MA05A
		1%	LMV431AIM5X	N08A	
		1.5%	LMV431IM5	N08B	
		1.5%	LMV431IM5X	N08B	
	Commercial Range 0°C to +70°C	1%	LMV431ACM5	N09A	
		1%	LMV431ACM5X	N09A	
		1.5%	LMV431CM5	N09B	
		1.5%	LMV431CM5X	N09B	

## DC/AC Test Circuits for Table and Curves

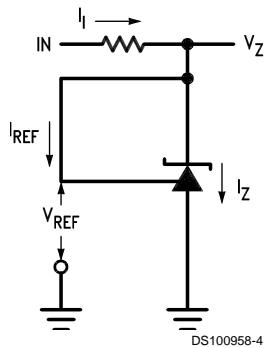
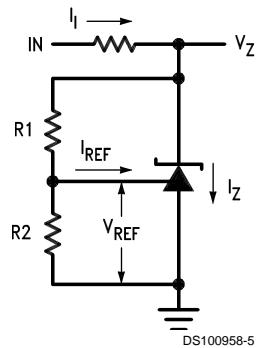


FIGURE 1. Test Circuit for  $V_z = V_{REF}$



Note:  $V_z = V_{REF} (1 + R_1/R_2) + I_{REF} \cdot R_1$

FIGURE 2. Test Circuit for  $V_z > V_{REF}$

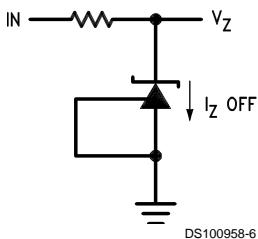


FIGURE 3. Test Circuit for Off-State Current

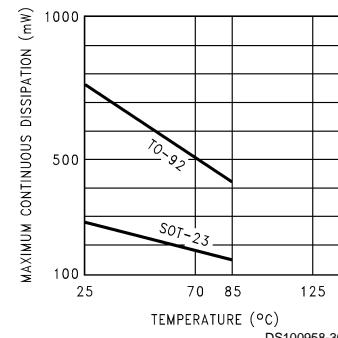
## Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Storage Temperature Range	-65°C to +150°C
Operating Temperature Range	
Industrial (LMV431AI, LMV431I)	-40°C to +85°C
Commercial (LMV431AC, LMV431C)	0°C to +70°C
Lead Temperature	
TO92 Package/SOT23 -5Package (Soldering, 10 sec.)	265°C
Internal Power Dissipation (Note 2)	
TO92	0.78W
SOT23-5 Package	0.28W
Cathode Voltage	35V
Continuous Cathode Current	-30 mA to +30mA
Reference Input Current range	-.05mA to 3mA

## Operating Conditions

Cathode Voltage	V <sub>REF</sub> to 30V
Cathode Current	0.1 mA to 15mA
Temperature range	
LMV431AI	-40°C ≤ T <sub>A</sub> ≤ 85°C
Thermal Resistance (θ <sub>JA</sub> )(Note 3)	
SOT23-5 Package	455 °C/W
TO-92 Package	161 °C/W
Derating Curve (Slope = -1/θ <sub>JA</sub> )	



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## LMV431C Electrical Characteristics

T<sub>A</sub> = 25°C unless otherwise specified

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V <sub>REF</sub>	Reference Voltage	V <sub>Z</sub> =V <sub>REF</sub> , I <sub>Z</sub> =10mA (See Figure 1)	1.222	1.24	1.258	V
		T <sub>A</sub> = 25°C T <sub>A</sub> = Full Range	1.21		1.27	
V <sub>DEV</sub>	Deviation of Reference Input Voltage Over Temperature (Note 4)	V <sub>Z</sub> = V <sub>REF</sub> , I <sub>Z</sub> =10mA, T <sub>A</sub> =Full Range (See Figure 1)		4	12	mV
$\frac{\Delta V_{REF}}{\Delta V_Z}$	Ratio of the Change in Reference Voltage to the Change in Cathode Voltage	I <sub>Z</sub> = 10mA (see Figure 2) V <sub>Z</sub> from V <sub>REF</sub> to 6V R <sub>1</sub> = 10k, R <sub>2</sub> = ∞ and 2.6K		-1.5	-2.7	mV/V
I <sub>REF</sub>	Reference Input Current	R <sub>1</sub> = 10kΩ, R <sub>2</sub> = ∞ I <sub>I</sub> = 10mA (see Figure 2)		0.15	0.5	μA
$\propto I_{REF}$	Deviation of Reference Input Current over Temperature	R <sub>1</sub> = 10kΩ, R <sub>2</sub> = ∞, I <sub>I</sub> = 10mA, T <sub>A</sub> = Full Range (see Figure 2)		0.05	0.3	μA
I <sub>Z(MIN)</sub>	Minimum Cathode Current for Regulation	V <sub>Z</sub> = V <sub>REF</sub> (see Figure 1)		55	80	μA
I <sub>Z(OFF)</sub>	Off-State Current	V <sub>Z</sub> =6V, V <sub>REF</sub> = 0V (see Figure 3)		0.001	0.1	μA
r <sub>Z</sub>	Dynamic Output Impedance (Note 5)	V <sub>Z</sub> = V <sub>REF</sub> , I <sub>Z</sub> = 0.1mA to 15mA Frequency = 0Hz (see Figure 1)		0.25	0.4	Ω

## LMV431I Electrical Characteristics

$T_A = 25^\circ\text{C}$  unless otherwise specified

Symbol	Parameter	Conditions		Min	Typ	Max	Units
$V_{\text{REF}}$	Reference Voltage	$V_Z = V_{\text{REF}}, I_Z = 10\text{mA}$ (See Figure 1)	$T_A = 25^\circ\text{C}$	1.222	1.24	1.258	V
			$T_A = \text{Full Range}$	1.202		1.278	
$V_{\text{DEV}}$	Deviation of Reference Input Voltage Over Temperature (Note 4)	$V_Z = V_{\text{REF}}, I_Z = 10\text{mA},$ $T_A = \text{Full Range}$ (See Figure 1)			6	20	mV
$\frac{\Delta V_{\text{REF}}}{\Delta V_Z}$	Ratio of the Change in Reference Voltage to the Change in Cathode Voltage	$I_Z = 10\text{mA}$ (see Figure 2) $V_Z$ from $V_{\text{REF}}$ to 6V $R_1 = 10\text{k}\Omega, R_2 = \infty$ and 2.6K			-1.5	-2.7	mV/V
$I_{\text{REF}}$	Reference Input Current	$R_1 = 10\text{k}\Omega, R_2 = \infty$ $I_I = 10\text{mA}$ (see Figure 2)			0.15	0.5	$\mu\text{A}$
$\propto I_{\text{REF}}$	Deviation of Reference Input Current over Temperature	$R_1 = 10\text{k}\Omega, R_2 = \infty,$ $I_I = 10\text{mA}, T_A = \text{Full Range}$ (see Figure 2)			0.1	0.4	$\mu\text{A}$
$I_{Z(\text{MIN})}$	Minimum Cathode Current for Regulation	$V_Z = V_{\text{REF}}$ (see Figure 1)			55	80	$\mu\text{A}$
$I_{Z(\text{OFF})}$	Off-State Current	$V_Z = 6\text{V}, V_{\text{REF}} = 0\text{V}$ (see Figure 3)			0.001	0.1	$\mu\text{A}$
$r_Z$	Dynamic Output Impedance (Note 5)	$V_Z = V_{\text{REF}}, I_Z = 0.1\text{mA to } 15\text{mA}$ Frequency = 0Hz (see Figure 1)			0.25	0.4	$\Omega$

## LMV431AC Electrical Characteristics

$T_A = 25^\circ\text{C}$  unless otherwise specified

Symbol	Parameter	Conditions		Min	Typ	Max	Units
$V_{\text{REF}}$	Reference Voltage	$V_Z = V_{\text{REF}}, I_Z = 10\text{ mA}$ (See Figure 1)	$T_A = 25^\circ\text{C}$	1.228	1.24	1.252	V
			$T_A = \text{Full Range}$	1.221		1.259	
$V_{\text{DEV}}$	Deviation of Reference Input Voltage Over Temperature (Note 4)	$V_Z = V_{\text{REF}}, I_Z = 10\text{mA},$ $T_A = \text{Full Range}$ (See Figure 1)			4	12	mV
$\frac{\Delta V_{\text{REF}}}{\Delta V_Z}$	Ratio of the Change in Reference Voltage to the Change in Cathode Voltage	$I_Z = 10\text{ mA}$ (see Figure 2) $V_Z$ from $V_{\text{REF}}$ to 6V $R_1 = 10\text{k}\Omega, R_2 = \infty$ and 2.6K			-1.5	-2.7	mV/V
$I_{\text{REF}}$	Reference Input Current	$R_1 = 10\text{k}\Omega, R_2 = \infty$ $I_I = 10\text{ mA}$ (see Figure 2)			0.15	0.50	$\mu\text{A}$
$\propto I_{\text{REF}}$	Deviation of Reference Input Current over Temperature	$R_1 = 10\text{k}\Omega, R_2 = \infty,$ $I_I = 10\text{ mA}, T_A = \text{Full Range}$ (see Figure 2)			0.05	0.3	$\mu\text{A}$
$I_{Z(\text{MIN})}$	Minimum Cathode Current for Regulation	$V_Z = V_{\text{REF}}$ (see Figure 1)			55	80	$\mu\text{A}$
$I_{Z(\text{OFF})}$	Off-State Current	$V_Z = 6\text{V}, V_{\text{REF}} = 0\text{V}$ (see Figure 3)			0.001	0.1	$\mu\text{A}$
$r_Z$	Dynamic Output Impedance (Note 5)	$V_Z = V_{\text{REF}}, I_Z = 0.1\text{mA to } 15\text{mA}$ Frequency = 0 Hz (see Figure 1)			0.25	0.4	$\Omega$

## LMV431AI Electrical Characteristics

 $T_A = 25^\circ\text{C}$  unless otherwise specified

Symbol	Parameter	Conditions		Min	Typ	Max	Units
$V_{\text{REF}}$	Reference Voltage	$V_Z = V_{\text{REF}}$ , $I_Z = 10\text{mA}$ (See Figure 1)	$T_A = 25^\circ\text{C}$	1.228	1.24	1.252	V
			$T_A = \text{Full Range}$	1.215		1.265	
$V_{\text{DEV}}$	Deviation of Reference Input Voltage Over Temperature (Note 4)	$V_Z = V_{\text{REF}}$ , $I_Z = 10\text{mA}$ , $T_A = \text{Full Range}$ (See Figure 1)			6	20	mV
$\frac{\Delta V_{\text{REF}}}{\Delta V_Z}$	Ratio of the Change in Reference Voltage to the Change in Cathode Voltage	$I_Z = 10\text{mA}$ (see Figure 2) $V_Z$ from $V_{\text{REF}}$ to 6V $R_1 = 10\text{k}$ , $R_2 = \infty$ and 2.6K			-1.5	-2.7	mV/V
$I_{\text{REF}}$	Reference Input Current	$R_1 = 10\text{k}\Omega$ , $R_2 = \infty$ $I_I = 10\text{mA}$ (see Figure 2)			0.15	0.5	$\mu\text{A}$
$\frac{\Delta I_{\text{REF}}}{\Delta T_A}$	Deviation of Reference Input Current over Temperature	$R_1 = 10\text{k}\Omega$ , $R_2 = \infty$ , $I_I = 10\text{mA}$ , $T_A = \text{Full Range}$ (see Figure 2)			0.1	0.4	$\mu\text{A}$
$I_{Z(\text{MIN})}$	Minimum Cathode Current for Regulation	$V_Z = V_{\text{REF}}$ (see Figure 1)			55	80	$\mu\text{A}$
$I_{Z(\text{OFF})}$	Off-State Current	$V_Z = 6\text{V}$ , $V_{\text{REF}} = 0\text{V}$ (see Figure 3)			0.001	0.1	$\mu\text{A}$
$r_Z$	Dynamic Output Impedance (Note 5)	$V_Z = V_{\text{REF}}$ , $I_Z = 0.1\text{mA}$ to 15mA Frequency = 0Hz (see Figure 1)			0.25	0.4	$\Omega$

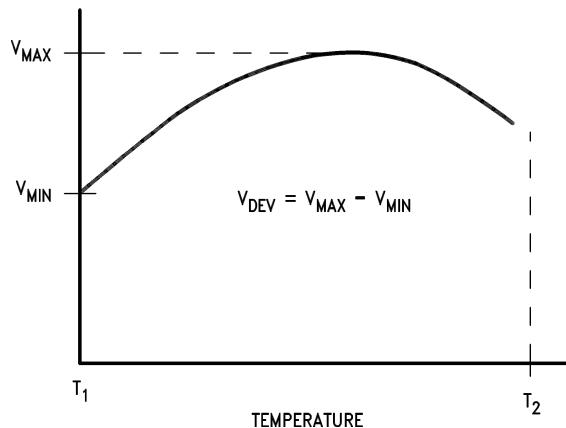
**Note 1:** Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Electrical specifications do not apply when operating the device beyond its rated operating conditions.

**Note 2:** Ratings apply to ambient temperature at  $25^\circ\text{C}$ . Above this temperature, derate the TO92 at  $6.2 \text{ mW}/^\circ\text{C}$ , and the SOT23-5 at  $2.2 \text{ mW}/^\circ\text{C}$ . See derating curve in Operating Condition section..

**Note 3:**  $T_J \text{ Max} = 150^\circ\text{C}$ ,  $T_J = T_A + (\theta_{JA} P_D)$ , where  $P_D$  is the operating power of the device.

**Note 4:** Deviation of reference input voltage,  $V_{\text{DEV}}$ , is defined as the maximum variation of the reference input voltage over the full temperature range. See following:

## LMV431AI Electrical Characteristics (Continued)



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The average temperature coefficient of the reference input voltage,  $\approx V_{REF}$ , is defined as:

$$\approx V_{REF} \frac{\text{ppm}}{\text{°C}} = \frac{\pm \left[ \frac{V_{\text{Max}} - V_{\text{Min}}}{V_{\text{REF}} \text{ (at 25°C)}} \right] 10^6}{T_2 - T_1} = \frac{\pm \left[ \frac{V_{\text{DEV}}}{V_{\text{REF}} \text{ (at 25°C)}} \right] 10^6}{T_2 - T_1}$$

Where:

$T_2 - T_1$  = full temperature change.

$\approx V_{REF}$  can be positive or negative depending on whether the slope is positive or negative.

Example:  $V_{\text{DEV}} = 6.0 \text{ mV}$ ,  $V_{\text{REF}} = 1240 \text{ mV}$ ,  $T_2 - T_1 = 125 \text{ °C}$ .

$$\approx V_{REF} = \frac{\left[ \frac{6.0 \text{ mV}}{1240 \text{ mV}} \right] 10^6}{125 \text{ °C}} = +39 \text{ ppm/°C}$$

**Note 5:** The dynamic output impedance,  $r_Z$ , is defined as:

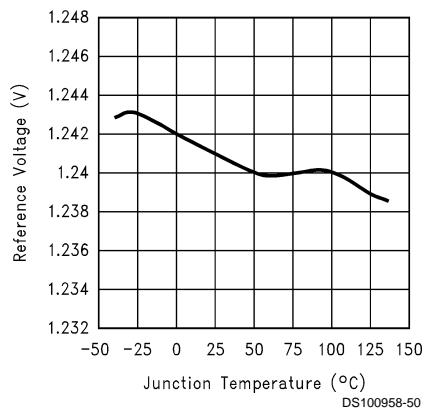
$$r_Z = \frac{\Delta V_Z}{\Delta I_Z}$$

When the device is programmed with two external resistors, R1 and R2, (see *Figure 2*), the dynamic output impedance of the overall circuit,  $r_Z$ , is defined as:

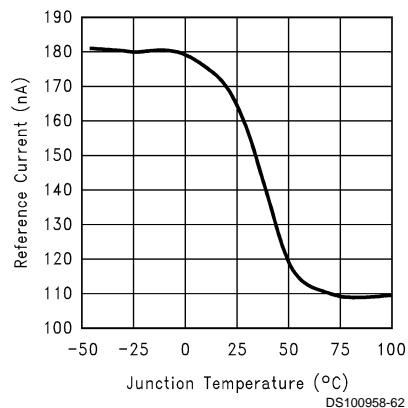
$$r_Z = \frac{\Delta V_Z}{\Delta I_Z} \approx \left[ r_Z \left( 1 + \frac{R1}{R2} \right) \right]$$

## Typical Performance Characteristics

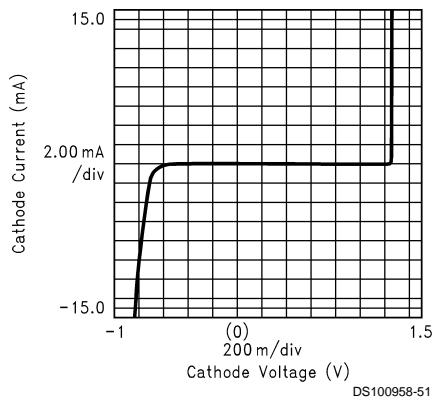
Reference Voltage vs. Junction Temperature



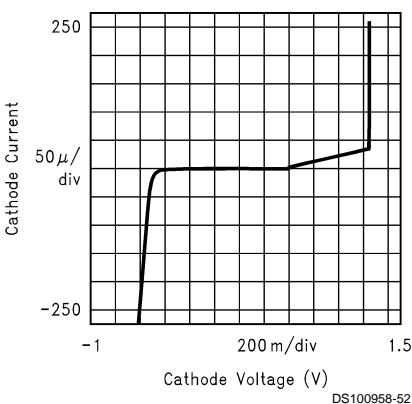
Reference Input Current vs. Junction Temperature



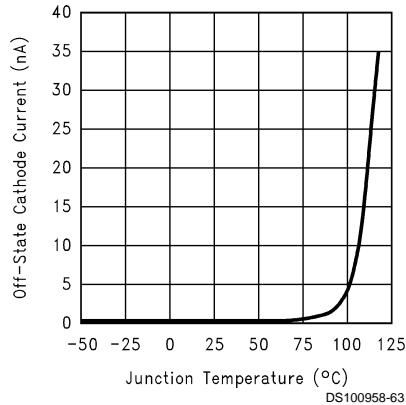
Cathode Current vs. Cathode Voltage 1



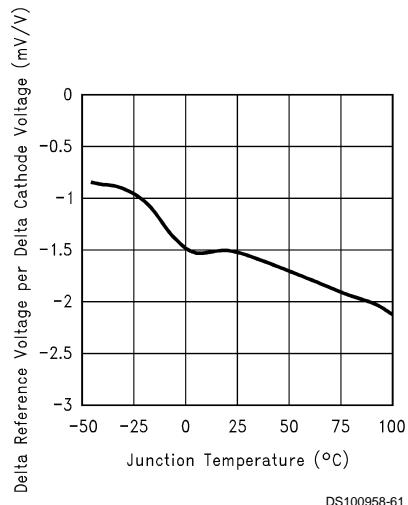
Cathode Current vs. Cathode Voltage 2



Off-State Cathode Current vs. Junction Temperature

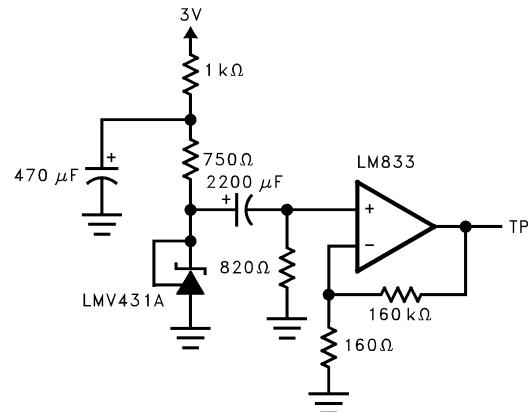
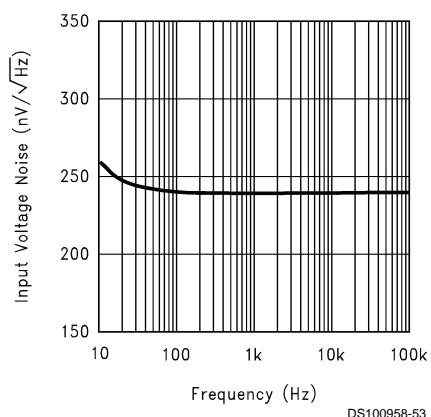


Delta Reference Voltage Per Delta Cathode Voltage vs. Junction Temperature



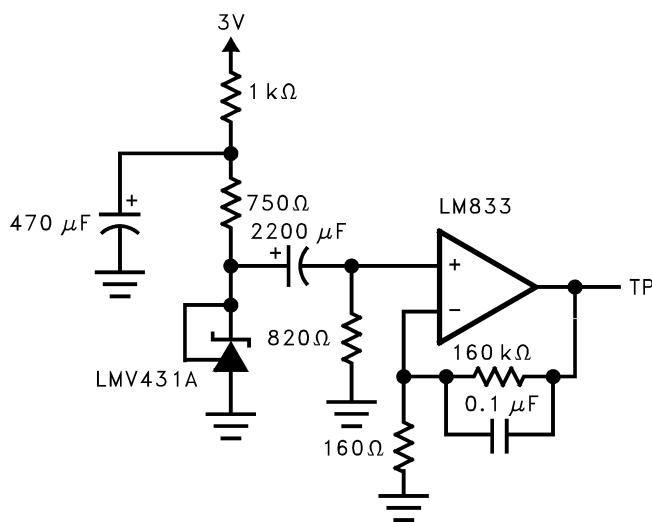
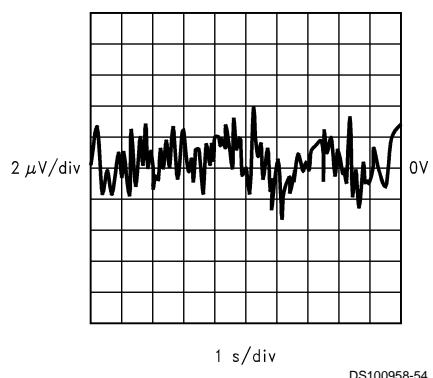
## Typical Performance Characteristics (Continued)

### Input Voltage Noise vs. Frequency



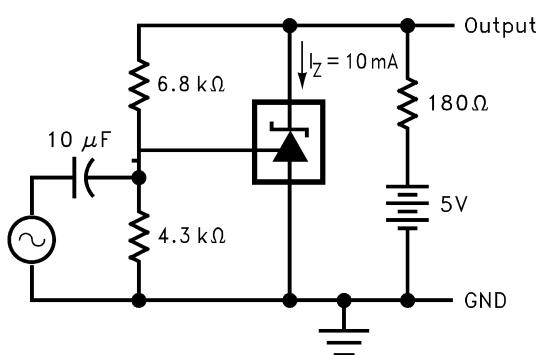
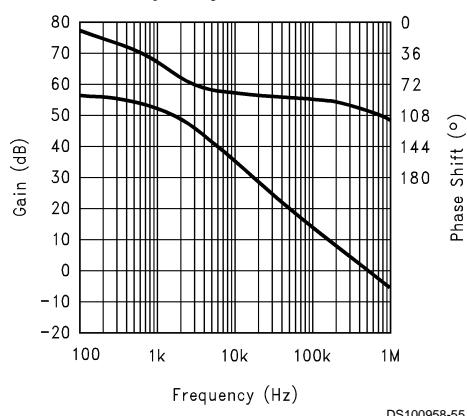
**Test Circuit for Input Voltage Noise vs Frequency**

### Low Frequency Peak to Peak Noise



**Test Circuit for Peak to Peak Noise (BW= 0.1Hz to 10Hz)**

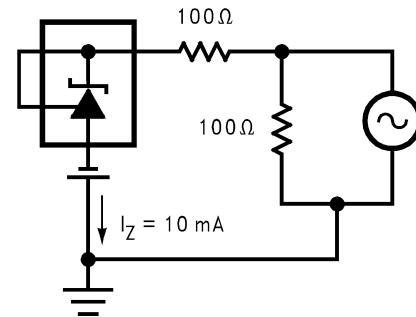
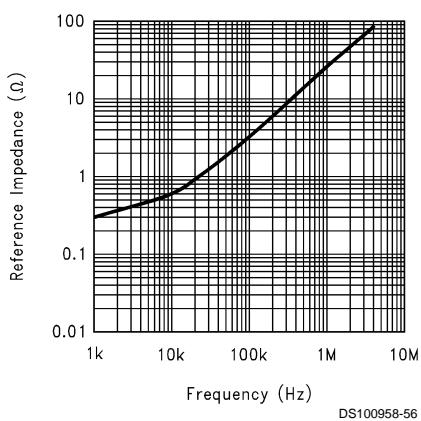
### Small Signal Voltage Gain and Phase Shift vs. Frequency



**Test Circuit For Voltage Gain and Phase Shift vs Frequency**

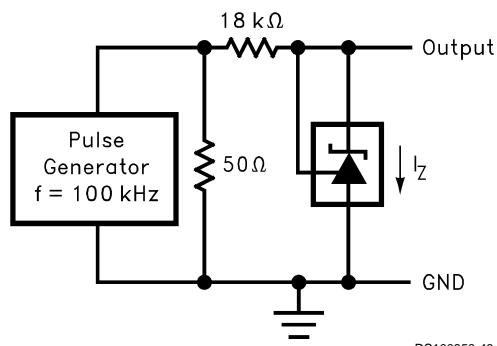
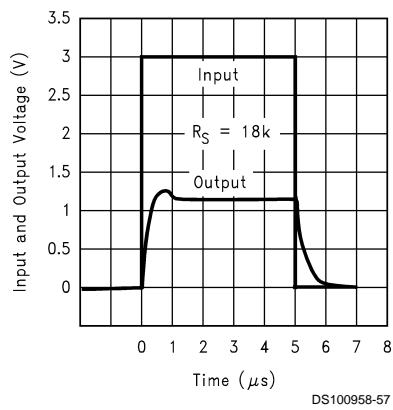
## Typical Performance Characteristics (Continued)

### Reference Impedance vs Frequency



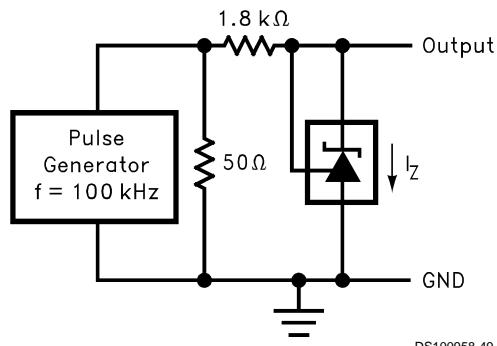
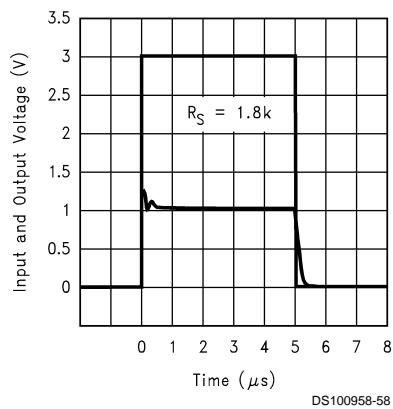
**Test Circuit For Reference Impedance vs Frequency**

### Pulse Response 1



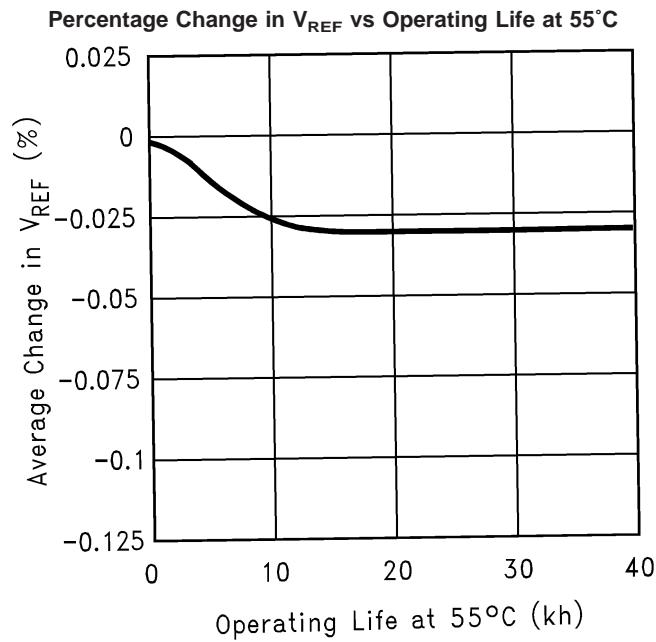
**Test Circuit for Pulse Response 1**

### Pulse Response 2



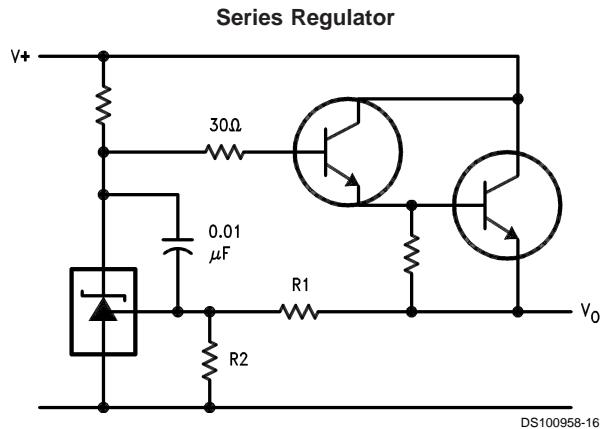
**Test Circuit for Pulse Response 2**

## Typical Performance Characteristics (Continued)



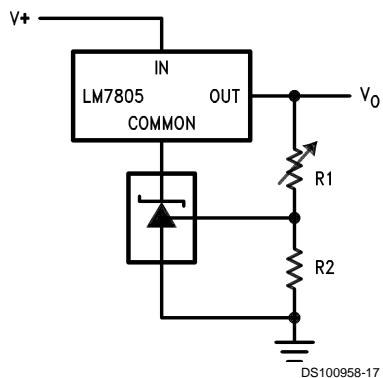
Extrapolated from life-test data taken at 125°C; the activation energy assumed is 0.7eV.

## Typical Applications



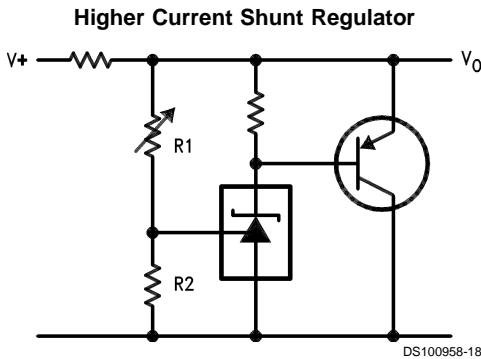
$$V_O \approx \left(1 + \frac{R_1}{R_2}\right) V_{REF}$$

### Output Control of a Three Terminal Fixed Regulator



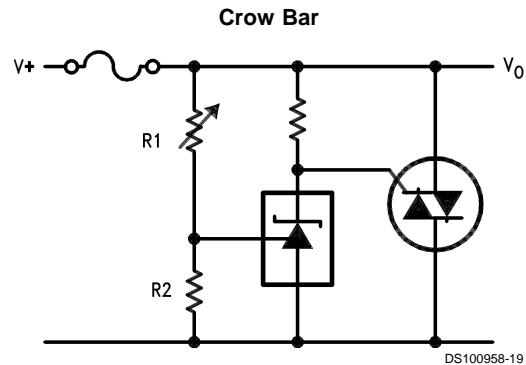
$$V_O = \left(1 + \frac{R_1}{R_2}\right) V_{REF}$$

$$V_O \text{ MIN} = V_{REF} + 5V$$

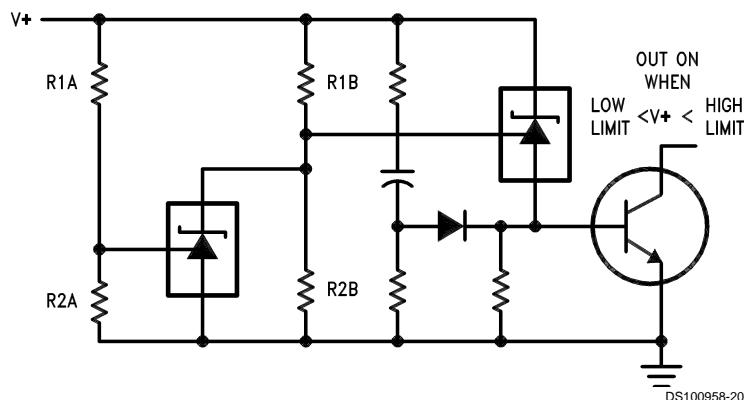


$$V_O \approx \left(1 + \frac{R_1}{R_2}\right) V_{REF}$$

$$V_{LIMIT} \approx \left(1 + \frac{R_1}{R_2}\right) V_{REF}$$



### Over Voltage/Under Voltage Protection Circuit

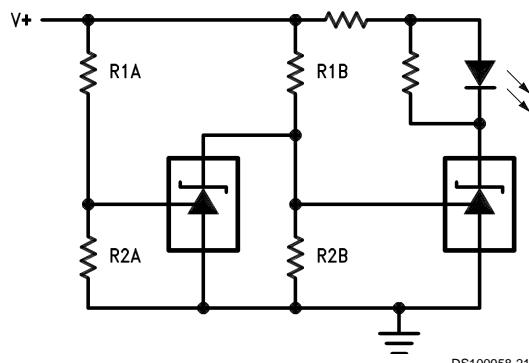


$$\text{LOW LIMIT} \approx V_{REF} \left(1 + \frac{R_{1B}}{R_{2B}}\right) + V_{BE}$$

$$\text{HIGH LIMIT} \approx V_{REF} \left(1 + \frac{R_{1A}}{R_{2A}}\right)$$

## Typical Applications (Continued)

**Voltage Monitor**



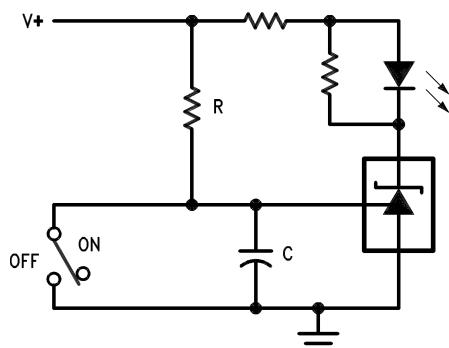
$$\text{LOW LIMIT} \approx V_{\text{REF}} \left( 1 + \frac{R_{1B}}{R_{2B}} \right)$$

LED ON WHEN  
LOW LIMIT < V<sup>+</sup> < HIGH LIMIT

$$\text{HIGH LIMIT} \approx V_{\text{REF}} \left( 1 + \frac{R_{1A}}{R_{2A}} \right)$$

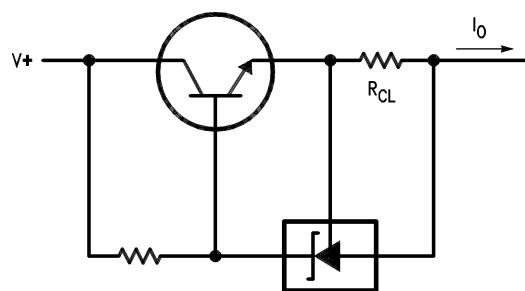
DS100958-21

**Delay Timer**



$$\text{DELAY} = R \cdot C \cdot \ln \frac{V^+}{(V^+) - V_{\text{REF}}}$$

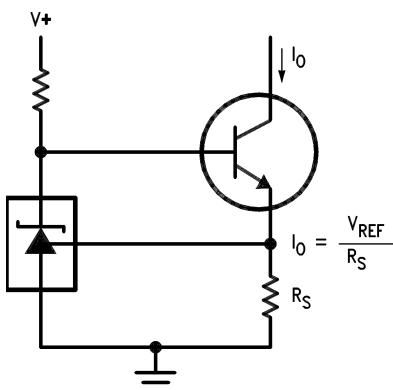
**Current Limiter or Current Source**



$$I_O = \frac{V_{\text{REF}}}{R_{\text{CL}}}$$

DS100958-23

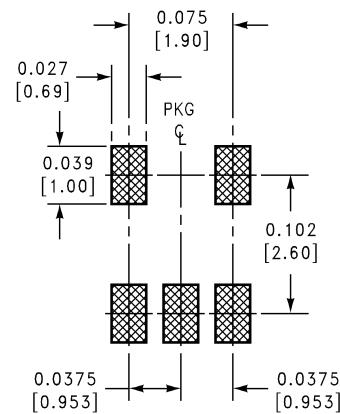
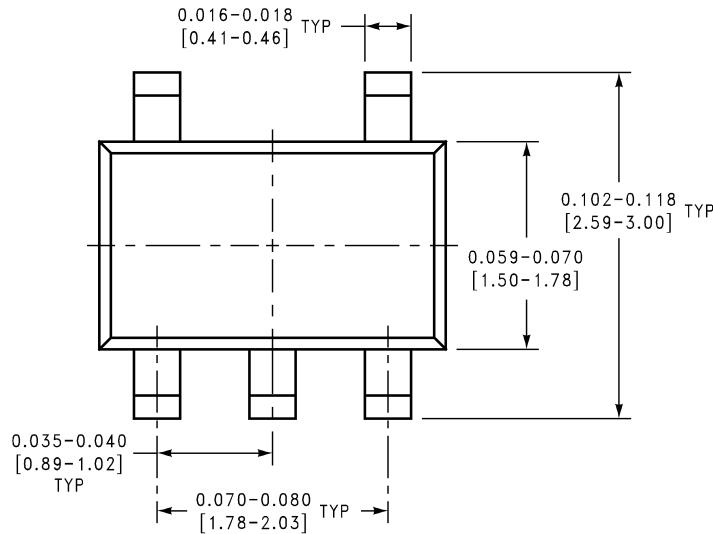
**Constant Current Sink**



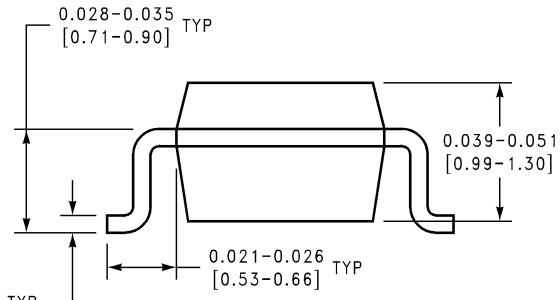
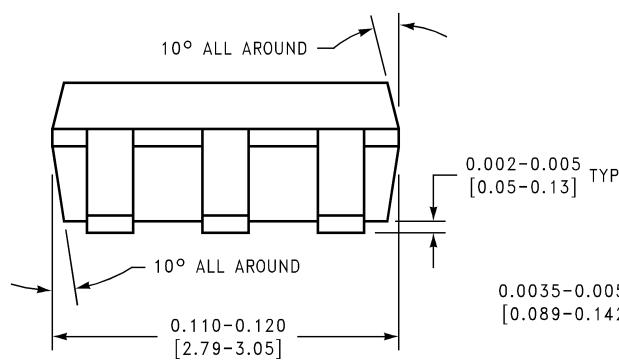
DS100958-24

## Physical Dimensions

inches (millimeters) unless otherwise noted



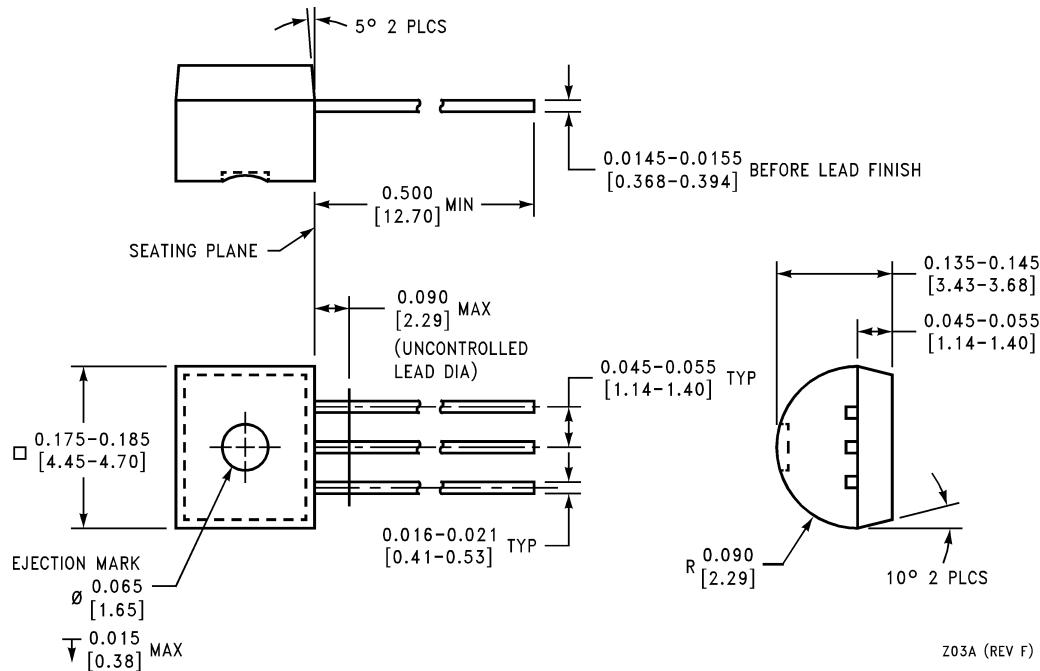
### LAND PATTERN RECOMMENDATION



MA05A (REV D)

**SOT23-5 Molded Small Outline Transistor Package (M5)**  
**Order Number LMV431AIM5, LMV431AIM5X, LMV431IM5, LMV431IM5X,**  
**LMV431ACM5, LMV431ACM5X, LMV431CM5 and LMV431CM5X**  
**NS Package Number MA05A**

## Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



Order Number LMV431AIZ, LMV431IZ, LMV431ACZ and LMV431CZ  
 NS Package Number Z03A

### LIFE SUPPORT POLICY

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



**National Semiconductor Corporation**  
 Americas  
 Tel: 1-800-272-9959  
 Fax: 1-800-737-7018  
 Email: support@nsc.com  
[www.national.com](http://www.national.com)

**National Semiconductor Europe**  
 Fax: +49 (0) 1 80-530 85 86  
 Email: europe.support@nsc.com  
 Deutsch Tel: +49 (0) 1 80-530 85 85  
 English Tel: +49 (0) 1 80-532 78 32  
 Français Tel: +49 (0) 1 80-532 93 58  
 Italiano Tel: +49 (0) 1 80-534 16 80

**National Semiconductor Asia Pacific Customer Response Group**  
 Tel: 65-2544466  
 Fax: 65-2504466  
 Email: sea.support@nsc.com

**National Semiconductor Japan Ltd.**  
 Tel: 81-3-5639-7560  
 Fax: 81-3-5639-7507