

HVU363A

Variable Capacitance Diode for TV tuner

REJ03G0523-0300
(Previous: ADE-208-234B)
Rev.3.00
Feb 24, 2005

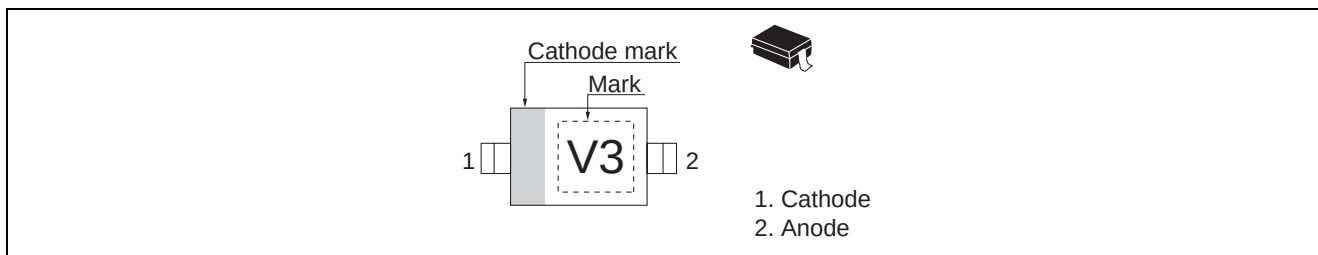
Features

- High capacitance ratio ($n = 15.0$ Typ)
- Low series resistance ($r_s = 0.75 \Omega$ max) and good C-V linearity.
- Wide range tolerance reduction to avoid tracking error. ($V_R = 1$ to 28 V)
- Ultra small Resin Package (URP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
HVU363A	V3	URP	PTSP0002ZA-A (URP)

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}^{*1}	35	V
Reverse voltage	V_R	32	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Note: 1. $R_L = 10\text{ k}\Omega$

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse voltage	V_R	32	—	—	V	$I_R = 1\text{ }\mu\text{A}$
Reverse current	I_{R1}	—	—	10	nA	$V_R = 30\text{ V}$
	I_{R2}	—	—	100		$V_R = 30\text{ V}, T_a = 60^\circ\text{C}$
Capacitance	C_1	34.65	—	42.35	pF	$V_R = 1\text{ V}, f = 1\text{ MHz}$
	C_{28}	2.361	—	2.754		$V_R = 28\text{ V}, f = 1\text{ MHz}$
Capacitance ratio	n	13.50	15.00	—	—	C_1/C_{28}
Series resistance	r_s	—	—	0.75	Ω	$C = 14\text{ pF}, f = 470\text{ MHz}$
Matching error	$\Delta C/C^{*1}$	—	—	2.00	%	$V_R = 1\text{ to }28\text{ V}, f = 1\text{ MHz}$
Linealty factor ^{*2}	—	—	-1.20	—	—	$\Delta\log C / \Delta\log V$

Note: 1. C.C system (Continuous Connected taping system) enable to make any 10 pcs of $\Delta C/C$ continuous in a reel, expect extention to another group.

Calculate Matching Error,

$$\Delta C/C = \frac{(C_{\max} - C_{\min})}{C_{\min}} \times 100\text{ (%)}$$

2. Calculate LF ($\Delta\log C / \Delta\log V$) at $V_R = 1$ through 28 V, $f = 1\text{ MHz}$. (Reference Value)

Main Characteristic

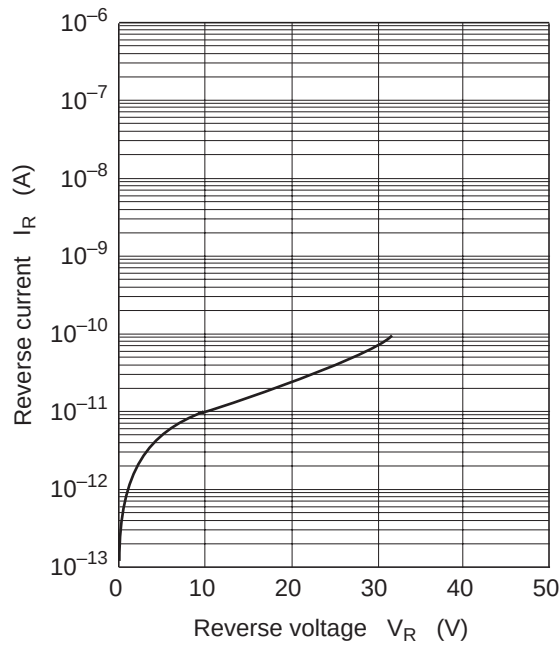


Fig.1 Reverse current vs. Reverse voltage

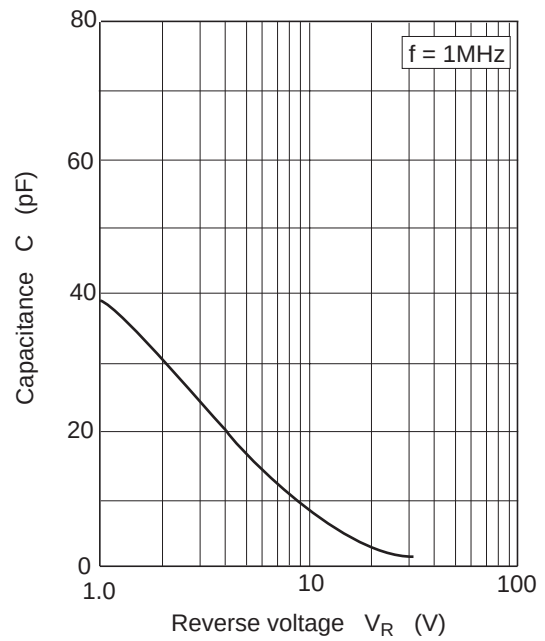


Fig.2 Capacitance vs. Reverse voltage

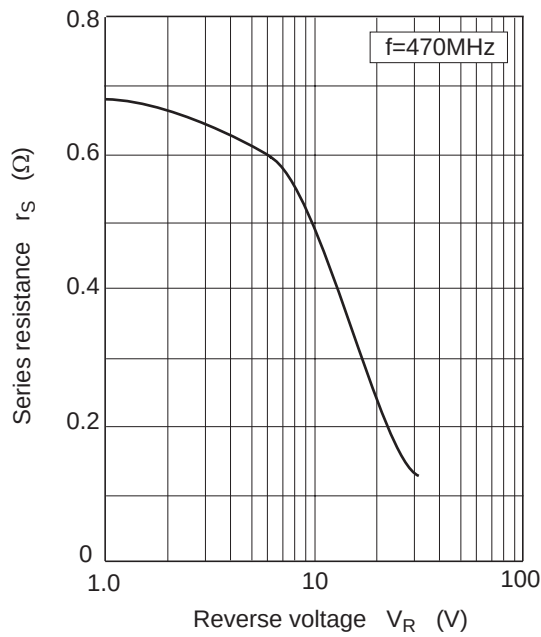


Fig.3 Series resistance vs. Reverse voltage

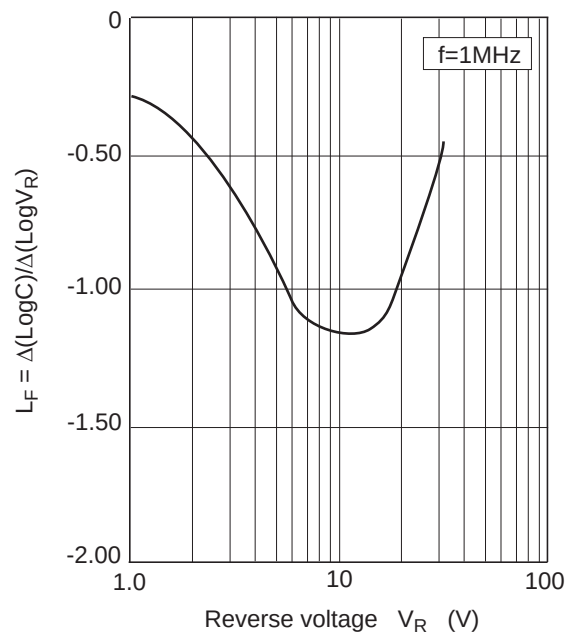
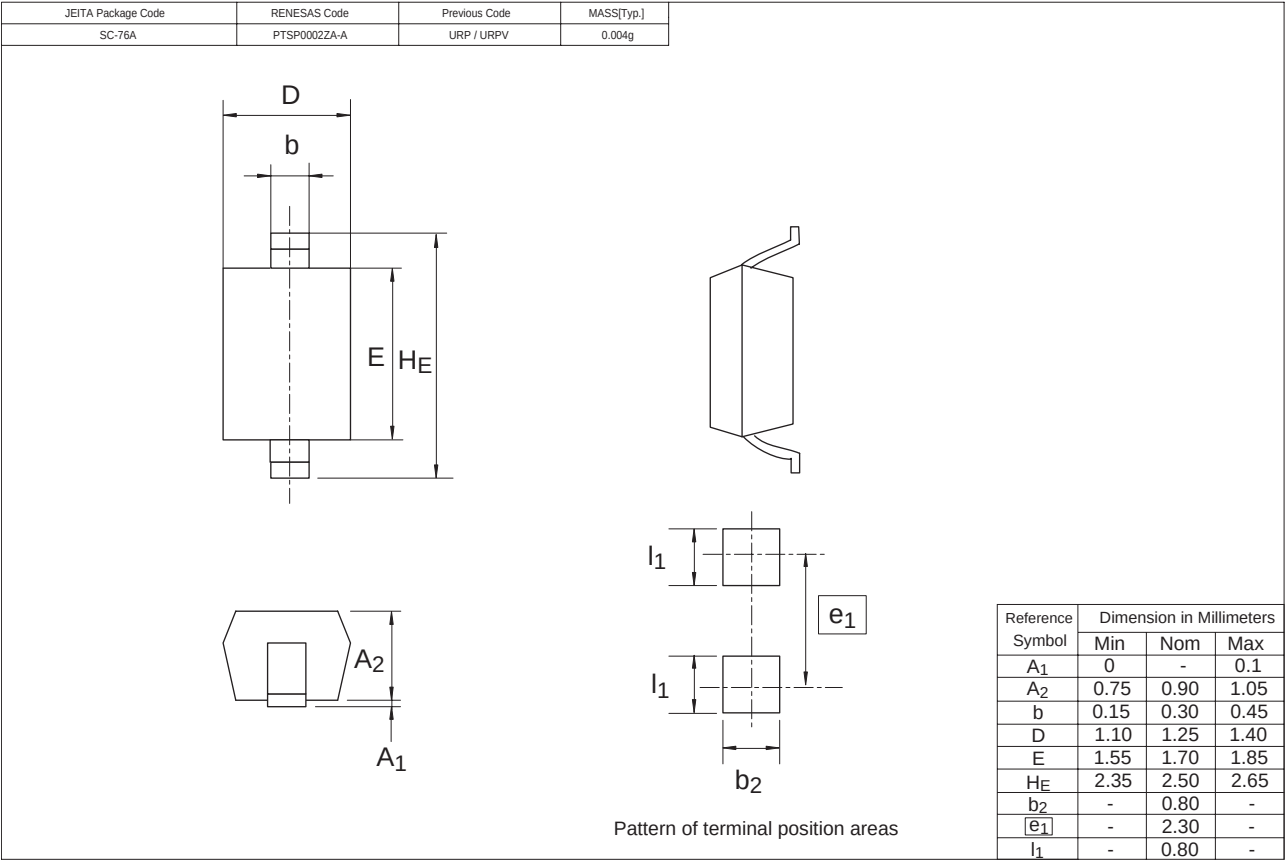


Fig.4 Linearity factor vs. Reverse voltage

Package Dimensions



Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs!

1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.
Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. or a third party.
2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.
The information described here may contain technical inaccuracies or typographical errors.
Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.
Please also pay attention to information published by Renesas Technology Corp. by various means, including the Renesas Technology Corp. Semiconductor home page (<http://www.renesas.com>).
4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
5. Renesas Technology Corp. semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials.
7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.



RENESAS SALES OFFICES

<http://www.renesas.com>

Refer to "<http://www.renesas.com/en/network>" for the latest and detailed information.

Renesas Technology America, Inc.

450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.

7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.

10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.

Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001