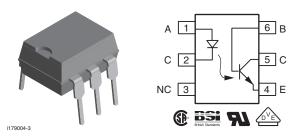




# Optocoupler, Phototransistor Output, with Base Connection, 300 V BV<sub>CEO</sub>

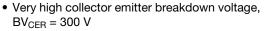


#### **DESCRIPTION**

The SFH640 is an optocoupler with very high  $BV_{CER}$ , a minimum of 300 V. It is intended for telecommunications applications or any DC application requiring a high blocking voltage.

#### **FEATURES**

- Good CTR linearity with forward current
- Low CTR degradation





Isolation test voltage: 5300 V<sub>RMS</sub>

Low coupling capacitance

- High common mode transient immunity
- Phototransistor optocoupler 6 pin DIP package with base connection
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

#### **AGENCY APPROVALS**

- UL1577, file no. E52744 system code H or J, double protection
- DIN EN 60747-5-2 (VDE 0884) available with option 1
- CSA 93751
- BSI IEC 60950; IEC 60065

ORDERING INFORMATION							
S F H 6 4	O - # X O # # T  CTR PACKAGE OPTION TAPE AND REEL  Option 7  > 0.7 mm	Option 9					
AGENCY CERTIFIED/PACKAGE	CTR (%)	CTR (%)					
AGENCI CENTIFIED/FACKAGE	10 mA						
UL, CSA, BSI	63 to 125 100 to 200						
DIP-6	SFH640-2 SFH640-3						
SMD-6, option 7	SFH640-2X007 SFH640-3X007T <sup>(1)</sup>						
VDE, UL, CSA, BSI	63 to 125 100 to 200						
SMD-6, option 9	- SFH640-3X019T <sup>(1)</sup>	SFH640-3X019T <sup>(1)</sup>					

#### **Notes**

- Additional options may be possible, please contact sales office.
- (1) Also available in tubes, do not put T on the end.

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
Reverse voltage		$V_{R}$	6.0	V				
DC forward current		I <sub>F</sub>	60	mA				
Surge forward current	t <sub>p</sub> ≤ 10 µs	I <sub>FSM</sub>	2.5	Α				
Total power dissipation		P <sub>diss</sub>	100	mW				



ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
OUTPUT							
Collector emitter voltage		$V_{CEO}$	300	V			
Collector base voltage		$V_{CBO}$	300	٧			
Emitter base voltage		$V_{EBO}$	7.0	V			
Collector current		I <sub>C</sub>	50	mA			
Surge collector current	t <sub>p</sub> ≤ 10 ms	I <sub>C</sub>	100	mA			
Total power dissipation		P <sub>diss</sub>	300	mW			
COUPLER							
Isolation test voltage		V	5300	$V_{RMS}$			
between emitter and detector		$V_{ISO}$	7500	$V_{PK}$			
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω			
isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω			
Insulation thickness between emitter and detector			≥ 0.4	mm			
Creepage distance			≥ 7	mm			
Clearance distance			≥ 7	mm			
Comparative tracking index per DIN IEC 112/VDE 0303, part 1		CTI	175				
Storage temperature range		T <sub>stg</sub>	- 55 to + 150	°C			
Operating temperature range		T <sub>amb</sub>	- 55 to + 100	°C			
Soldering temperature (1)	max. 10 s, dip soldering: distance to seating plane ≥ 1.5 mm	T <sub>sld</sub>	260	°C			

#### Notes

<sup>(1)</sup> Refer to reflow profile for soldering conditions for surface mounted devices (SMĎ). Refer to wave profile for soldering conditions for through hole devices (DIP).

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT	1201 00112111011	1	01202			11111111	<u> </u>
Forward voltage	I <sub>F</sub> = 10 mA		V <sub>V</sub>		1.1	1.5	V
Reverse voltage	I <sub>R</sub> = 10 μA		$V_R$	6			V
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>		0.01	10	μΑ
Capacitance	V <sub>F</sub> = 0 V, f = 1 MHz		Co		25		pF
Thermal resistance			R <sub>thja</sub>		750		K/W
OUTPUT							
Collector emitter breakdown voltage	$I_{CE} = 1 \text{ mA},$ $R_{BE} = 1 \text{ M}\Omega$		BV <sub>CER</sub>	300			V
Voltage emitter base	I <sub>EB</sub> = 10 μA		$BV_{BEO}$	7			V
Collector emitter capacitance	V <sub>CE</sub> = 10 V, f = 1 MHz		C <sub>CE</sub>		7		pF
Collector base capacitance	V <sub>CB</sub> = 10 V, f = 1 MHz		C <sub>CB</sub>		8		pF
Emitter base capacitance	V <sub>EB</sub> = 5 V, f = 1 MHz		C <sub>EB</sub>		38		pF
Thermal resistance			R <sub>thja</sub>		250		K/W
COUPLER							
Coupling capacitance			C <sub>C</sub>		0.6		pF
Saturation voltage collector emitter	$I_F = 10 \text{ mA}, I_C = 3.2 \text{ mA}$	SFH640-2	V <sub>CEsat</sub>		0.25	0.4	V
	$I_F = 10 \text{ mA}, I_C = 5 \text{ mA}$	SFH640-3	V <sub>CEsat</sub>		0.25	0.4	V
Collector emitter leakage current	$V_{CE} = 200 \text{ V}, R_{BE} = 1 \text{ M}\Omega$		I <sub>CER</sub>		1	100	nA

#### Note

<sup>•</sup> Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.



CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio	$I_F = 10 \text{ mA}, V_{CE} = 10 \text{ V}$	SFH640-2	I <sub>C</sub> /I <sub>F</sub>	63		125	%
	$I_F = 1 \text{ mA}, V_{CE} = 10 \text{ V}$	SFH640-2	I <sub>C</sub> /I <sub>F</sub>	22	45		%
	$I_F = 10 \text{ mA}, V_{CE} = 10 \text{ V}$	SFH640-3	I <sub>C</sub> /I <sub>F</sub>	100		200	%
	$I_F = 1 \text{ mA}, V_{CE} = 10 \text{ V}$	SFH640-3	I <sub>C</sub> /I <sub>F</sub>	34	70		%

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$I_C = 2 \text{ mA}, R_L = 100 \Omega, V_{CC} = 10 \text{ V}$	t <sub>on</sub>		5		μs
Rise time	$I_C = 2 \text{ mA}, R_L = 100 \Omega, V_{CC} = 10 \text{ V}$	t <sub>r</sub>		2.5		μs
Turn-off time	$I_C = 2 \text{ mA}, R_L = 100 \Omega, V_{CC} = 10 \text{ V}$	t <sub>off</sub>		6		μs
Fall time	$I_C = 2 \text{ mA}, R_L = 100 \Omega, V_{CC} = 10 \text{ V}$	t <sub>f</sub>		5.5		μs

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

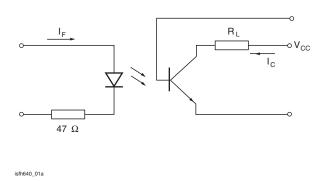


Fig. 1 - Switching Times Measurement Test Circuit and Waveform

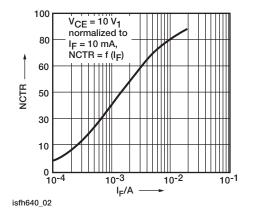


Fig. 3 - Current Transfer Ratio (typ.)

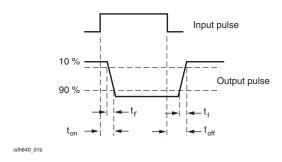


Fig. 2 - Switching Times Measurement Test Circuit and Waveform

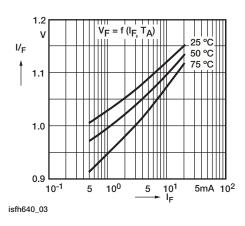


Fig. 4 - Diode Forward Voltage (typ.)

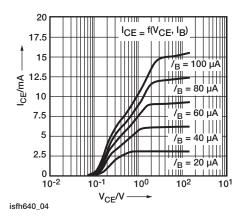


Fig. 5 - Output Characteristics (typ.)

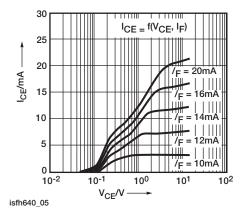


Fig. 6 - Output Characteristics (typ.)

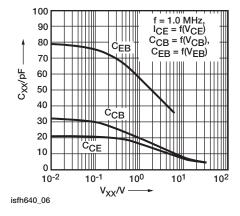


Fig. 7 - Transistor Capacitances (typ.)

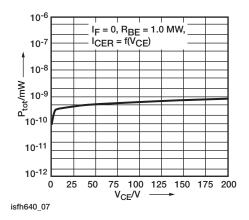


Fig. 8 - Collector-Emitter Leakage Current (typ.)

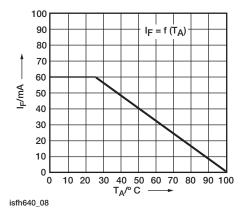


Fig. 9 - Permissible Loss Diode

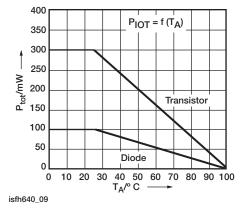
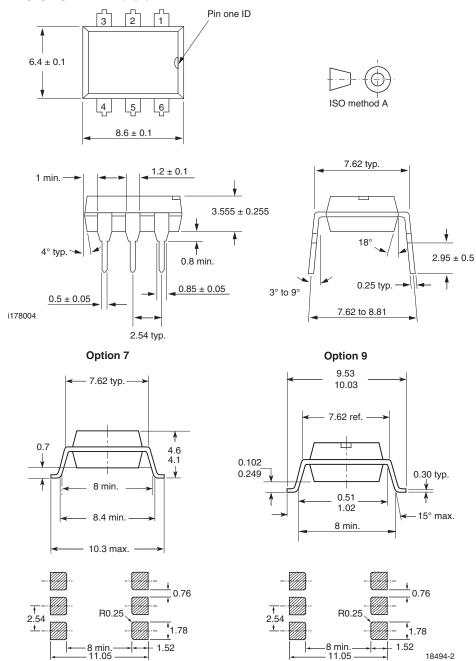


Fig. 10 - Permissible Power Dissipation

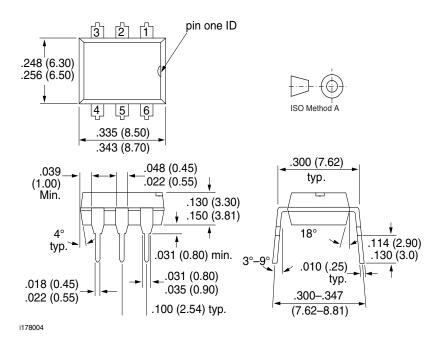
#### **PACKAGE DIMENSIONS** in millimeters



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## **Package Dimensions in Inches (mm)**





#### Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

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- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

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