

Products > ADC1413D105W0 demoboard; compliant with Lattice, Altera, Xilinx FPGA boards through specific connectors

# ADC1413D105W0 Obsolete

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IDT's ADC demonstration board is suitable for dynamic performances evaluation from low to high IF configuration. A FPGA mother board (Xilinx, Altera, and Lattice) could be connected to ease the evaluation and analysis of the ADC dynamic and enable usage of the JESD204A full features sets

## Features

- On board Data clock generator
- SMA connector for clock and analog inputs
- Single POWER supply (on board regulators)
- On board demodulator + LO synthesizer

## Product Options

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Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Buy Sample
ADC1413D105W0-DB	Obsolete	<a href="#">BD0</a>	BOARD	0	I	No	<a href="#">Availability</a>

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ADC1413D105W0 - Evaluation Board

