

## 1. General description

The CBT3245A provides eight bits of high-speed TTL-compatible bus switching. The low ON resistance of the switch allows connections to be made with minimal propagation delay.

The CBT3245A is organized as one 8-bit bus switches with one output enable ( $\overline{OE}$ ) input. When  $\overline{OE}$  is LOW, the switch is on and port A is connected to the B port. When  $\overline{OE}$  is HIGH, each switch is disabled.

## 2. Features and benefits

- 5  $\Omega$  switch connection between two ports
- TTL-compatible control input levels
- Multiple package options
- Latch-up protection exceeds 500 mA per JESD78
- ESD protection:
  - HBM JESD22-A114F exceeds 2000 V
  - MM JESD22-A115B exceeds 150 V
  - CDM JESD22-C101C exceeds 1000 V
- Specified from -40 °C to +85 °C

## 3. Ordering information

Table 1. Ordering information

Type number	Package				Version
	Temperature range	Name	Description		
CBT3245AD	-40 °C to +85 °C	SO20	plastic small outline package; 20 leads; body width 7.5 mm		SOT163-1
CBT3245ADB	-40 °C to +85 °C	SSOP20	plastic shrink small outline package; 20 leads; body width 5.3 mm		SOT339-1
CBT3245APW	-40 °C to +85 °C	TSSOP20	plastic thin shrink small outline package; 20 leads; body width 4.4 mm		SOT360-1
CBT3245ABQ	-40 °C to +85 °C	DHVQFN20	plastic dual-in-line compatible thermal enhanced very thin quad flat package; no leads; 20 terminals; body 2.5 x 4.5 x 0.85 mm		SOT764-1

## 4. Functional diagram

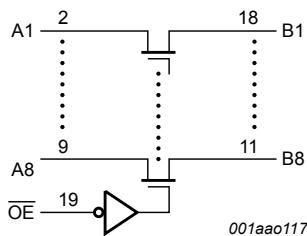


Fig. 1. Logic diagram

## 5. Pinning information

### 5.1. Pinning

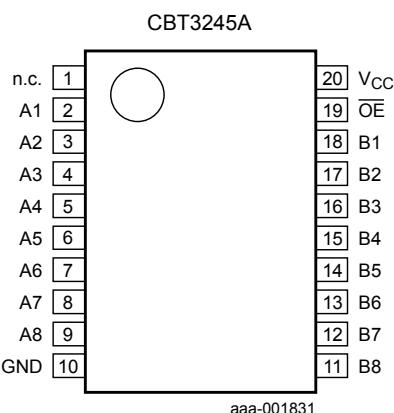
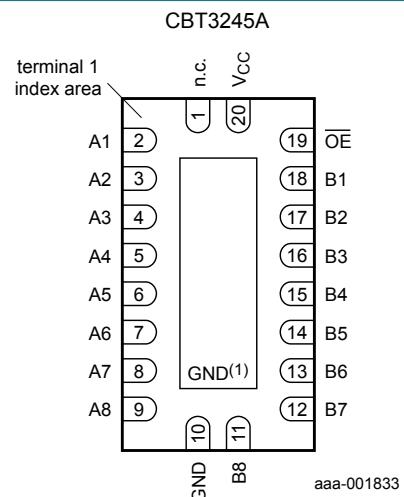


Fig. 2. Pin configuration for SOT163-1 (SO20), SOT339-1 (SSOP20) and SOT360-1 (TSSOP20)



(1) This is not a supply pin. The substrate is attached to this pad using conductive die attach material. There is no electrical or mechanical requirement to solder this pad. However, if it is soldered, the solder land should remain floating or be connected to GND.

Fig. 3. Pin configuration for SOT764-1 (DHVQFN20)

### 5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
n.c.	1	not connected
A1 to A8	2, 3, 4, 5, 6, 7, 8, 9	data input/output (A port)
GND	10	ground (0 V)
B1 to B8	18, 17, 16, 15, 14, 13, 12, 11	data input/output (B port)
OE	19	output enable input (active LOW)
V <sub>CC</sub>	20	positive supply voltage

## 6. Functional description

**Table 3. Functional description**

*H = HIGH voltage level; L = LOW voltage level; Z = high-impedance OFF-state.*

Input	Input/output
OE	An, Bn
L	An = Bn
H	Z

## 7. Limiting values

**Table 4. Limiting values**

*In accordance with the Absolute Maximum Rating System (IEC 60134).*

$T_{amb}$  = -40 °C to +85 °C, unless otherwise specified.

Symbol	Parameter	Conditions	Min	Max	Unit	
$V_{CC}$	supply voltage		-0.5	+7.0	V	
$V_I$	input voltage		[1]	-0.5	+7.0	V
$I_{OK}$	output clamping current	$V_O < 0$ V	-50	-	mA	
$V_O$	output voltage		[1]	-0.5	+7.0	V
$I_o$	output current	$V_O < 0$ V	-	$\pm 128$	mA	
$I_{IK}$	input clamping current	$V_I < 0$ V	-50	-	mA	
$T_{stg}$	storage temperature		-65	+150	°C	

[1] The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

## 8. Recommended operating conditions

**Table 5. Recommended operating conditions**

*All unused control inputs of the device must be held at  $V_{CC}$  or GND to ensure proper device operation.*

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{CC}$	supply voltage		4.0	-	5.5	V
$V_{IH}$	HIGH-level input voltage		2.0	-	-	V
$V_{IL}$	LOW-level input voltage		-	-	0.8	V
$T_{amb}$	ambient temperature	operating in free air	-40	-	+85	°C

## 9. Static characteristics

**Table 6. Static characteristics**

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	$T_{amb} = -40 \text{ }^{\circ}\text{C to } +85 \text{ }^{\circ}\text{C}$			Unit
			Min	Typ <sup>[1]</sup>	Max	
$V_{IK}$	input clamping voltage	$V_{CC} = 4.5 \text{ V}$ ; $I_I = -18 \text{ mA}$	-	-	-1.2	V
$I_I$	input leakage current	$V_{CC} = 5.5 \text{ V}$ ; $V_I = \text{GND or } 5.5 \text{ V}$	-	-	$\pm 5$	$\mu\text{A}$
$I_{CC}$	supply current	$V_{CC} = 5.5 \text{ V}$ ; $I_O = 0 \text{ mA}$ ; $V_I = V_{CC}$ or GND	-	1	3	$\mu\text{A}$
$\Delta I_{CC}$	additional supply current	per input pin; $V_{CC} = 5.5 \text{ V}$ ; one input at [2] 3.4 V, other inputs at $V_{CC}$ or GND	-	-	3.5	mA
$C_I$	input capacitance	control pins; $V_I = 3 \text{ V}$ or 0 V	-	3.2	-	pF
$C_{IO(off)}$	off-state input/output capacitance	port off; $V_I = 3 \text{ V}$ or 0 V; $\bar{OE} = V_{CC}$	-	6.6	-	pF
$R_{ON}$	ON resistance	$V_{CC} = 4.5 \text{ V}$ ; $V_I = 0 \text{ V}$ ; $I_I = 64 \text{ mA}$ [3]	-	5	7	$\Omega$
		$V_{CC} = 4.5 \text{ V}$ ; $V_I = 0 \text{ V}$ ; $I_I = 30 \text{ mA}$ [3]	-	5	7	$\Omega$
		$V_{CC} = 4.5 \text{ V}$ ; $V_I = 2.4 \text{ V}$ ; $I_I = -15 \text{ mA}$ [3]	-	10	15	$\Omega$

[1] All typical values are measured at  $V_{CC} = 5 \text{ V}$  and  $T_{amb} = 25 \text{ }^{\circ}\text{C}$ .

[2] This is the increase in supply current for each input that is at the specified TTL voltage level rather than  $V_{CC}$  or GND.

[3] Measured by the voltage drop between the An and the Bn terminals at the indicated current through the switch. ON resistance is determined by the lowest voltage of the two (An or Bn) terminals.

## 10. Dynamic characteristics

**Table 7. Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V). For test circuit see Fig. 6.

Symbol	Parameter	Conditions	$T_{amb} = -40 \text{ }^{\circ}\text{C to } +85 \text{ }^{\circ}\text{C}$		Unit
			Min	Max	
$t_{pd}$	propagation delay	An, Bn to Bn, An; see Fig. 4 [1][2]			
		$V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	-	0.25	ns
$t_{en}$	enable time	$\bar{OE}$ to An or Bn; see Fig. 5 [2]			
		$V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	1.0	5.9	ns
$t_{dis}$	disable time	$\bar{OE}$ to An or Bn; see Fig. 5 [2]			
		$V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	1.0	6.0	ns

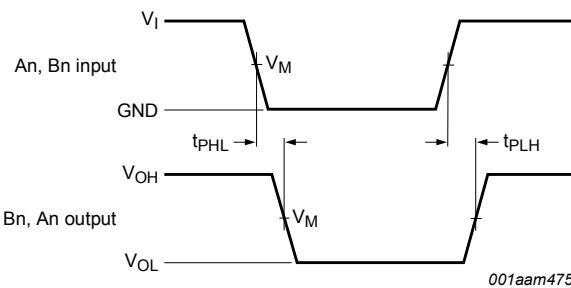
[1] The propagation delay is the calculated RC time constant of the typical ON resistance of the switch and the specified load capacitance, when driven by an ideal voltage source (zero output impedance).

[2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .

$t_{en}$  is the same as  $t_{PZL}$  and  $t_{PZH}$ .

$t_{dis}$  is the same as  $t_{PLZ}$  and  $t_{PHZ}$ .

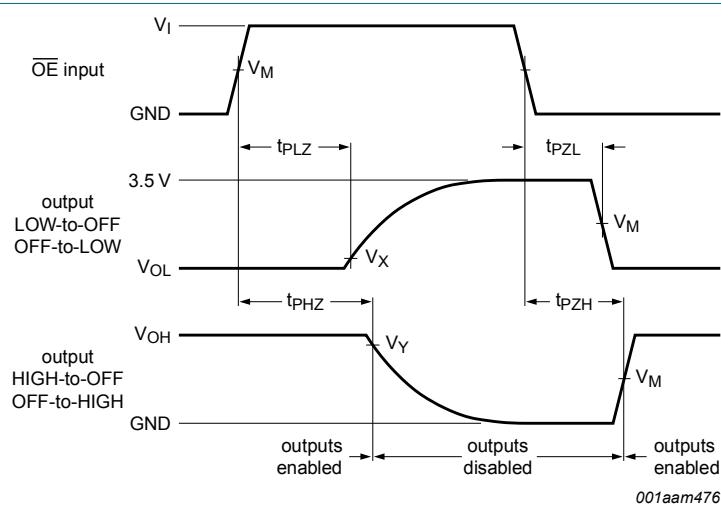
## 10.1. Waveforms and test circuit



Measurement points are given in [Table 8](#).

Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

**Fig. 4. The data input (An, Bn) to output (Bn, An) propagation delay times**



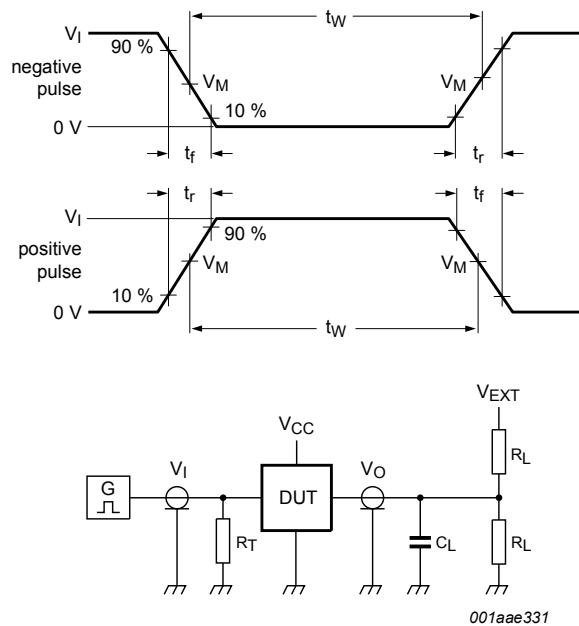
Measurement points are given in [Table 8](#).

Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

**Fig. 5. Enable and disable times**

**Table 8. Measurement points**

Supply voltage	Input	Output			
$V_{CC}$	$V_I$	$V_M$	$V_M$	$V_X$	$V_Y$
$V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	GND to 3.0 V	1.5 V	1.5 V	$V_{OL} + 0.3 \text{ V}$	$V_{OH} - 0.3 \text{ V}$



001aae331

Test data is given in [Table 9](#).

All input pulses are supplied by generators having the following characteristics: PRR  $\leq$  10 MHz;  $Z_o = 50 \Omega$ .

The outputs are measured one at a time with one transition per measurement.

Definitions for test circuit:

$R_L$  = Load resistance.

$C_L$  = Load capacitance including jig and probe capacitance.

$R_T$  = Termination resistance should be equal to output impedance  $Z_o$  of the pulse generator.

$V_{EXT}$  = External voltage for measuring switching times.

**Fig. 6. Test circuit for measuring switching times**

**Table 9. Test data**

Supply voltage	Input		Load		$V_{EXT}$		
	$V_I$	$t_r, t_f$	$C_L$	$R_L$	$t_{PLH}, t_{PHL}$	$t_{PLZ}, t_{PZL}$	$t_{PHZ}, t_{PZH}$
$V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	GND to 3.0 V	$\leq 2.5 \text{ ns}$	50 pF	500 $\Omega$	open	7.0 V	open

## 11. Package outline

SO20: plastic small outline package; 20 leads; body width 7.5 mm

SOT163-1

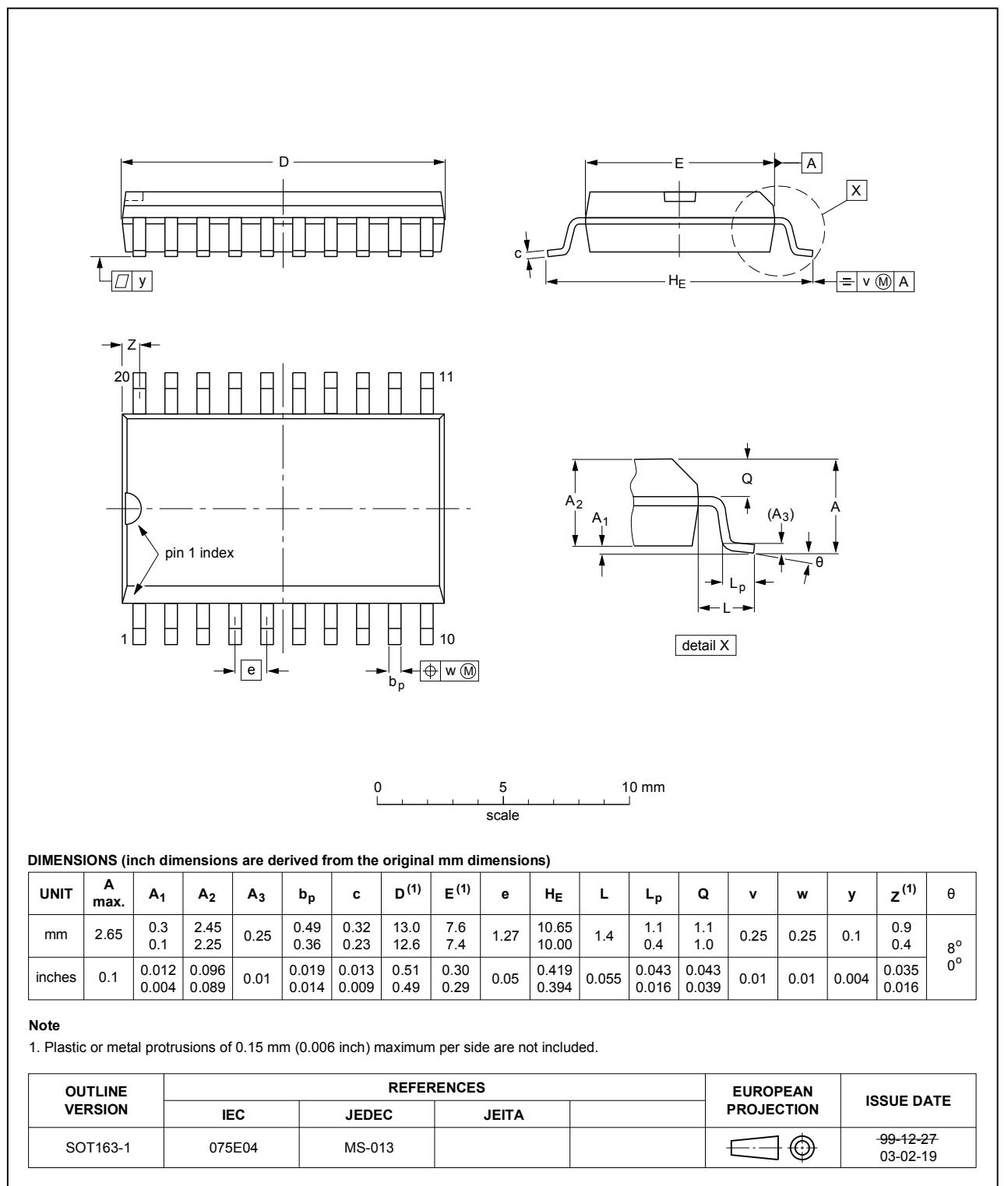
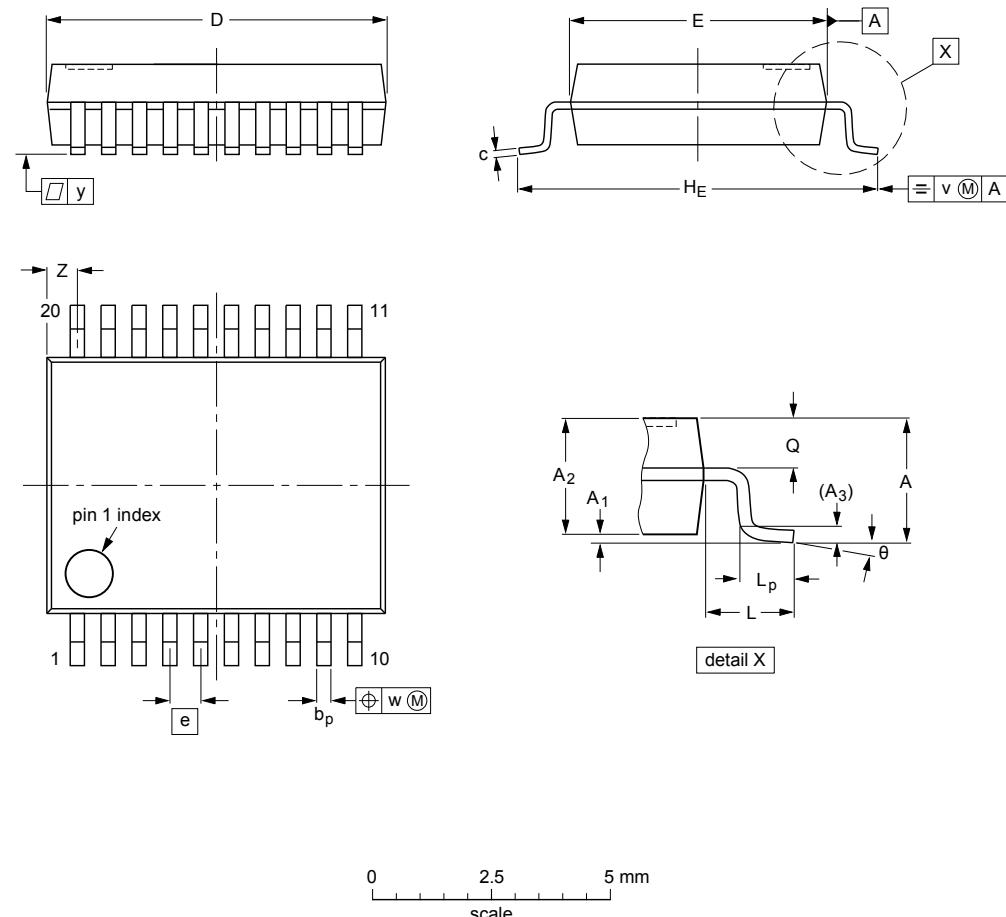


Fig. 7. Package outline SOT163-1 (SO20)

SSOP20: plastic shrink small outline package; 20 leads; body width 5.3 mm

SOT339-1



## DIMENSIONS (mm are the original dimensions)

UNIT	A max.	A <sub>1</sub>	A <sub>2</sub>	A <sub>3</sub>	b <sub>p</sub>	c	D <sup>(1)</sup>	E <sup>(1)</sup>	e	H <sub>E</sub>	L	L <sub>p</sub>	Q	v	w	y	Z <sup>(1)</sup>	θ
mm	2 0.05	0.21 1.65	1.80	0.25	0.38 0.25	0.20 0.09	7.4 7.0	5.4 5.2	0.65	7.9 7.6	1.25	1.03 0.63	0.9 0.7	0.2	0.13	0.1	0.9 0.5	8° 0°

## Note

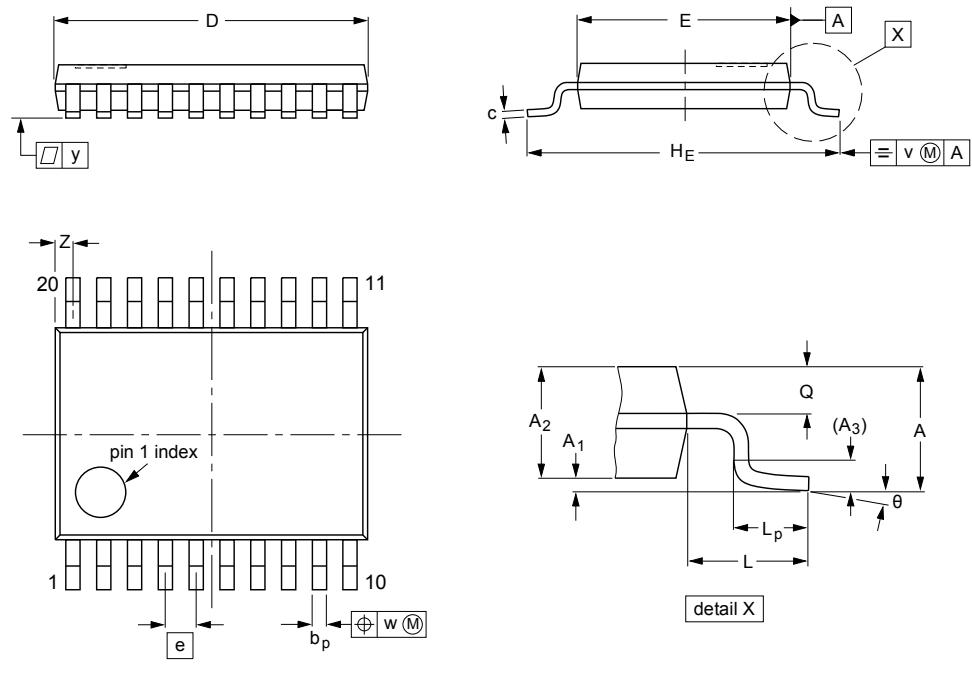
1. Plastic or metal protrusions of 0.2 mm maximum per side are not included.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT339-1		MO-150				99-12-27 03-02-19

Fig. 8. Package outline SOT339-1 (SSOP20)

TSSOP20: plastic thin shrink small outline package; 20 leads; body width 4.4 mm

SOT360-1



## DIMENSIONS (mm are the original dimensions)

UNIT	A max.	A <sub>1</sub>	A <sub>2</sub>	A <sub>3</sub>	b <sub>p</sub>	c	D <sup>(1)</sup>	E <sup>(2)</sup>	e	H <sub>E</sub>	L	L <sub>p</sub>	Q	v	w	y	z <sup>(1)</sup>	θ
mm	1.1 0.05	0.15 0.80	0.95 0.25	0.25 0.19	0.30 0.19	0.2 0.1	6.6 6.4	4.5 4.3	0.65	6.6 6.2	1	0.75 0.50	0.4 0.3	0.2	0.13	0.1	0.5 0.2	8° 0°

## Notes

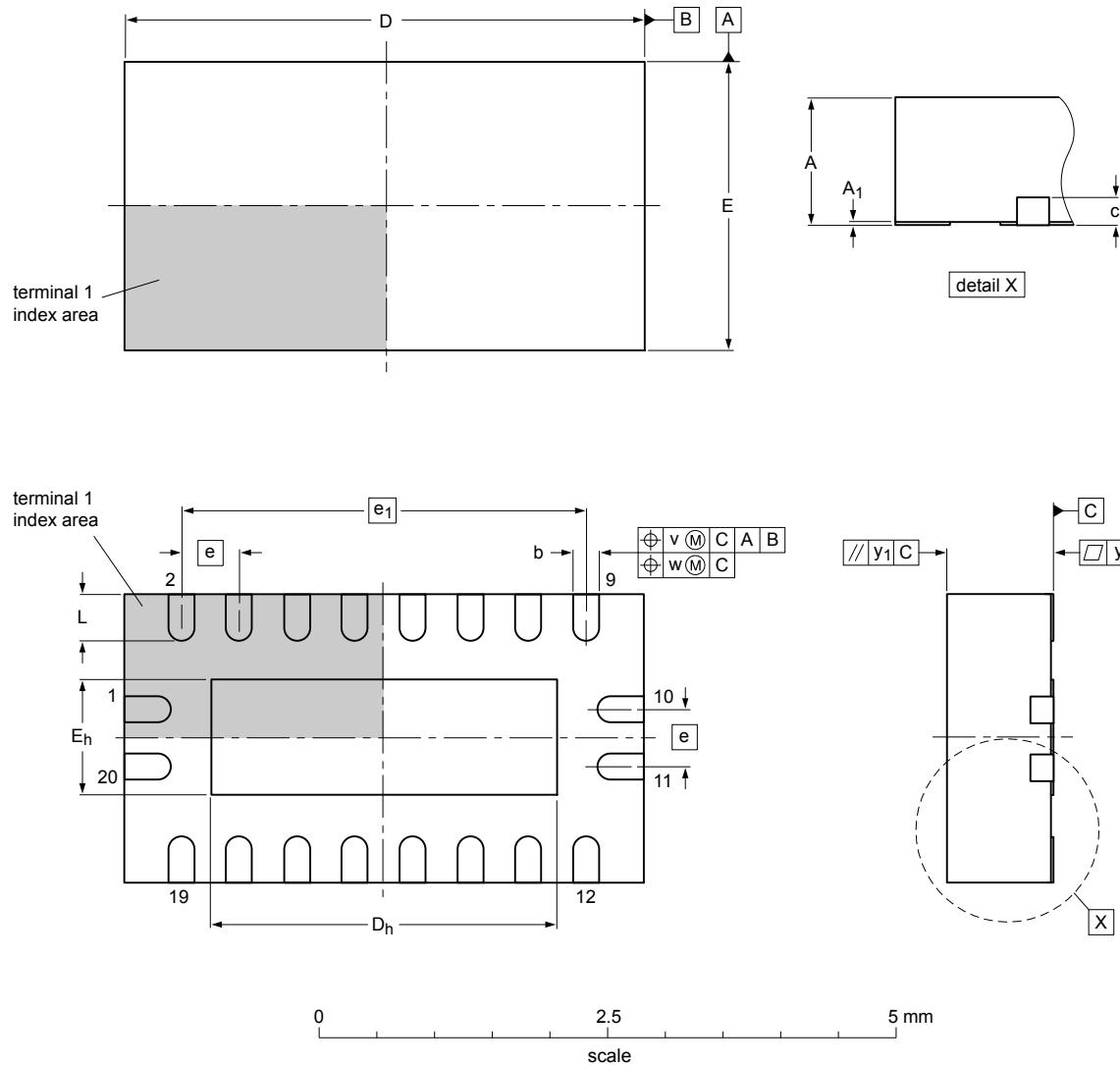
1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT360-1		MO-153				-99-12-27 03-02-19

Fig. 9. Package outline SOT360-1 (TSSOP20)

DHVQFN20: plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads;  
20 terminals; body 2.5 x 4.5 x 0.85 mm

SOT764-1



Dimensions (mm are the original dimensions)

Unit	A <sup>(1)</sup>	A <sub>1</sub>	b	c	D <sup>(1)</sup>	D <sub>h</sub>	E <sup>(1)</sup>	E <sub>h</sub>	e	e <sub>1</sub>	L	v	w	y	y <sub>1</sub>
mm	max	1.00	0.05	0.30	4.6	3.15	2.6	1.15	0.5						
mm	nom	0.90	0.02	0.25	0.2	4.5	3.00	2.5	1.00	0.5	3.5	0.4	0.1	0.05	0.1
mm	min	0.80	0.00	0.18	4.4	2.85	2.4	0.85	0.3						

Note

1. Plastic or metal protrusions of 0.075 mm maximum per side are not included.

sot764-1\_po

Outline version	References			European projection	Issue date
	IEC	JEDEC	JEITA		
SOT764-1	---	MO-241	---		03-01-27 14-12-12

Fig. 10. Package outline SOT764-1 (DHVQFN20)

## 12. Abbreviations

**Table 10. Abbreviations**

Acronym	Description
CDM	Charged Device Model
ESD	ElectroStatic Discharge
DUT	Device Under Test
HBM	Human Body Model
MM	Machine Model
PRR	Pulse Rate Repetition
TTL	Transistor-Transistor Logic

## 13. Revision history

**Table 11. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
CBT3245A v.4	20190430	Product data sheet	-	CBT3245A v.3
Modifications:			<ul style="list-style-type: none"> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Type number CBT3245ADS (SOT724-1/SSOP20) removed.</li> <li>Package outline drawing SOT764-1 updated.</li> </ul>	
CBT3245A v.3	20120105	Product data sheet	-	CBT3245A v.2
Modifications:			<ul style="list-style-type: none"> <li>The format of this document has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Marking code removed from order information section.</li> <li>Description of <math>C_L</math> and <math>C_{I/O}</math> corrected (errata).</li> </ul>	
CBT3245A v.2	20020627	Product data sheet	-	CBT3245A v.1
CBT3245A v.1	20020218	Product data sheet	-	-

## 14. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

### Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

**Short data sheet** — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

### Disclaimers

**Limited warranty and liability** — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

**Right to make changes** — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use** — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal

injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

**Limiting values** — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

**Terms and conditions of commercial sale** — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nexperia.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Non-automotive qualified products** — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

### Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

## Contents

<b>1. General description.....</b>	<b>1</b>
<b>2. Features and benefits.....</b>	<b>1</b>
<b>3. Ordering information.....</b>	<b>1</b>
<b>4. Functional diagram.....</b>	<b>2</b>
<b>5. Pinning information.....</b>	<b>2</b>
5.1. Pinning.....	2
5.2. Pin description.....	2
<b>6. Functional description.....</b>	<b>3</b>
<b>7. Limiting values.....</b>	<b>3</b>
<b>8. Recommended operating conditions.....</b>	<b>3</b>
<b>9. Static characteristics.....</b>	<b>4</b>
<b>10. Dynamic characteristics.....</b>	<b>4</b>
10.1. Waveforms and test circuit.....	5
<b>11. Package outline.....</b>	<b>7</b>
<b>12. Abbreviations.....</b>	<b>11</b>
<b>13. Revision history.....</b>	<b>11</b>
<b>14. Legal information.....</b>	<b>12</b>

---

© Nexperia B.V. 2019. All rights reserved

For more information, please visit: <http://www.nexperia.com>

For sales office addresses, please send an email to: [salesaddresses@nexperia.com](mailto:salesaddresses@nexperia.com)

**Date of release: 30 April 2019**